

TO: T10 Membership, ADI Working Group
 FROM: Rod Wideman, Quantum; rod.wideman@quantum.com
 DATE: April 17th, 2007
 SUBJECT: ADI ADC-2 Letter Ballot Comment QTM-159 (document T10/07-191r1)

Rev1 – Changes per ADC-2 conference call 4/18/07.
 Rev0 – Initial draft.

Related Documents

ADC-2 Revision 7
 ADC-2 Revision 7c
 T10/06-475r2

Introduction

This proposal is intended to address ADC-2 letter ballot comment QTM-159 (reproduced below from T10/06-475 for convenience).

QTM-159	77	last paragraph	T	Do we need "A MUE bit set to zero may not allow a DT device to prepare"?
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Discussion

The behavior is not currently specified when the microcode update enable (MUE) bit is set to zero.

Proposed Changes to ADC-2

Changes to 6.2.2.3.2 (First paragraph after table 51 in rev07c):

Proposed new text shown in [blue underline](#):

A microcode update enable (MUE) bit set to one allows the DT device to prepare to accept a medium containing a microcode image. A description of this preparation is outside the scope of this standard. [The behavior when the MUE bit is set to zero is vendor-specific](#). The MUE bit shall be set to zero by the DT device after the microcode update process completes or is aborted.