

TO: T10 Membership, ADI Working Group
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 DATE: April 17th, 2007
 SUBJECT: ADI ADC-2 Letter Ballot Comment QTM-157 (document T10/07-190r0)

Rev0 – Initial draft.

Related Documents

ADC-2 Revision 7
 ADC-2 Revision 7c
 T10/06-475r2
 SSC-2

Introduction

This proposal is intended to address ADC-2 letter ballot comment QTM-157 (reproduced below from T10/06-475 for convenience).

QTM-157	77	fifth paragraph	T	Do we need "A wp bit set to zero shall not write protect the medium."? Do we need to specify then that it has no bearing on media that are physically write-protected?
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Discussion

The write protect (WP) bit description is insufficient to avoid ambiguous behavior. The proposed resolution is to associate its behavior with software write protection as specified in SSC-2, so as to not duplicate or alter an existing understood meaning.

Proposed Changes to ADC-2

Changes to 6.2.2.3.2 (Sixth paragraph after table 51 in rev07c):

Original text:

A write protect (WP) bit set to one shall write protect the medium. The WP bit shall be set to zero by the DT device each time a medium is unloaded.

Proposed text:

[A write protect \(WP\) bit set to one shall enable software write protection \(see SSC-2\). A \(WP\) bit set to zero may disable software write protection, dependent on other write protection mechanisms. The WP bit shall be set to zero by the DT device each time a medium is unloaded.](#)