

To: T10 Technical Committee  
 From: Rob Elliott, HP (elliott@hp.com)  
 Date: 3 April 2007  
 Subject: 07-159r0 SPC-4 Add QTSS and QUAS bits to REPORT SUPPORTED TMF

**Revision history**

Revision 0 (3 April 2007) First revision

**Related documents**

spc4r09 - SCSI Primary Commands - 4 (SPC-4) revision 9  
 sam4r10 - SCSI Architecture Model - 4 (SAM-4) revision 10 - incorporates 07-066r1 and 07-067r1  
 07-066r1 - SAM-4 SAS-2 QUERY TASK SET task management function (Rob Elliott, HP)  
 07-067r1 - SAM-4 SAS-2 QUERY UNIT ATTENTION task management function (Rob Elliott, HP)

**Overview**

Bits need to be added to the REPORT SUPPORTED TASK MANAGEMENT FUNCTIONS parameter data (in SPC-4) indicating support for the new task management functions recently added to SAM-4: QUERY UNIT ATTENTION and QUERY TASK SET.

**Suggested changes to SPC-4**

**6.25 REPORT SUPPORTED TASK MANAGEMENT FUNCTIONS command**

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The format of the parameter data returned by the REPORT TASK MANAGEMENT FUNCTIONS command is shown in table 174.

**Table 174 — REPORT SUPPORTED TASK MANAGEMENT FUNCTIONS parameter data**

Byte\Bit	7	6	5	4	3	2	1	0
0	ATS	ATSS	CACAS	CTSS	LURS	QTS	TRS	WAKES
1	Reserved					<del>Rsvd</del> QUAS	<del>Rsvd</del> QTSS	ITNRS
2	Reserved							
3	Reserved							

An ABORT TASK supported (ATS) bit set to one indicates the ABORT TASK task management function (see SAM-4) is supported by the logical unit. An ATS bit set to zero indicates the ABORT TASK task management function is not supported.

An ABORT TASK SET supported (ATSS) bit set to one indicates the ABORT TASK SET task management function (see SAM-4) is supported by the logical unit. An ATSS bit set to zero indicates the ABORT TASK SET task management function is not supported.

A CLEAR ACA supported (CACAS) bit set to one indicates the CLEAR ACA task management function (see SAM-4) is supported by the logical unit. An CACAS bit set to zero indicates the CLEAR ACA task management function is not supported.

A CLEAR TASK SET supported (CTSS) bit set to one indicates the CLEAR TASK SET task management function (see SAM-4) is supported by the logical unit. An CTSS bit set to zero indicates the CLEAR TASK SET task management function is not supported.

A LOGICAL UNIT RESET supported (LURS) bit set to one indicates the LOGICAL UNIT RESET task management function (see SAM-4) is supported by the logical unit. An LURS bit set to zero indicates the LOGICAL UNIT RESET task management function is not supported.

A QUERY TASK supported (QTS) bit set to one indicates the QUERY TASK task management function (see SAM-4) is supported by the logical unit. An QTS bit set to zero indicates the QUERY TASK task management function is not supported.

A TARGET RESET supported (TRS) bit set to one indicates the TARGET RESET task management function (see SAM-2) is supported by the logical unit. An TRS bit set to zero indicates the TARGET RESET task management function is not supported.

A WAKEUP supported (WAKES) bit set to one indicates the WAKEUP task management function (see SAM-2) is supported by the logical unit. An WAKES bit set to zero indicates the WAKEUP task management function is not supported.

[A QUERY TASK SET supported \(QTSS\) bit set to one indicates the I\\_T NEXUS RESET task management function \(see SAM-4\) is supported by the logical unit. A QTSS bit set to zero indicates the QUERY TASK SET task management function is not supported.](#)

[A QUERY UNIT ATTENTION supported \(QUAS\) bit set to one indicates the I\\_T NEXUS RESET task management function \(see SAM-4\) is supported by the logical unit. A QUAS bit set to zero indicates the QUERY TASK SET task management function is not supported.](#)

An I\_T NEXUS RESET supported (ITNRS) bit set to one indicates the I\_T NEXUS RESET task management function (see SAM-4) is supported by the logical unit. An ITNRS bit set to zero indicates the I\_T NEXUS RESET task management function is not supported.