Proposed 6G SAS Phy Specs for EMI Reduction

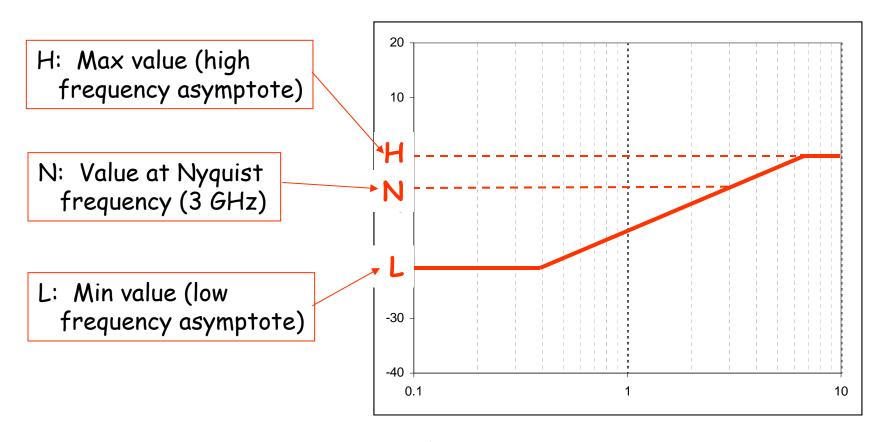
Mike Jenkins LSI Logic

Proposed Modified or Additional Specifications to Control EMI

- Differential to Common Mode Conversion (SCD11 & SCD22)
 - Constrains CM voltage generated from differential signal
 - Similar to "Impedance Balance" in SATA spec
- TX Common Mode Voltage
 - Convert from single RMS or peak voltage to a limit across frequency spectrum

Proposed Format of Specs

Measured Value < max [L, min [H, N + 13.3 log10(F/3GHz)]] 100 MHz < F < 6 GHz



20 Dec 2006

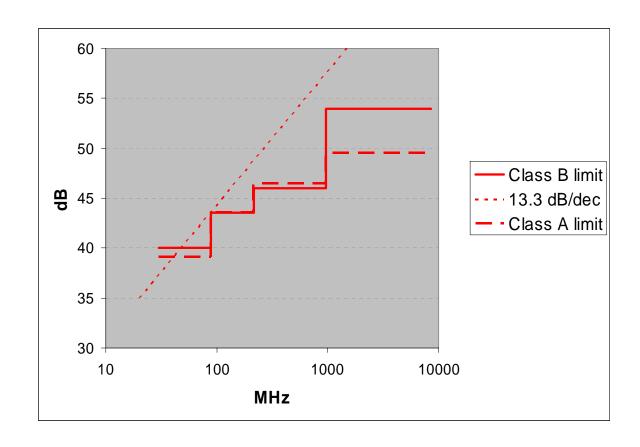
T10/07-007r1

FCC Radiated Emissions Limits

(relative to 1uV/meter)

Mechanism for converting common mode noise into EMI is unknown, but EMI is assumed to be proportional to the noise that causes it.

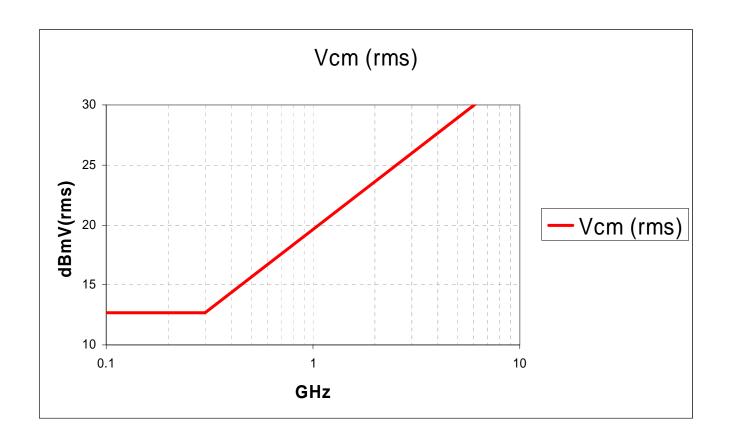
Hence, limit curve for each parameter (in dB) should reasonably track these Radiated Emission limits.



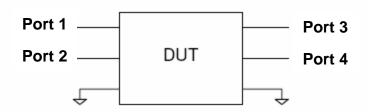
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Proposed TX CM Voltage Limit

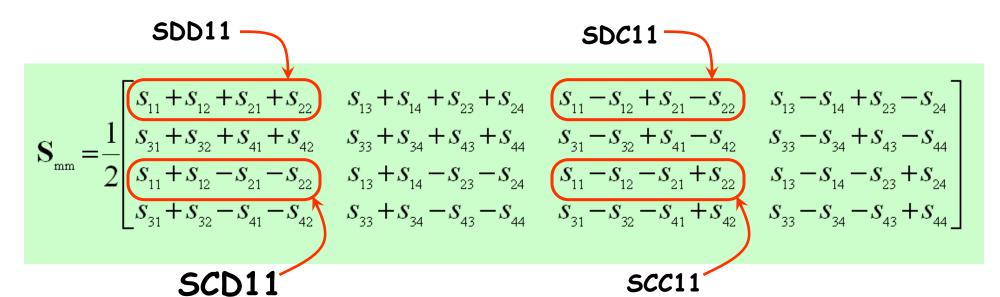
(L = 12.7 dBmV(rms), N = 26 dBmV(rms))



S-parameter Single-ended to Mixed Mode Conversion



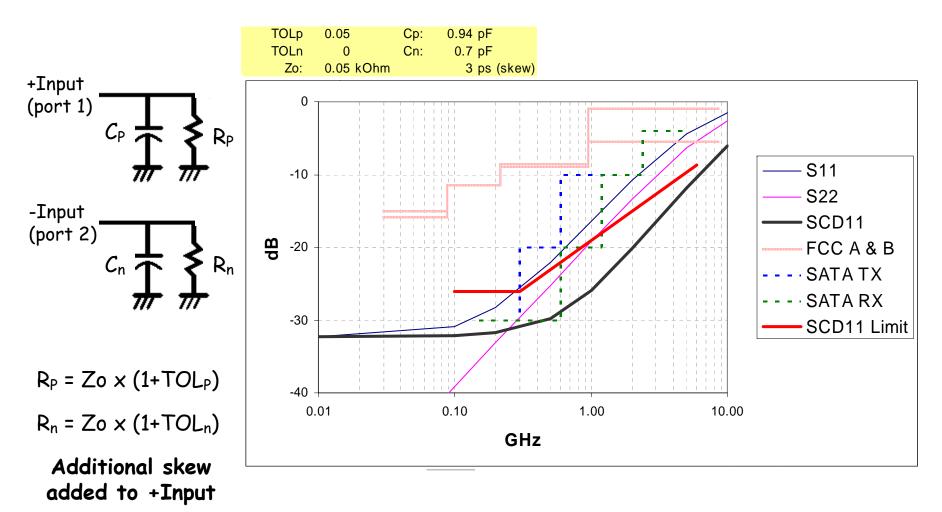
Note: $S_{12} = S_{21}$ (reciprocal), so $SDC11 = SCD11 = (S_{11}-S_{22})/2$



For derivation of conversion to mixed mode parameters, see (for example): http://www.simtech.a-star.edu.sq/Research/TechnicalReports/TR04JT03.pdf

Proposed SCD11, SCD22 Limit

(L = -26dB, N = -12.7dB)



A Note about Impedance Scaling in Mixed Mode Conversion

For scattering parameters, the incident & reflected 'power waves' are defined as $V^+/sqrt(Z_{ref}^+)$ and $V^-/sqrt(Z_{ref}^-)$, respectively, where Z_{ref}^+ is the reference impedance of the incident wave and Z_{ref}^- is the reference impedance of the reflected wave.

In mixed mode parameters, these impedances can be different. Hence, a differential input voltage of 1200mVpp encountering the proposed limit SCD11 of -12.7dB = 0.232 at the Nyquist frequency results in a reflected common mode voltage of:

 $V_{cm} = 1200 \text{mVpp} \times 0.232 \times \text{sqrt}[25\Omega/100\Omega] = 69.6 \text{mVpp}.$

This limit compares reasonably with the proposed +26dBmV(rms) common mode TX voltage limit, equivalent to 20mV(rms) = 56.6mVpp.

ref: http://en.wikipedia.org/wiki/S-parameters