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Mr. John Lohmayer
T10 Chairman
LSI Logic Corporation
4420 Arrows West Drive
Colorado Spring, CO 80907

Mr. Ralph O. Weber
OSD Technical Editor
ENDL Texas
Suite 102#178
18484 Preston Rd.
Dallas, TX 75252

Subject: Invitation for Coordination on OSD based NAND Flash Software Interface
Standardization

Dear colleagues,

The NAND I/F Working Group has been established by the MIPI Alliance in the end of 2005 with a charter to investigate the subject of **SW interfaces standardization for a better and faster integration of NAND Flash products into embedded HW and SW platforms**. The group is composed of leading NAND flash industry players and in particular, some of the leading NAND device manufacturers.

The MIPI NAND I/F WG is working on the high-level functional (SW) interface between the OS/platform and the flash device. Within this framework, MIPI initiated interaction with OS vendors and embedded computing platform vendors to investigate the need and possibility of additional/alternative abstractions, other than block device abstraction schemes (e.g. object based, file based etc.). These high-level functional interface abstractions may also have significant potential impact on physical transport layer of the interface for the attachment of NAND flash storage devices to host systems.

The T10 OSD (Object-based Storage Device) draft specification is one of the above mentioned alternative abstractions considered by us. We think that it would be mutually valuable to establish formal liaison between the two organizations. We could contribute to further refinement of the OSD draft and T10 could provide valuable input for the MIPI NAND I/F WG.

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Please indicate if you are interested in investigating the possibility of establishing formal contact between the two groups.

Your prompt response is highly valued and appreciated,
With best regards

Avraham Shimor
Standardization Coordinator
M-systems Flash Disk Pioneers Ltd.
(Member of both T10 and the MIPI NAND I/F WG)

acting for the MIPI NAND I/F WG