

To: T10 Technical Committee
 From: Rob Elliott, HP (elliott@hp.com)
 Date: 12 March 2005
 Subject: 05-073r1 SPC-3 Report target port group transitioning support

Revision history

Revision 0 (15 February 2005) First revision
 Revision 1 (12 March 2005) Incorporated comments from March CAP WG - moved to byte 1 bit 7

Related documents

03-343r1 SPC-3 Report supported asymmetric access states (incorporated in spc3r16)(Rob Elliott, HP)
 spc3r21d - SCSI Primary Commands - 3 revision 21d (Ralph Weber, ENDL)

Overview

03-343 added bits to the REPORT TARGET PORT GROUPS parameter data indicating which asymmetric access states are supported: active optimized, active non-optimized, standby, and unavailable.

This proposal requests one more bit indicating that the transitioning "state" (a value of Fh) can also be returned.

Suggested changes to SPC-3 (as a late letter ballot comment)

6.25 REPORT TARGET PORT GROUPS command

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There shall be one target port group descriptor (see table 164) for each target port group.

Table 164 — Target port group descriptor parameter data

Byte\Bit	7	6	5	4	3	2	1	0
0	PREF	Reserved			ASYMMETRIC ACCESS STATE			
1	T_SUP	Reserved			U_SUP	S_SUP	AN_SUP	AO_SUP
2	TARGET PORT GROUP							
3	Reserved							
4	STATUS CODE							
5	Vendor unique							
6	TARGET PORT COUNT							
7	Target port descriptor(s)							
8	First target port descriptor (see table 167)							
	...							
n	First target port descriptor (see table 167)							

A preferred target port (PREF) bit set to one indicates that the target port group is a preferred target port group for accessing the addressed logical unit (see 5.8.2.6). A PREF bit set to zero indicates the target port group is not a preferred target port group.

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If any of the T_SUP, U_SUP bit, S_SUP bit, AN_SUP bit, or AO_SUP bit are set to one, then the T_SUP, U_SUP bit, S_SUP bit, AN_SUP bit, and AO_SUP bit are as defined in this standard. If the T_SUP, U_SUP bit, S_SUP bit, AN_SUP bit, and AO_SUP bit are all set to zero, then which asymmetric access states are supported is vendor specific.

A transitioning supported (T_SUP) bit set to one indicates that the device server supports returning the ASYMMETRIC ACCESS STATE field set to Fh (i.e., transitioning between states). A T_SUP bit set to zero indicates that the device server does not return an ASYMMETRIC ACCESS STATE field set to Fh.