

Paul von Stamwitz	AMCC
Kevin Marks	Dell
Barry Olawsky	HP
Bill Bissonette	Intel
George Penokie	IBM
Alvin Cox	Seagate
Kalev Sepp	Tektronics
Don Schulte	Vitesse

8 people present.

Agenda:

1. SAS STP and SATA signal level simulation. (Bill Bissonette)

Simulation of SATA worst-case amplitude with .55UI jitter discussed. Concerns about 3Gbps SATA devices in SAS backplane environment may require a second TCTF to limit backplane loss for such applications. Jitter eye closure will be looked at to determine if the simulation provides a true model of the receiver performance requirement.

2. TCTF editorial update. (Barry Olawsky)

Barry Olawsky plans to have a draft proposal for the next call.

3. Definitions and acronym update proposal (Bill Ham)

Alvin to post proposal by 10/4. Discussion on next call.

4. Jitter measurement similar to FC-MJSQ (Bill Ham)

Alvin to post proposal by 10/4. Discussion on next call.

Conference call schedule:

Next call:

Date: Thursday, October 14, 2004

Time: 10:00 am, Central Daylight Time (GMT -05:00, Chicago)

All Participants should use the following information to reach the conference call:

Toll Free Dial in Number: (866) 279-4742

International Access/Caller Paid Dial In Number: (309) 229-0118

PARTICIPANT CODE: 3243413

WEBEX information:

url: seagate.webex.com (no www)

Topic: SAS PHY WG

Date: Thursday, October 14, 2004

Time: 10:00 am, Central Daylight Time (GMT -05:00, Chicago)

Meeting number: 825549498

Meeting password: section5