

Paul von Stamwitz (AMCC)
Jim Lott (Dallas Semiconductor)
Kevin Marks (Dell)
Barry Olawsky (HP)
George Penokie (IBM)
Mark Seidel (Intel)
Bill Lye (PMC-Sierra)
Alvin Cox (Seagate)
David Springberg (Vitesse)
Bill Gintz (Zues)

10 people present.

Discussion:

1. 03-240r4 SAS-1.1 Internal wide connector and cable Rob Elliott

Rob not on call.

2. 04-195r2 SAS-1.1 Internal Wide Connector/Cable Electrical Requirements Barry Olawsky

Reviewed note change concerning TDR rise time. Still need work in this area. Suggested that the table entry description remain the same and that the note cover filtering and loss considerations per device being tested. Barry will review SFF-8410 and MJSQ concerning note text. Bill Gintz will look for other industry references.

70ps rise time proposed to be 70ps max.

Reviewed updated cable characteristic charts. Cable does have greater loss than the compliance channel at low frequencies. This area needs to be dealt with, but loss is considered low enough that it does not impact performance. Barry will discuss with Mike Jenkins.

3. Impact to SAS 1.1 PHY to include SATA at 3 Gbps. Andrew Cable

Andrew not on call.

4. New items

None.

5. Schedule

Next call:

Date: Thursday, August 12, 2004

Time: 10:00 am, Central Daylight Time (GMT -05:00, Chicago)

All Participants should use the following information to reach the conference call:

Toll Free Dial in Number: (866) 279-4742

International Access/Caller Paid Dial In Number: (309) 229-0118

PARTICIPANT CODE: 3243413

WEBEX information:

url: seagate.webex.com (no www)

Topic: SAS PHY WG

Date: Thursday, August 12, 2004

Time: 10:00 am, Central Daylight Time (GMT -05:00, Chicago)

Meeting number: 825549498

Meeting password: section5