

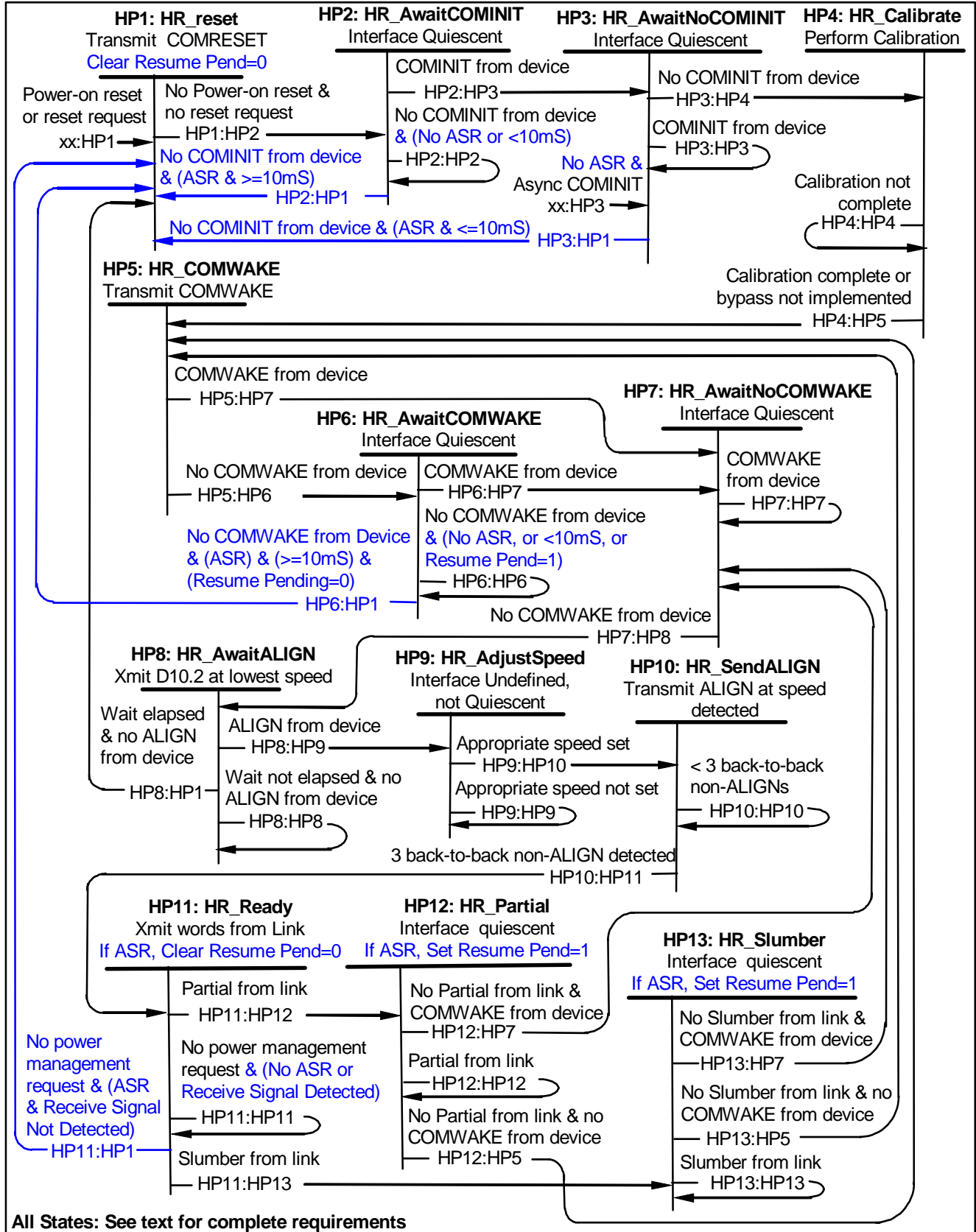
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This document includes State Diagrams derived from the Serial ATA II Specification: Extensions to Serial ATA 1.0, T10 document number T10/03-264r1.

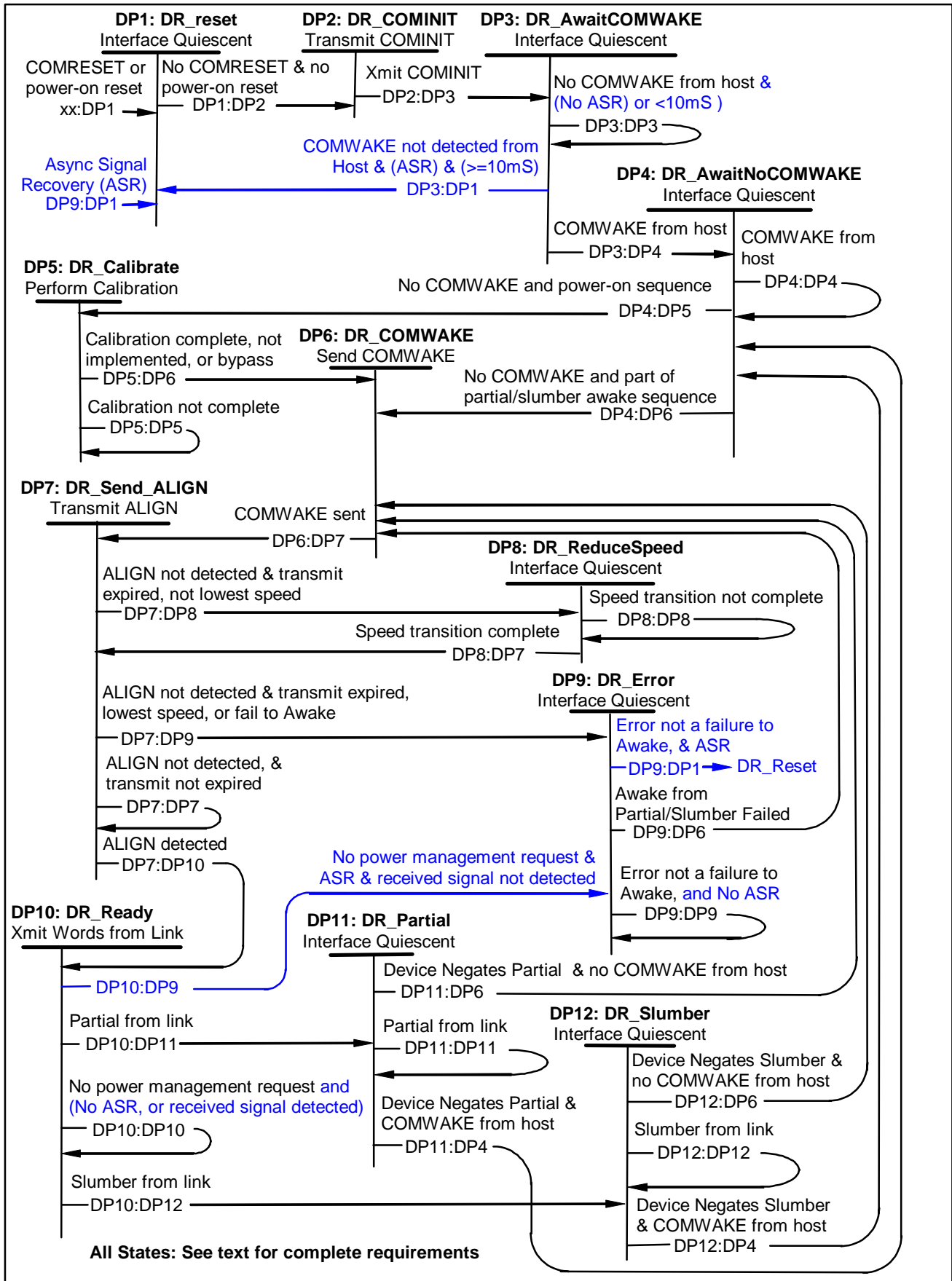
The diagrams are based on the T13 ATA-7 style of drawing state diagrams. Since the SATA II document (03-264r1) does not provide state machine diagrams in the traditional T10 or T13 formats, it is difficult to see the changes from SATA 1.0 to SATA II. These diagrams provide a simple means to compare the SATA II extensions to the SATA 1.0 document. The differences in SATA 1.0 state machines (Host PHY Initialization, Device Phy Initialization, and Device Idle) are shown in BLUE typeface. The new state machines are also included for FPDMA Command Queuing.

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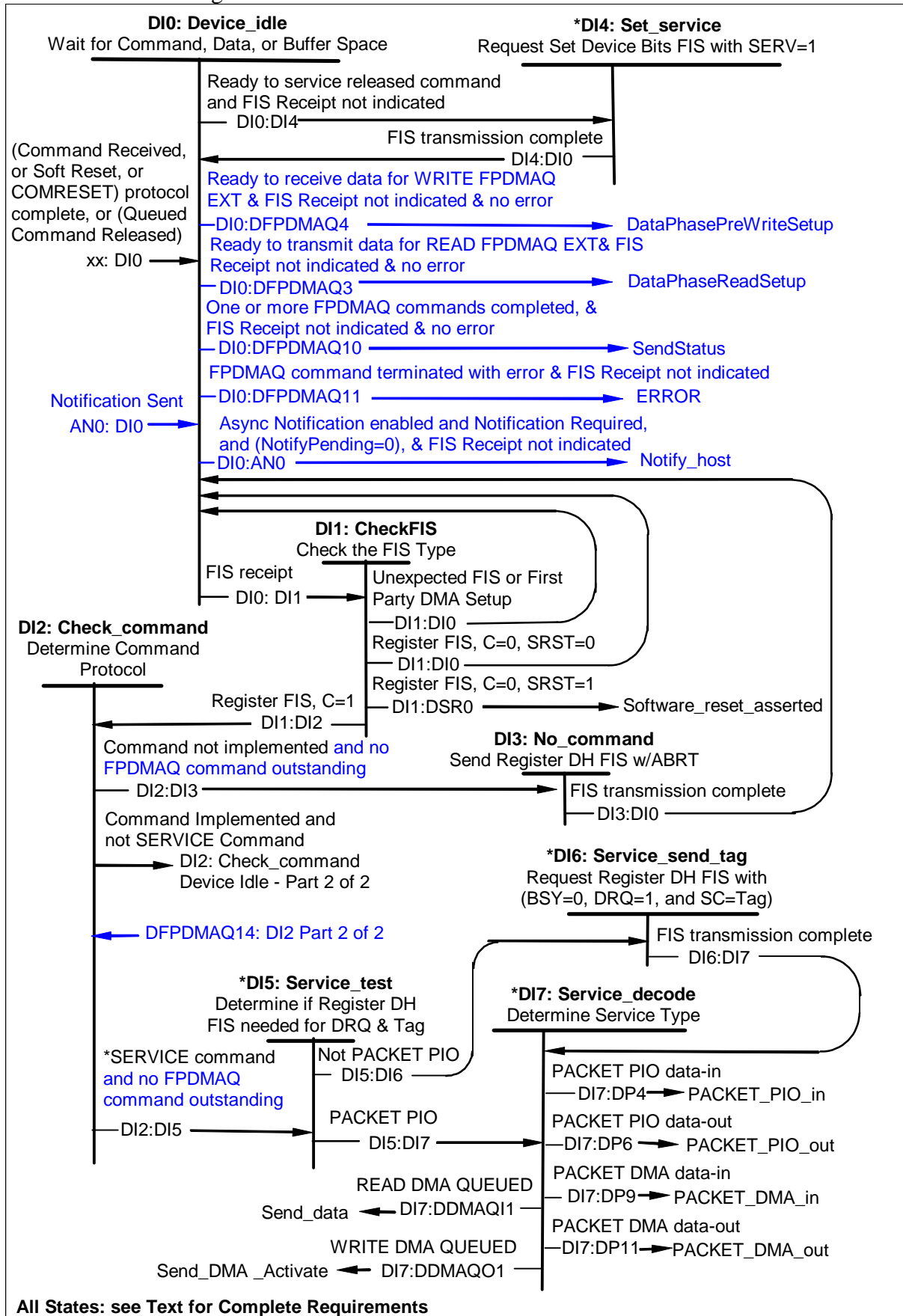
Host Phy Initialization State Machine



Device Phy Initialization State Machine



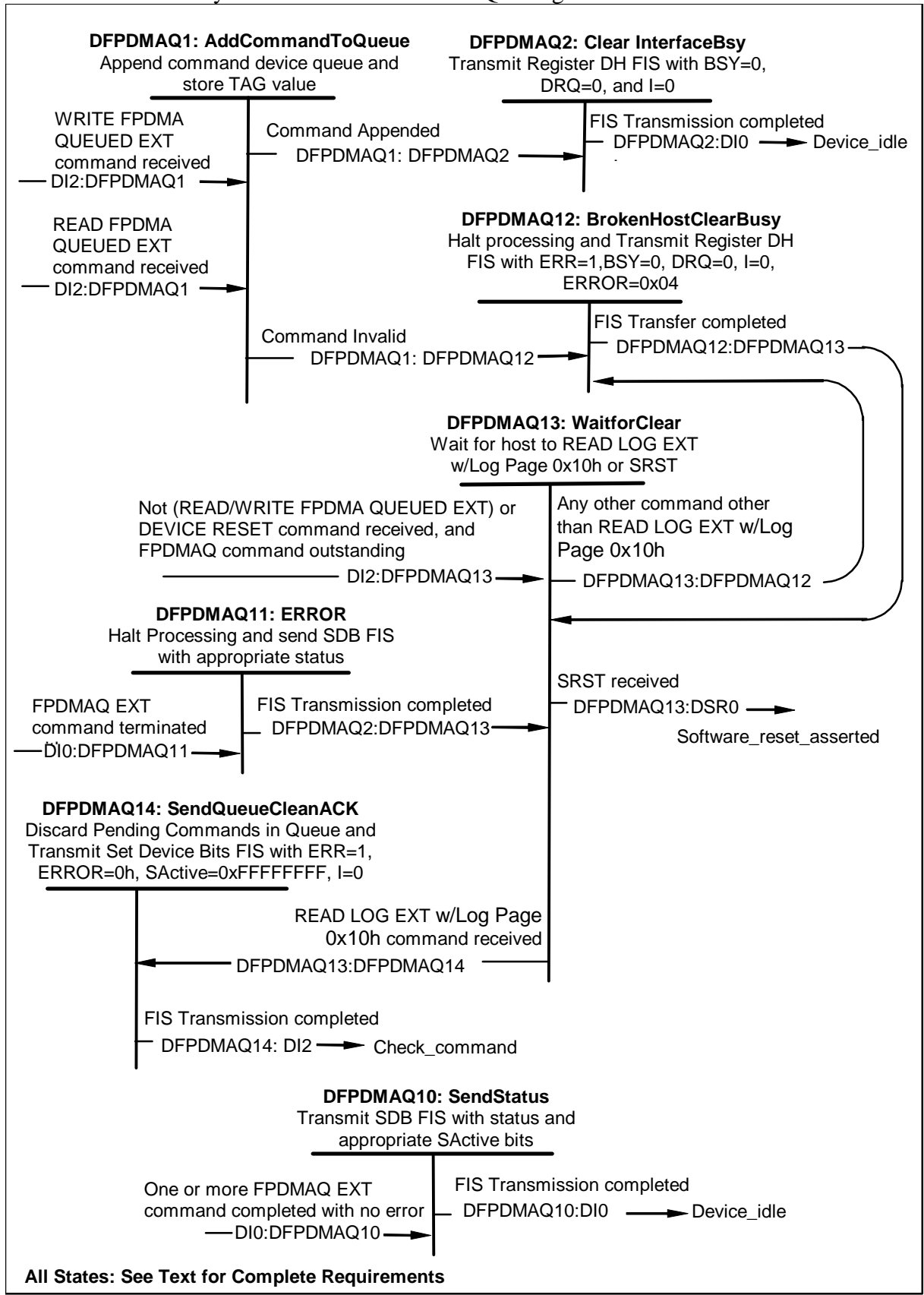
Device Idle State Diagram – Part 1 of 2



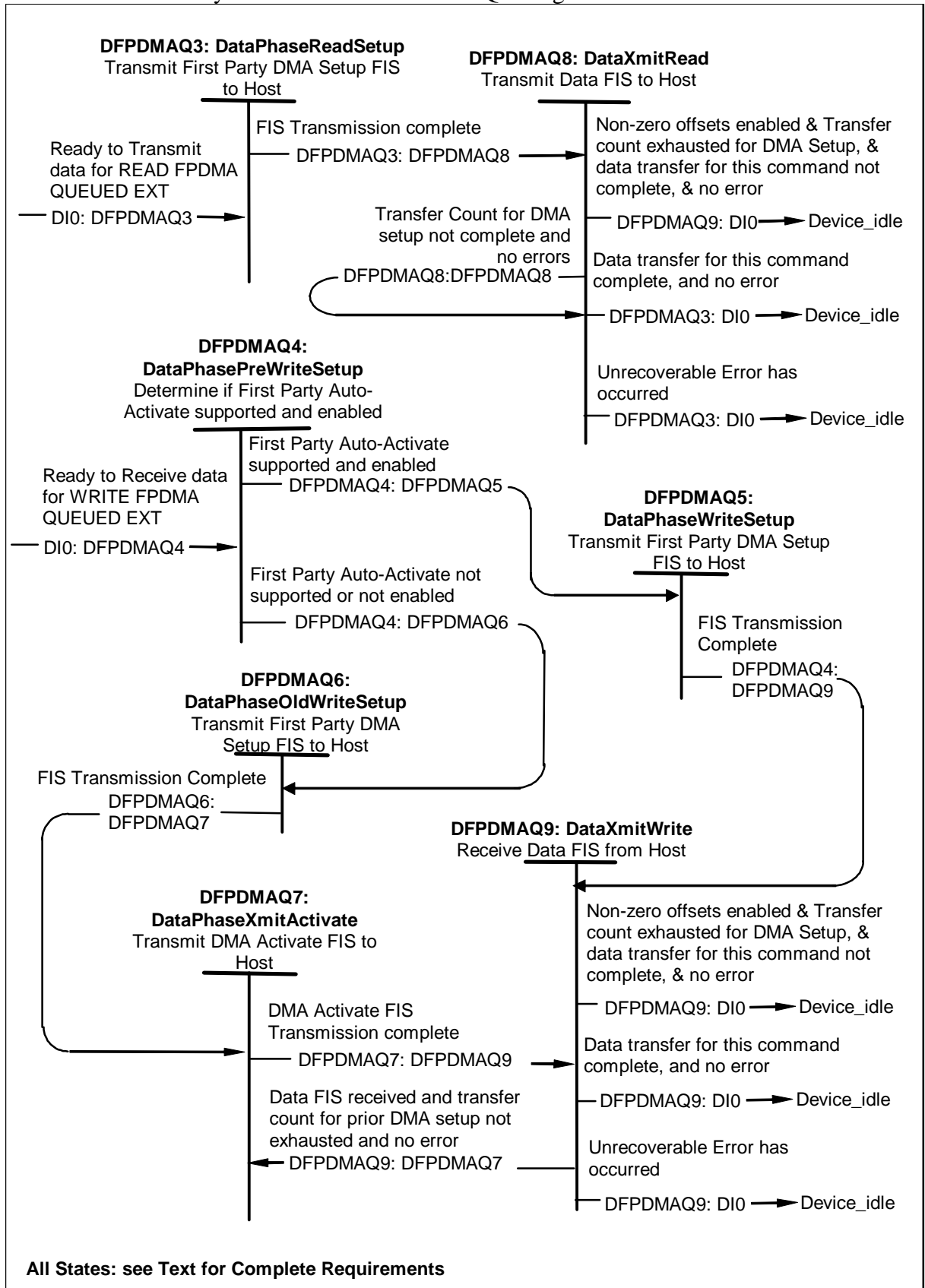
Device Idle State Diagram – Part 2 of 2



Device Command Layer Protocol for Command Queuing Part 1 of 2



Device Command Layer Protocol for Command Queuing Part 2 of 2



Host Command Layer Protocol for Command Queuing

