



Date: 14 October 2003
To: T10 Technical Committee
From: Ralph O. Weber
Subject: Making the DQUE bit obsolete in SAM-3

When 03-254v0 made the DQUE bit in the Control mode page obsolete in SPC-3, it overlooked SAM-3.

In the introduction to the task set management examples at the end of SAM-3, the DQUE bit is mentioned as a bit that is ignored by the examples.

Change 1: To eliminate the reference to the DQUE bit and to reduce the need for future changes to the task set management examples introduction or the examples themselves, the following changes should be made:

8.8 Task set management examples

8.8.1 Introduction

Several task set management scenarios are shown in 8.8.2, 8.8.3, and 8.8.4. The examples are valid for configurations with one or multiple SCSI initiator ports, when the TST field contains 000b (i.e., the interaction among tasks in a task set is independent of the SCSI initiator port originating a task). The examples are also valid for a single SCSI initiator port, when the TST field contains 001b (i.e., task set management proceeds independently for each SCSI initiator port and the events and transitions in one SCSI initiator port's task set do not affect the task set management for another SCSI initiator port's task set). Throughout these examples, the scope of the task set box drawn in each snapshot depends on the setting of the TST field in the Control mode page (see [SPC-2 SPC-3](#)).

The figure accompanying each example shows successive snapshots of a task set after various events, such as task creation or completion. In all cases, the constraints on task completion order established using [Control mode page \(see SPC-3\) fields other than the TST field \(e.g., the QUEUE ALGORITHM MODIFIER field\) and DQUE bit in the Control mode page \(see SPC-2\)](#) are not in effect.