To: T10 Technical Committee
From: Robert Sheffield (Robert.L.Sheffield@intel.com), Intel Corporation
Date: October 23, 2002
Subject: T10/02-439r0, SAS SMP Phy Control Extensions

Revision History
Revision 0 (October 18, 2002) first revision

Related Documents
SAS-r02a – Serial Attached SCSI revision 02a.

Overview
This proposal adds specific controls to allow an initiator greater control over the OOB initialization of an expander port known to be connected to a SATA target to mitigate potential incompatibilities between the SATA device and the SAS OOB signalling.

Suggested Changes
Modify table 114 – Phy operation, to add the following operations:

<table>
<thead>
<tr>
<th>Code</th>
<th>Function</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>08h</td>
<td>BYPASS COMSAS</td>
<td>Until next power-cycle, bypass generation of the COMSAS during any link reset or HARD_RESET sequence. Once COMINIT is both sent and received, proceed directly to the SP19:SATA_COMWAKE state.</td>
</tr>
<tr>
<td>09h</td>
<td>END BYPASS COMSAS</td>
<td>Restores the normal mode of attempting COMSAS before proceeding to the SATA COMWAKE sequence.</td>
</tr>
</tbody>
</table>

Modify the OOB state machines accordingly.