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6.2 ADP compliance points

An ADP compliance point is a defined point in the ADP physical interconnection. At an ADP compliance point a compliant device shall meet the ADP interoperability specifications. ADP compliance points always occur at separable connectors. Table n lists the ADP compliance points.

Table n. ADP Compliance points

Compliance point	Type	Description
T _t	intra-enclosure	Target port connector; transmit serial port
T _r	intra-enclosure	Target port connector; receive serial port
C _t	inter-enclosure	External cabinet connector; transmit serial port
C _r	inter-enclosure	External cabinet connector; receive serial port

6.3 ADP reference points

An ADP reference point is a defined point in the ADP physical interconnection. Every ADP compliance point is an ADP reference point. Table n lists the ADP reference points in addition to the ADP compliance points listed in Table n.

Table n. Additional ADP reference points

Reference point	Description
I _t	Initiator port connector; transmit serial port
I _r	Initiator port connector; receive serial port

6.4 General interface specification

A Tx-Rx connection is a complete simplex signal path from the output reference point of one ADI device to the input reference point of a second ADP device.

This section specifies characteristics of the electrical signal and the signaling media at the compliance points T_t , T_r , C_t , and C_r in a Tx-Rx connection.

All Tx-Rx connections shall operate with a Bit Error Rate (BER) $< 10^{-12}$. The parameters specified in this section support meeting the BER requirement under all conditions including the minimum input and output amplitude levels.

These specifications are based on ensuring interoperability across multiple vendors supplying the technologies (transceivers and cable plants) under the tolerance limits specified in the document. Tx-Rx connections operating at the maximum specified distance may require some form of equalization (e.g., transmitter pre-emphasis, receiver adaptive equalization, or passive cable equalization) to enable the connection to meet the signal requirements. A specific installation may obtain a longer distance by engineering a Tx-Rx connection based on knowledge of the technology characteristics and the conditions of installation and operation (e.g., a closed engineering environment); however, such distance extensions are outside the scope of this standard.

Table n defines the general interface characteristics.

Table n. General interface characteristics



Characteristic Units	1,5 Gbps	3,0 Gbps
Data rate MBps	150	300
Nominal bit rate Mbaud	1 500	3 000
Unit interval (UI) ps	666,667	333,333
Data rate tolerance at T_r , C_r , I_r ppm	+/-100	+/-100
Data rate tolerance at T_t , C_t , I_t ppm	+/-100	+/-100
Media Impedance ^a ohm	100	100
Notes: a The media impedances are the differential, or odd mode, impedances.		

6.5 Eye Masks

6.5.1 Eye masks overview

The eye masks shown in this clause provide graphical representations of the voltage and time limits. The time values between $X1$ and $1-X1$ cover all but 10^{-12} of the jitter population. The random content of the total jitter population has a range of $\pm 7\sigma$. Current oscilloscope technology only supports approximately $\pm 3\sigma$, therefore the traditional method of using an oscilloscope to compare the signals against these masks to ascertain jitter compliance is invalid. The oscilloscope remains valid for determining rise/fall times, amplitude, and under and overshoots.

6.5.2 Transmitted eye mask

Figure n describes the transmitted eye masks at the T_t , and C_t compliance points. These eye masks apply directly to compliance points, and indirectly to non-compliance reference points.



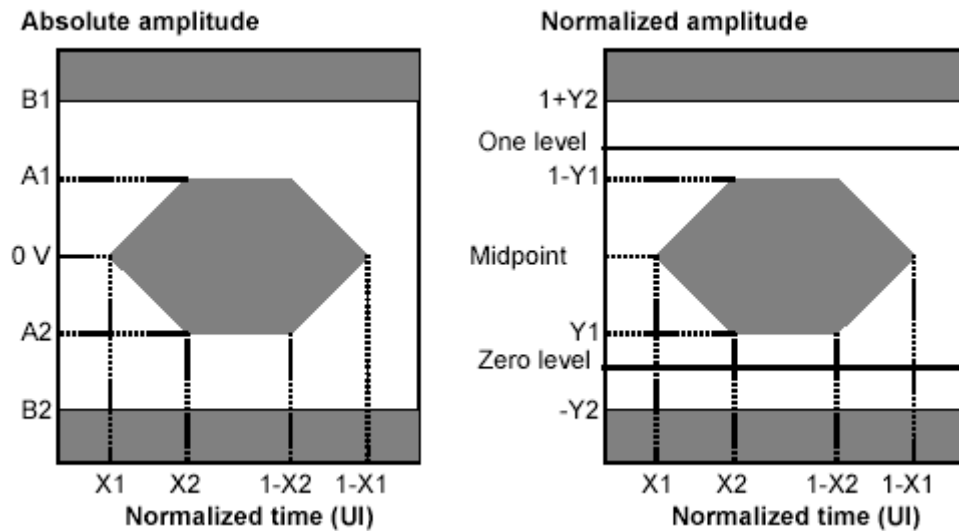


Figure n. Absolute and normalized amplitude eye diagrams at T_t and C_t

For unbalanced drivers the absolute amplitude values assume A.C. coupling between the test load and the driver.

Drivers must meet the normalized and the absolute amplitude requirements. The Y1 and Y2 amplitudes allow signal undershoot and overshoot, respectively, relative to the levels determined to be one and zero.

To accurately determine the one and zero levels for use with the normalized mask, use an oscilloscope having an internal histogram capability. Use the voltage histogram capability and set the time limits of the histogram to extend from 0,4 UI to 0,6 UI. Set the voltage limits of the histogram to include only the data associated with the one level. The one level to be used with the normalized mask shall be the mean of the histogram. Repeat this procedure for the zero level.

The eye diagram mask applies to jitter after application of a single pole high-pass frequency-weighting function that progressively attenuates jitter at 20 dB/decade below a frequency of bit rate/1 667.

6.5.3 Delivered (receive) eye mask

Figure n describes the delivered (received) eye mask. This eye mask applies to jitter after the application of a single pole high-pass frequency-weighting function that progressively attenuates jitter at 20 dB/decade below a frequency of the bit rate/1 667.



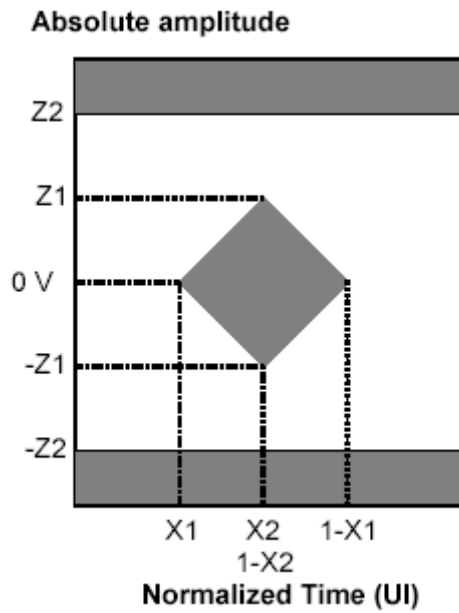


Figure n. Eye mask at T_r and C_r

Verifying compliance with the limits represented by the received eye mask should be done with reverse channel traffic present in order that the measurement takes the effects of cross talk into account.

6.5.4 Jitter tolerance masks

Tolerance eye masks at T_t and C_t shall be based on Figure n and shall be constructed using the X_2 , Z_1 and Z_2 values given in Table n. X_1 values shall be half the value for total jitter given in the tables for jitter value frequencies above bit rate/1 667.

The tolerance masks are identical to the output masks except that the X_1 and X_2 values are each increased by half the amount of the sinusoidal jitter values given in the jitter tolerance tables.

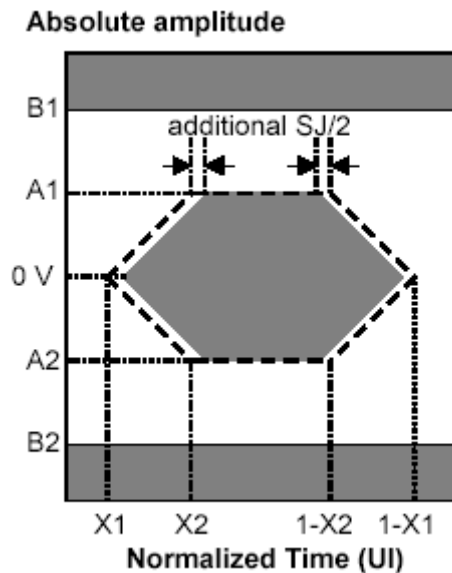


Figure n. Deriving a tolerance mask at T_t and C_t

Receiver Tolerance eye masks at T_r and C_r shall be based on Figure n and shall be constructed using the X_2 and Z_2 values given in Table n. X_1 shall be half the value for total jitter given in these tables for jitter frequencies above bit rate/1 667.

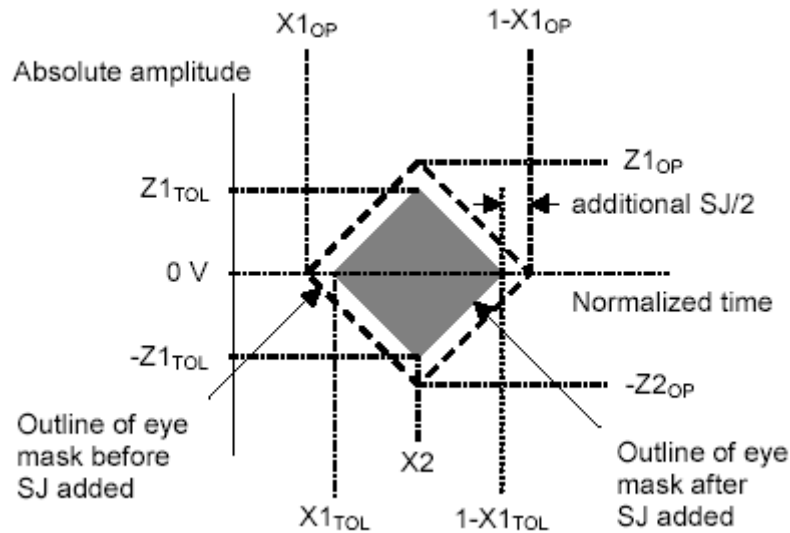


Figure n. Deriving a tolerance mask at T_r and C_r

However, the leading and trailing edge slopes of Figure n shall be preserved. As a result the amplitude value of Z1 is less than that given in Table n and must therefore be calculated from those slopes as follows:

$$Z1_{Tol} = Z1_{OP} \times (X2_{OP} - (0,5 \times (\text{additional SJ UI}) - X1_{OP})) / (X2_{OP} - X1_{OP})$$

Z1Tol = value for Z1 to be used for the tolerance masks

Z1OP, X1OP, and X2OP are the values in Table 18 for Z1, X1, and X2

The X1 points in the receive tolerance masks are greater than the X1 points in the output masks, again due to the addition of sinusoidal jitter.

Figure n defines the sinusoidal jitter mask.

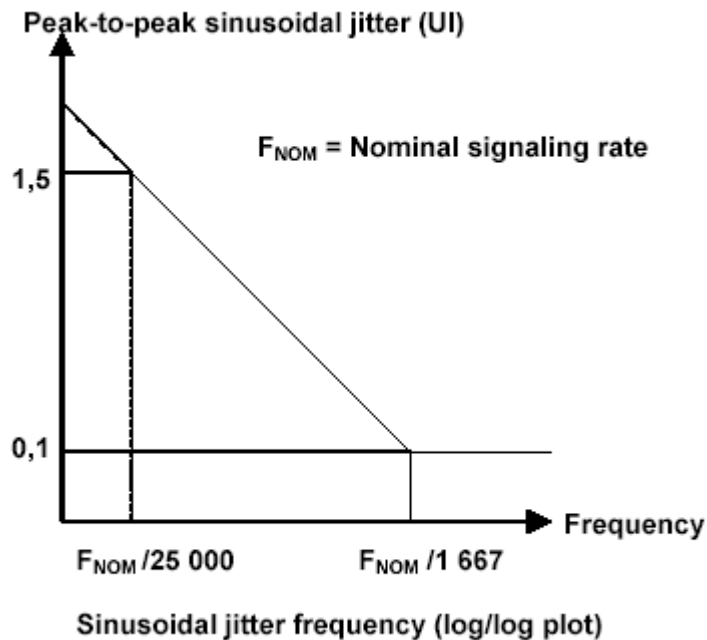


Figure n. Sinusoidal jitter mask

6.6 Transmitted signal characteristics

6.7 Termination

6.8 Bus timing values

6.9 Timing description

6.10 Measurement points

6.11 Timing parameters

6.12 Setup and hold timings

7 Bus signals

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7.4 Signal sources