SPI-5 Termination clean up T10/02-226r1
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Table 23 - I-V requirements for differential impedance, common mode impedance, and V_{BIAS} tests

Values (see figure 45)	Differential impedance and V_{BIAS} tests [a] (see figure 44)	Common mode impedance and V_{BIAS} tests (see figure 46)
V_1 (mV)	n/a	1125
V_2 (mV)	n/a	1375
I_1 (mA)	1,0	n/a
I_2 (mA)	1,1	n/a
V_3 (V)	1,0	2,0
V_4 (V)	-1,0	0,5
I_{MAX} (mA)	9,00 [d]	N/A
I_{MIN} (mA)	-11,25 [d]	N/A
S_1 (Ω)	100 [b]	75 to 100 [c]
S_2 (Ω)	110 [b]	300 to 400 [c]
Measurement	D.C.	D.C.

[a] $V_A + V_B = 2,5 \pm 0,2$ V (see figure 44)

[b] The differential impedances of S_1 and S_2 is the open bus segment path value that allowed be set to any value from 55 Ω to 130 Ω ± 14% for closed bus segment path. The difference between S_1 and S_2 shall not be greater than 10 Ω across the 27 lines.

~~The differential impedance shall be set to a nominal of 105 Ω if the system is not a closed system.~~ Open bus segment path is a segment that may be constructed by end users. A closed bus segment path is constructed by the manufacturer.

[c] The common mode S_1 and S_2 impedances change with differential impedance changes such that the nominal S_1 (i.e., 100 Ω) and nominal S_2 (i.e., 110 Ω) differential is an S_1 common mode of 100 Ω and an S_2 common mode of 300 Ω.

[d] I_{MAX} and I_{MIN} are measured at the nominal differential impedance where S_1 is 100 Ω and S_2 is 110 Ω.