

To: T10 Technical Committee
From: Rob Elliott, HP (elliott@hp.com)
Date: 3 June 2002
Subject: T10/02-216r0 SAS SMP transport layer state diagrams

Revision History

Revision 0 (3 June 2002) first revision

Related Documents

sas-r00a - Serial Attached SCSI revision 00a

Overview

SMP transport layer state diagrams are needed. These interface to two lower-level link layer state machines:

- 1) the SAS link layer state machine (SL); and
 - 2) the SAS SMP link layer state machine,
- and to an undefined higher-level application layer state machine.

Suggested Changes [all new text]

9.4.5 SMP transport layer state diagrams

9.4.5.1 SMP transport layer state diagrams for an application client

9.4.5.1.1 SMP transport layer state diagrams for an application client overview

Figure xx describes the SMP transport layer state machine for an application client.

SMP transport layer - application client

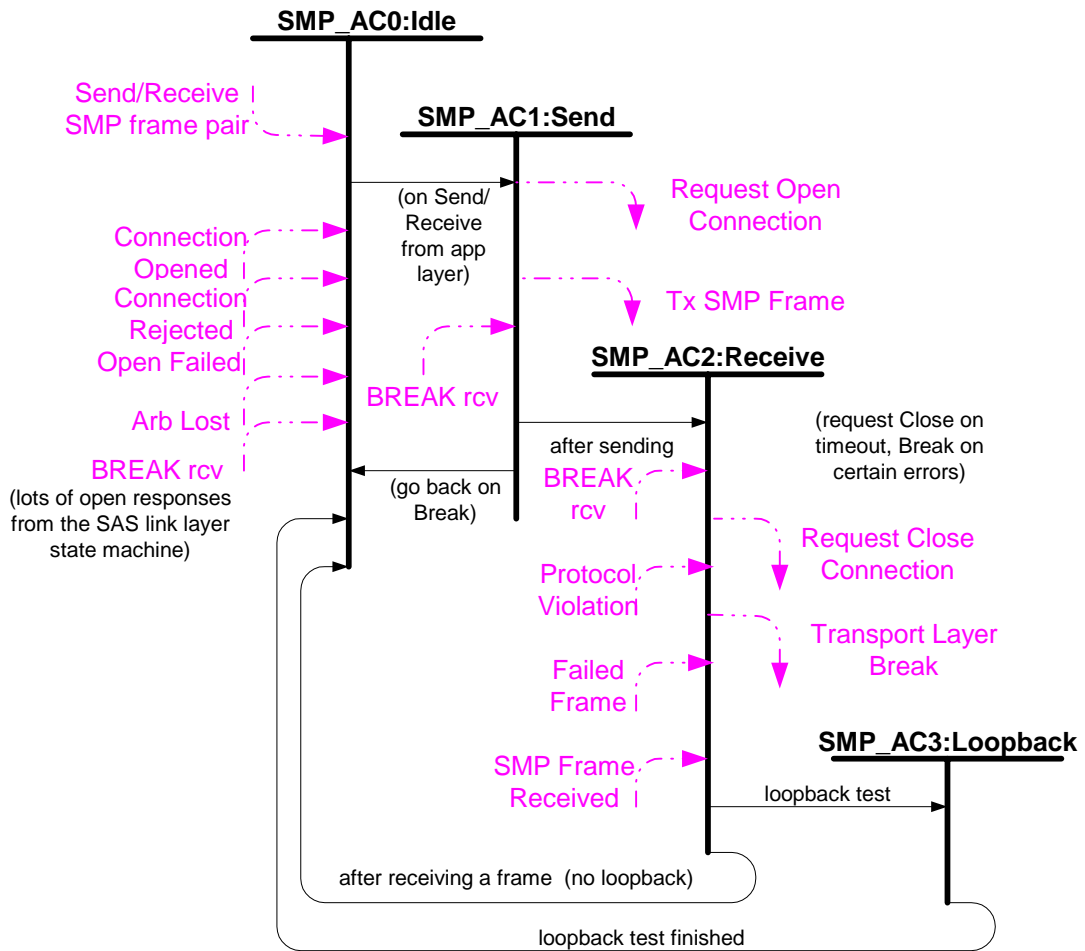


Figure 1. SMP transport layer state machine - application client

9.4.5.1.2 SMP_AC0 state

[add text here]

9.4.5.1.2 SMP_AC1 state

[add text here]

9.4.5.1.2 SMP_AC2 state

[add text here]

9.4.5.1.2 SMP_AC3 state

[add text here]

9.4.5.2 SMP transport layer state diagrams for a device server

9.4.5.2.1 SMP transport layer state diagrams for a device server overview

Figure yy describes the SMP transport layer state machine for a device server.

SMP transport layer - device server

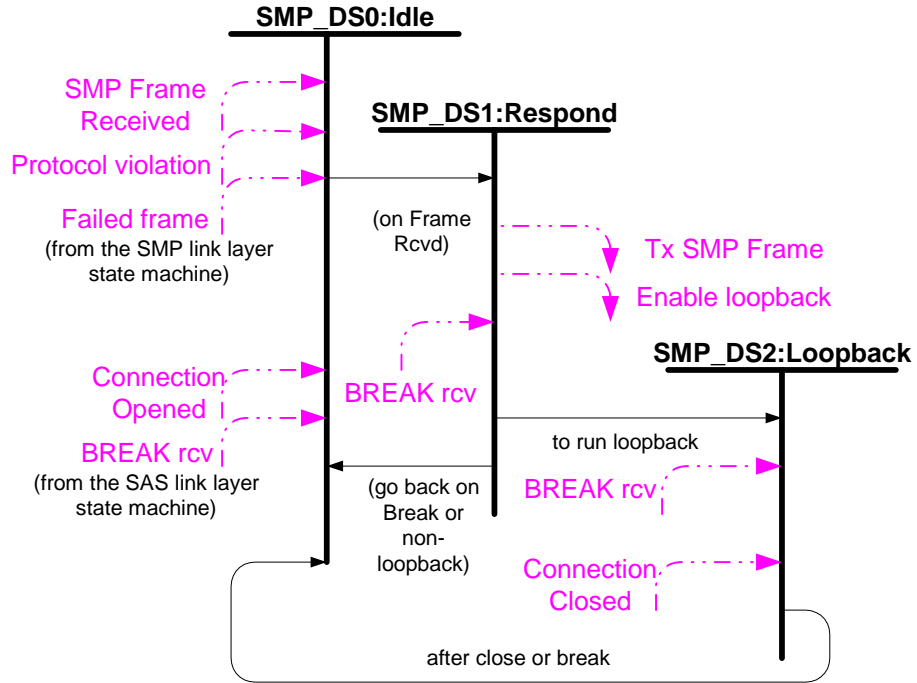


Figure 2. SMP transport layer state machine - device server

9.4.5.1.2 SMP_DS0 state

[add text here]

9.4.5.1.2 SMP_DS1 state

[add text here]

9.4.5.1.2 SMP_DS2 state

[add text here]