

To: T10 Technical Committee  
 From: Dennis Spicher ([dennis.spicher@hp.com](mailto:dennis.spicher@hp.com)) and Rob Elliott, HP ([elliott@hp.com](mailto:elliott@hp.com))  
 Date: 15 May 2002  
 Subject: T10/02-192r0 SES-2 IDENT control for each element

### **Revision History**

Revision 0 (15 May 2002) first revision

### **Related Documents**

ses-r08b - SCSI Enclosure Services revision 8b

### **Overview**

In section 7.2.2 Device element defined for enclosure pages, a control bit (for SEND DIAGNOSTICS) and a status bit (for RECEIVE DIAGNOSTIC RESULTS) are defined for the device element at byte offset 2, bit number 1; these bits are called RQST IDENT and IDENT, respectively. These bits allow the application client to control a visual indication (e.g., a blinking LED) to help locate the device. However, many of the other element types do not have a corresponding pair of bits defined.

### **Suggested Changes**

Define a RQST IDENT and IDENT pair for each element type (using a reserved bit in each existing element format).

Type code	Type of element	Byte Offset	Bit	New
01h	Device	2	1	No
02h	Power supply	1	7	Yes
03h	Cooling element	1	7	Yes
04h	Temperature sensors	1	7	Yes
05h	Door lock	1	7	Yes
06h	Audible alarm	1	7	Yes
07h	Enclosure services controller electronics	1	7	Yes
08h	SCC controller electronics	1	7	Yes
09h	Nonvolatile cache	1	7	Yes
0Bh	Uninterruptible power supply	3	7	Yes
0Ch	Display	1	7	Yes
0Dh	Key pad entry device	1	7	Yes
0Fh	SCSI port/transceiver	1	7	Yes
10h	Language	1	7	Yes
11h	Communication port	1	7	Yes
12h	Voltage sensor	1	7	Yes
13h	Current sensor	1	7	Yes
14h	SCSI target port	1	7	Yes
15h	SCSI initiator port	1	7	Yes
16h	Simple sub-enclosure	1	7	Yes