

To: T10 Technical Committee  
From: Rob Elliott, Compaq Computer Corporation (Robert.Elliott@compaq.com)  
Date: 28 June 2001  
Subject: SPI-4 Obsolete AIP annex

### **Revision History**

Revision 0 (28 June 2001) first revision

### **Related Documents**

spi4r05 - SCSI Parallel Interface - 4 revision 5

### **Overview**

With the industry implementing packetized protocol (information units) in Ultra 320 (SPI-4), I propose that we remove AIP (Asynchronous Information Protection) from SPI-4.

AIP was a temporary measure designed for Ultra 160 devices that lacked packetized protocol support (all of them), which protects the command and status information under a CRC. Due to the swift introduction of Ultra 320, AIP for Ultra 160 has not seen much use in the industry. It wasn't defined early enough to meet Ultra 160 product schedules. It is likely to exist only in Ultra 320 products, which also support packetized protocol. Packetized protocol can be used at 160 MB/sec and slower speeds, so AIP is not needed even when Ultra 320 mode is disabled (e.g. due to domain validation).

Removing the informative annex describing it in SPI-4 doesn't preclude AIP from being implemented based on SPI-3.

### **Suggested Changes**

Remove Annex O - Alternate error detection for the asynchronous information phases (COMMAND, MESSAGE, and STATUS).

Add mention of the obsolete feature in section 1:

#### **1 Scope**

This standard has made obsolete the following:

[a\) Alternate error detection for the asynchronous information phases \(COMMAND, MESSAGE, and STATUS\)](#)