To   : T10 Technical Committee
From   : Brian Day, LSI Logic Corporation
Date   : May 17, 2001
Subject : P_CRCA Streaming Clarifications

The current wording from SPI-4, revision 4 from section 14.3.4:

During read streaming the sequence of SPI data stream information units shall end when the P_CRCA signal is asserted before the end of the current SPI data stream information unit boundary. If during the last SPI data stream information unit the P_CRCA signal was not asserted and initiator detects a REQ transition after receiving the last iuCRC for a SPI data stream information unit that initiator shall receive the next SPI data stream information unit. If during the last SPI data stream information unit the P_CRCA signal was asserted and initiator detects a REQ transition after receiving the last iuCRC for a SPI data stream information unit that initiator shall logically disconnect from the current I_T_L_Q nexus.

We propose the following changes and additions to clarify the target requirements on P_CRCA usage (shown as underlined and italicized):

During read streaming the target shall end a sequence of SPI data stream information units by performing one of the following:
>   *a) should assert the P_CRCA signal before the end of the current SPI data stream information unit boundary (see 8.2).*
>   *b) may change the C/D, I/O, or MSG signals on a SPI data stream information unit boundary.*

During read streaming if during the last SPI data stream information unit the P_CRCA signal was not asserted and *an* initiator detects a REQ transition after receiving the last iuCRC for a SPI data stream information unit that initiator shall receive the next SPI data stream information unit. If during the last SPI data stream information unit the P_CRCA signal was asserted and *an* initiator detects a REQ transition after receiving the last iuCRC for a SPI data stream information unit that initiator shall logically disconnect from the current I_T_L_Q nexus.

And add an additional note at the bottom of Table 29:

A SCSI device is not required to use write flow control even if it is enabled.