

To: T10 Technical Committee  
From: Rob Elliott, Compaq Computer Corporation (Robert.Elliott@compaq.com)  
Date: 14 May 2001  
Subject: Minutes of SRP teleconference – 11 May 2001

### **Revision History**

Revision 0 (14 May 2001) first revision

### **Attendance**

Bob Nixon	Emulex
Chuck Gibson	Adaptec
Cris Simpson	Intel
Ed Gardner	Ophidian Designs
George Penokie	Tivoli/IBM
Kamran Tavakoli	Adaptec
Keith Holt	LSI Logic
Paul Grun	Intel
Ralph Weber	ENDL
Rob Haydt	Microsoft
Robert Elliott	Compaq
Xai Phan	McData

### **Minutes**

SRP revision 4 was submitted by the deadline set by the T10 chair. The group agreed that it is a good step forward and that a letter ballot in July is still possible.

Ed Gardner will incorporate editorial changes and any technical changes for which consensus is reached on the T10 reflector into revision 5 by 5/24/2001.

Cris Simpson will create a proposed rewrite for section 4.1 (the model section). He will ask Ed for any material he has so far and create a combined or competing proposals for that section.

Cris Simpson will ensure there are enough reserved fields for the future multichannel proposal.

George Penokie will write a SAM mapping annex.

With the June meeting or thereafter, the editor should only make technical changes when a proposal has been approved by the SRP working group (similar to the model used by SPI-4 and SPC-2, but without the plenary approval step).

### **Schedule**

5/24/2001 Thu 10:30am Mountain time  
SRP revision 5 due.

5/25/2001 Fri  
Conference call to review SRP revision 5. Cris Simpson to host.

6/19/2001 Tue 9am - 7pm  
6/20/2001 Wed 9am – 2pm  
SRP working group meeting. Redmond, WA; Microsoft campus; hosted by Rob Haydt. Review SRP revision 6.

7/18/2001 Thu (after T10 plenary)  
7/19/2001 Fri 9am – 2pm  
SRP working group meeting. Colorado Springs, CO; part of T10 week.