
SCSI Signal & Cable Analysis Using Actual U320 Target and Initiator Silicon

Seagate Technology
VLSI Controller Development
Scotts Valley California

Seagate U320 Hardware

- Seagate's invitation to bring system interconnect configurations to the lab for test has been taken up by numerous system manufacturers with positive results and the invitation is still open on a RSVP basis. Priorities may require the invitation being narrowed in the future.
- All testing was performed with fully-integrated U320 Initiator and Target Silicon.
 - No external drivers or generators.
 - Initiator is legacy-compatible SCSI with full U320 capability.
 - Target is Seagate Disc controller, full legacy compatible with full U320 capability.

Seagate Testing Conditions

- All testing was with full SCSI data bus, backplanes were populated with real drives, and cables and terminators were legacy U160 hardware.
- Testing was always performed with all data bits running full random data pattern, (256K).
- When known, the worst data bit was selected and the worst slot(s) of the backplane. First & last slot always shown.
 - Some determined by experiment, some by vendor.
- Probes were at drive PCBA, at the chip pins.
- Driver settings were ± 500 mV under all conditions to maintain consistency. Slew rates were approximately 400 mV/nS.
Precomp at 25%

Seagate Testing Conditions

- Test setup had poor stub and terminator configurations due to emulation and probe hardware, excessive PCBA routing, etc. on Seagate Initiator.
 - This added offset and additional initiator-end noise which will not be seen in the actual HBA's or drives.
 - This has already been verified with new hardware configurations.

OEM Backplanes Tested

- Several OEM Backplanes data will be shown.
 - 5-slot, Major OEM Volume Product
 - Very good quality at U160, good cable-to-first-drive spacing, excellent drive-to-drive routing. About 3cm between drives.
 - 6-slot Backplane "A", Major OEM, Volume Product
 - Very poor drive-to-drive routing, and poor signal quality at U160. About 3cm between drives. Could not do 12m due to special connector requirements.
 - 6-slot Backplane "B", Major OEM, In Production
 - Fair to good performance at U160. About 3.5cm between drives.
 - 10-slot, Major OEM, High Volume Product
 - Fair performance at U160, but known problems on certain bits and slots. Very high resistance first to last slot. About 3cm between drives.

Other Backplanes Tested

- Several OEM's had designs which were tested at our lab, but we cannot show data for confidentiality reasons. In all cases:
 - The signals at U320 were as good or better than at U160.
 - Noisy backplanes showing high reflections at U160 also showed high reflections at U320
 - In some cases the 80 mHz was constructive, and the amplitude at U320 was greater.
- Seagate backplanes not shown for time constraints, but can be provided. No issues noted.

Cables Tested

- Data on several cable configurations will be shown.
 - 28" Twisted-Flat cable.
 - 12m Amphenol Twisted Flat cable.
 - 12 meter Madison Round cable.
 - 25 meter Hitachi Round cable.
- Multiple cable configurations available, and Seagate will provide data on any Cable/Backplane configuration requested, as long as we have hardware or it is provided.

Results

- Data with actual U320 controller and integrated precomp transceiver was in every case better than data taken with prior test chips.
- Data with U320 driver was in every case much better than data taken with generator-driver experiments.
 - The theorized deficiencies in using general purpose test equipment has been confirmed with the physical verification using real SPI-4 transceivers.
- Data with precomp enabled on short-cable to backplane configurations was better than experiments, and in some cases improved signal integrity.
- The viability and effectiveness of Precomp has been confirmed.

More Results

- The proposed signal masks are fully supported by test results of all available system interconnect configurations.
- In all cases, if the signals passed U160, they also passed U320 amplitude margins (at 2nS & 3 nS)
- In all cases, the data "eye" at U320 was as good or better than data "eye" at U160.
 - With the exception of the width, of course.
- There was never a poorer amplitude at U320 than at U160 when precomp was enabled.
- Some backplanes "amplify" 80 MHz signals due to favorable reflections.

SCSI Signal, Backplane & Cable Analysis

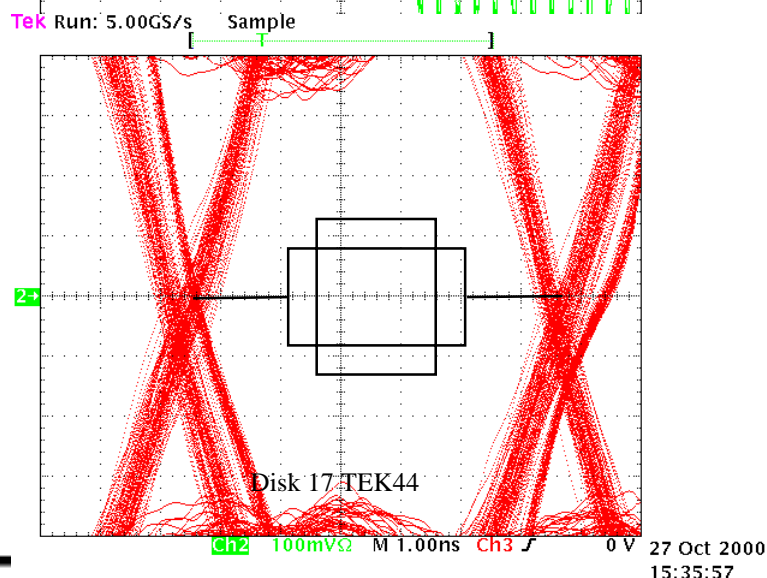
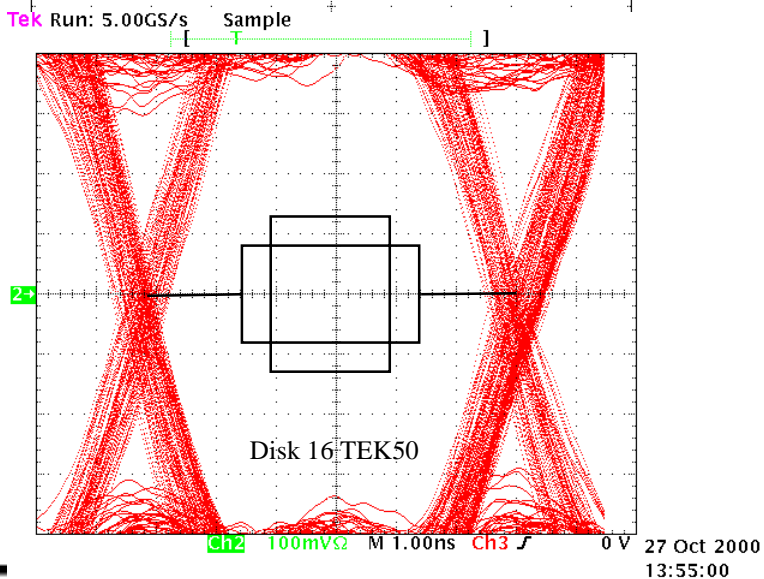
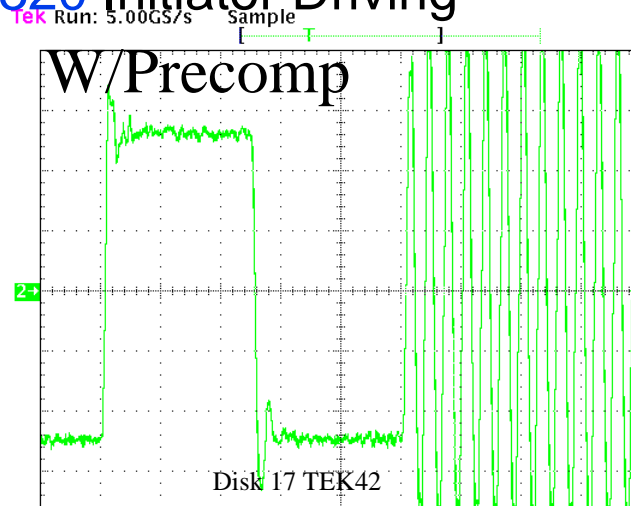
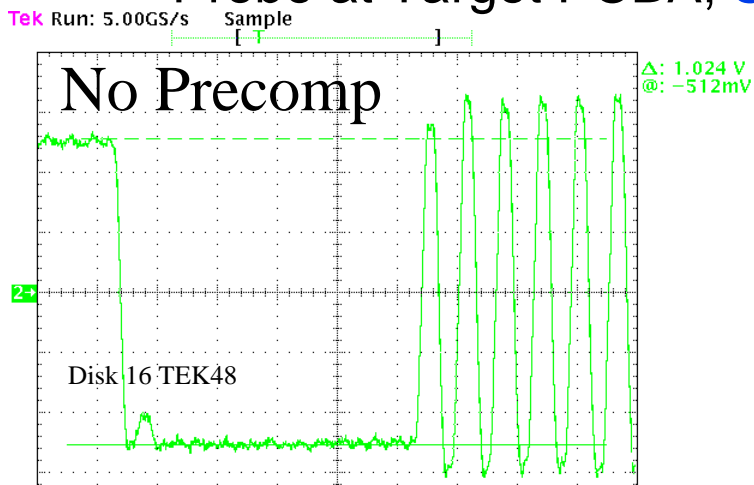
5-Slot Backplane

U320 Target and Initiator Silicon

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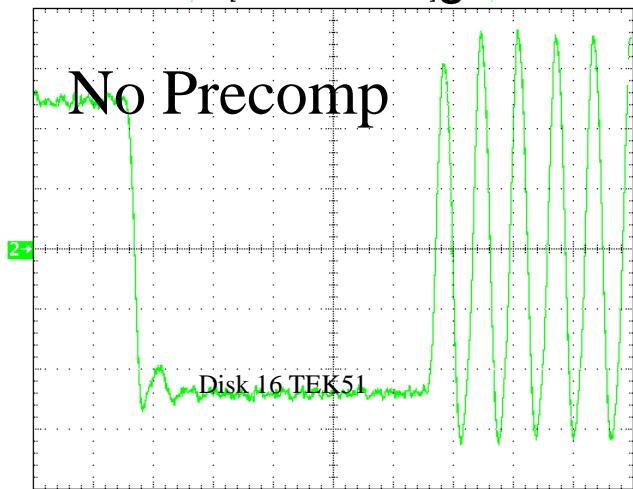
5-Slot Backplane - SU320 Initiator -28" TnF, First Slot

Probe at Target PCBA, Seagate U320 Initiator Driving

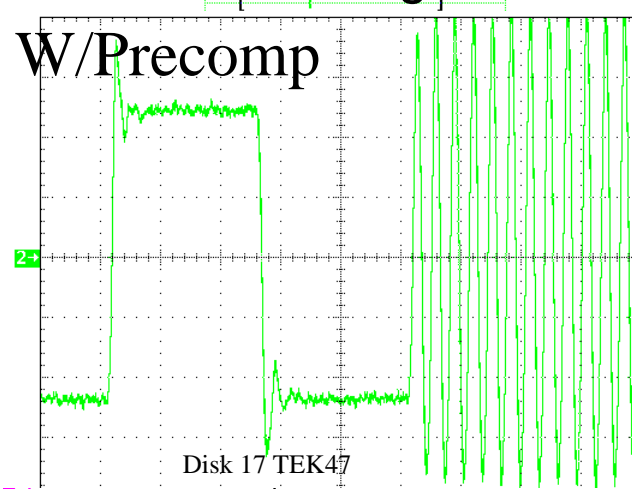


5-Slot Backplane - SU320 Initiator -28" TnF, Middle Slot

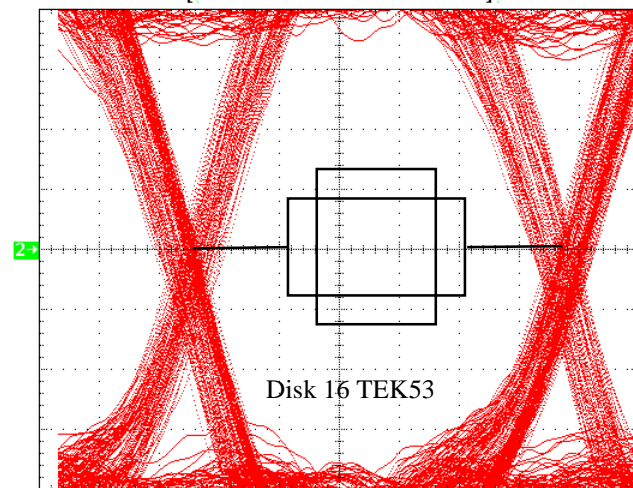
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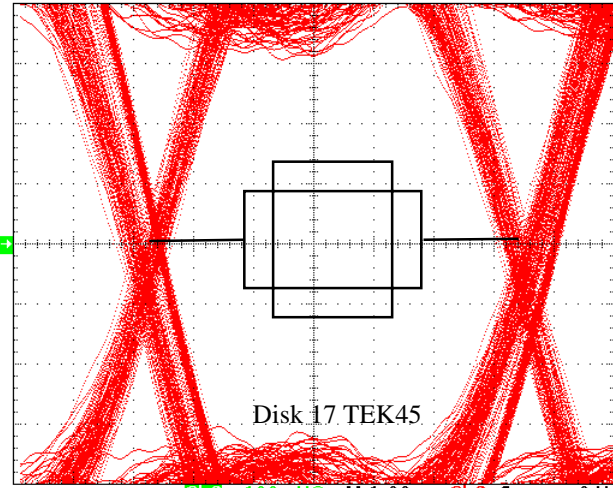
Tek Run: 5.00GS/s Sample



Tek Run: 5.00GS/s Sample



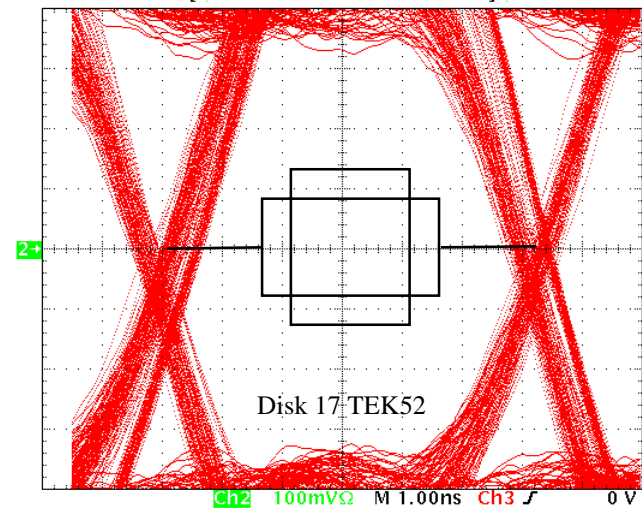
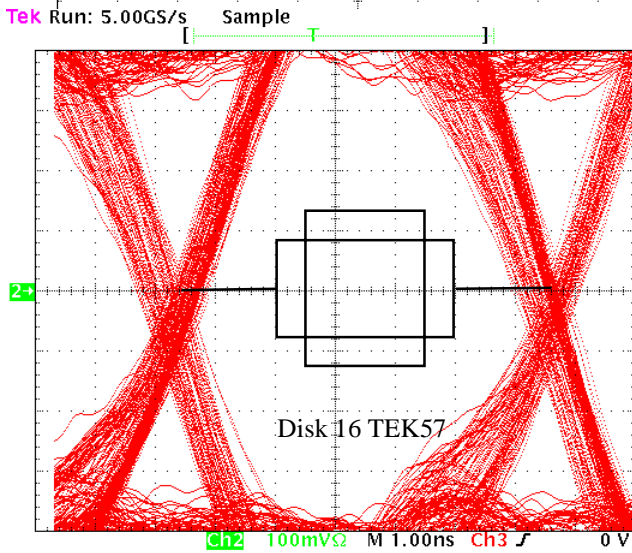
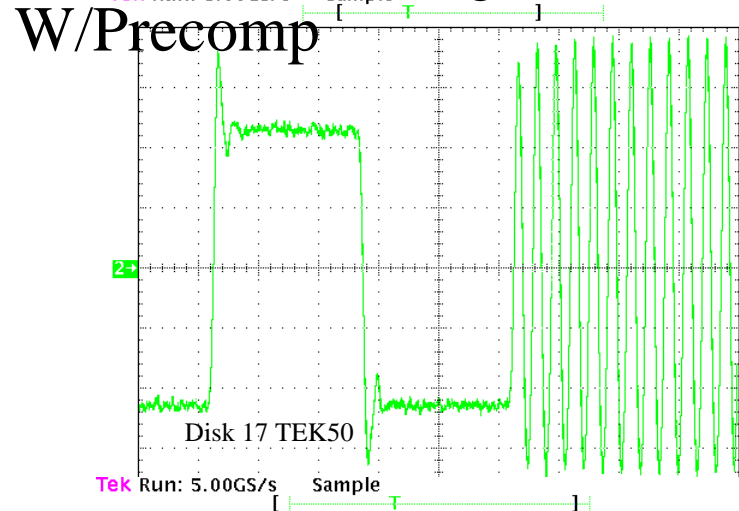
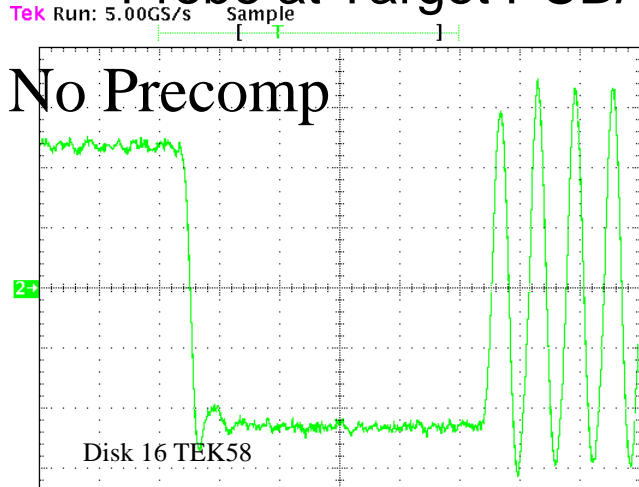
Ch2 100mVΩ M 1.00ns Ch3 J 0 V 27 Oct 2000 14:06:17



Ch2 100mVΩ M 1.00ns Ch3 J 0 V 27 Oct 2000 15:39:42

5-Slot Backplane - SU320 Initiator -28" TnF, Last Slot

Probe at Target PCBA, Seagate U320 Initiator Driving

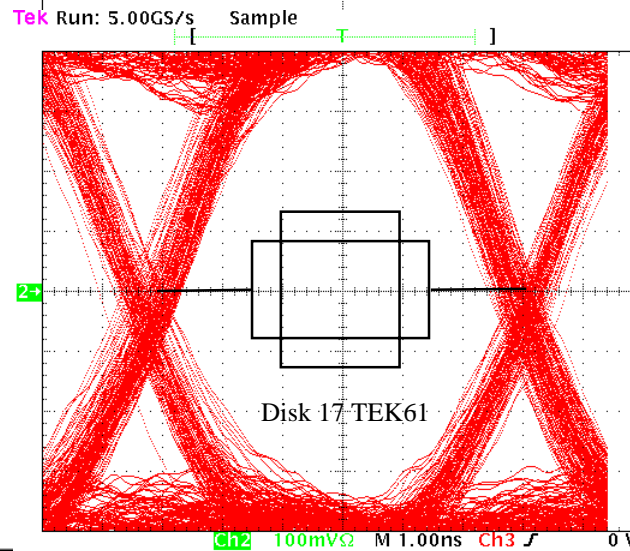
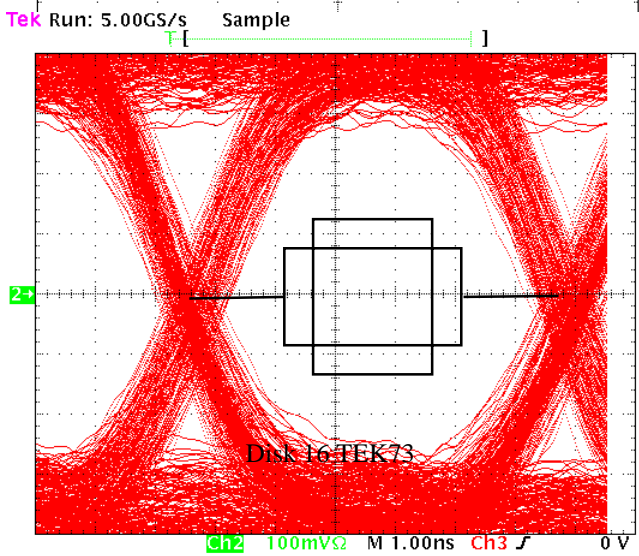
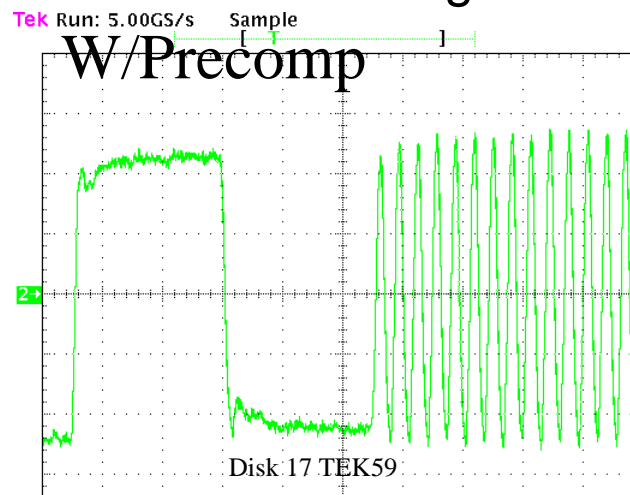
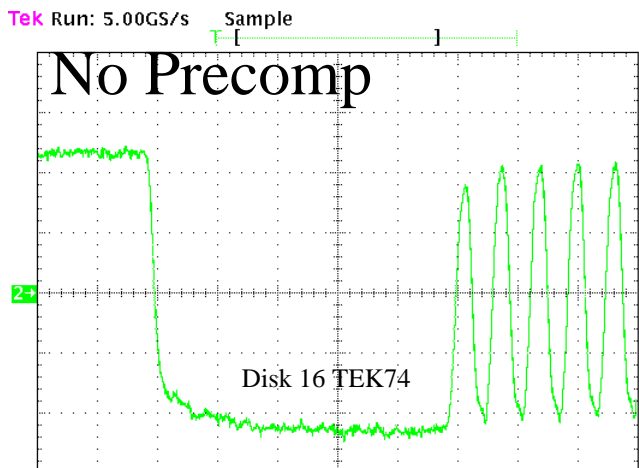


27 Oct 2000
14:19:29

27 Oct 2000
15:47:00

5-Slot Backplane - SU320 Initiator -12m Rnd , First Slot

Probe at Target PCBA, Seagate U320 Initiator Driving

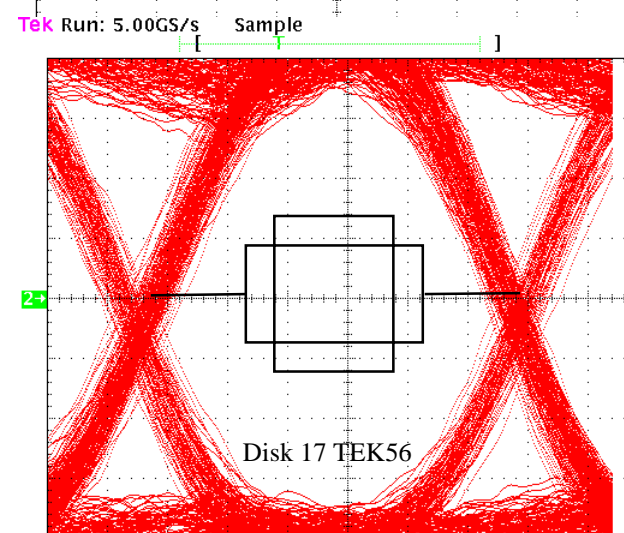
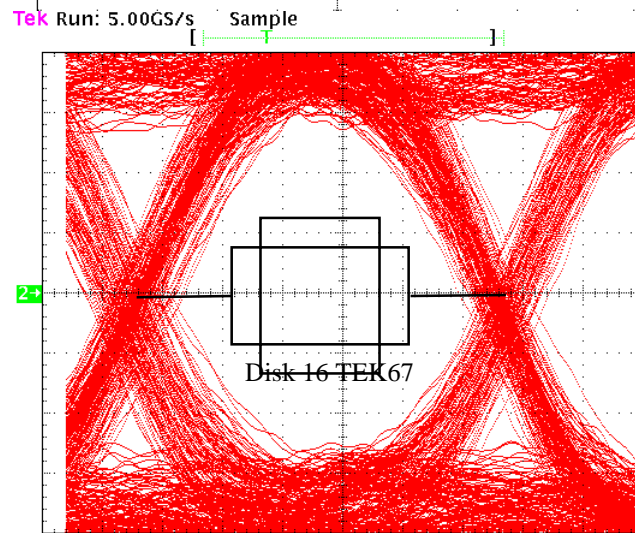
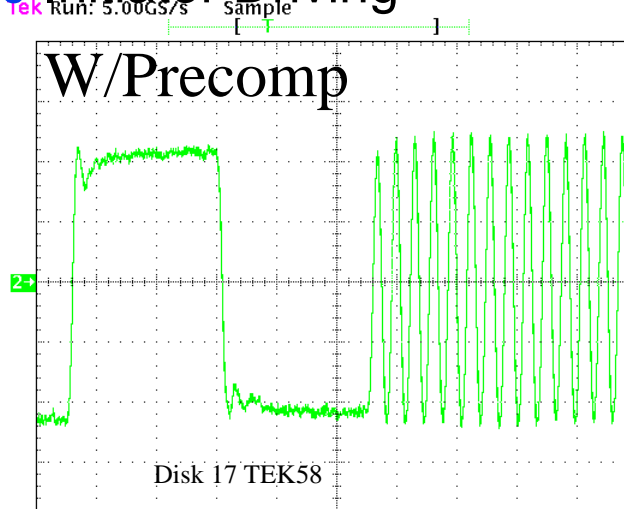
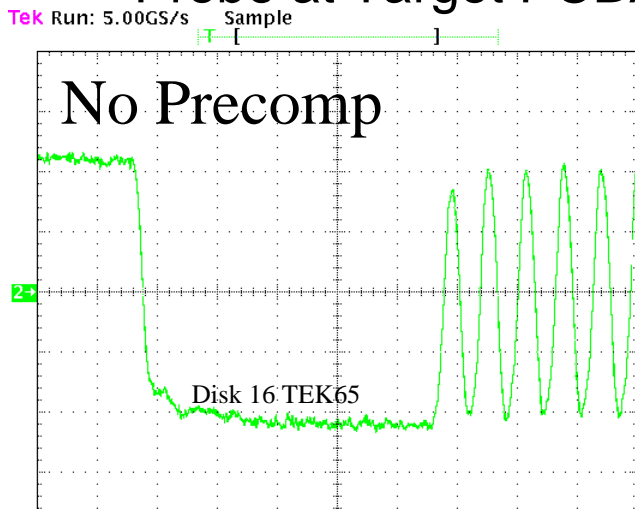


27 Oct 2000
14:51:33

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16:06:18

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Probe at Target PCBA, Seagate U320 Initiator Driving

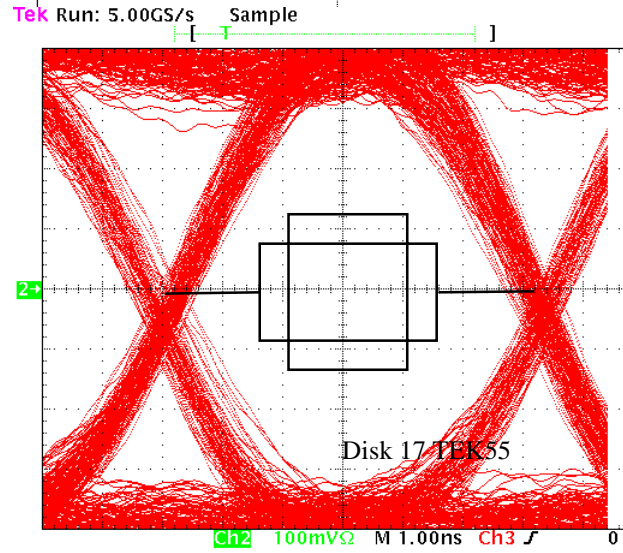
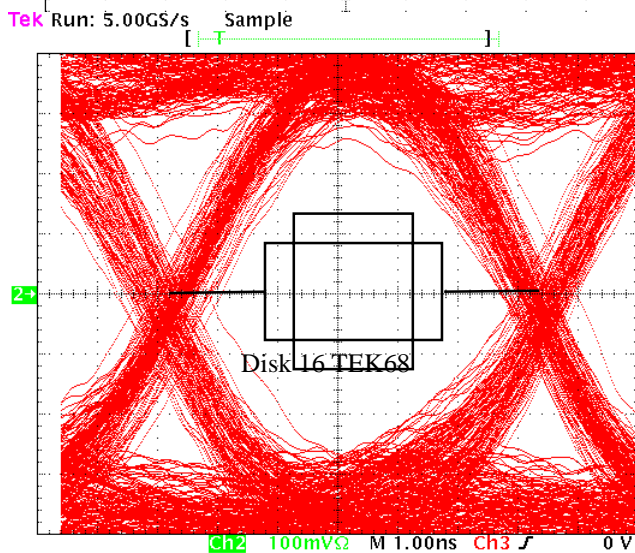
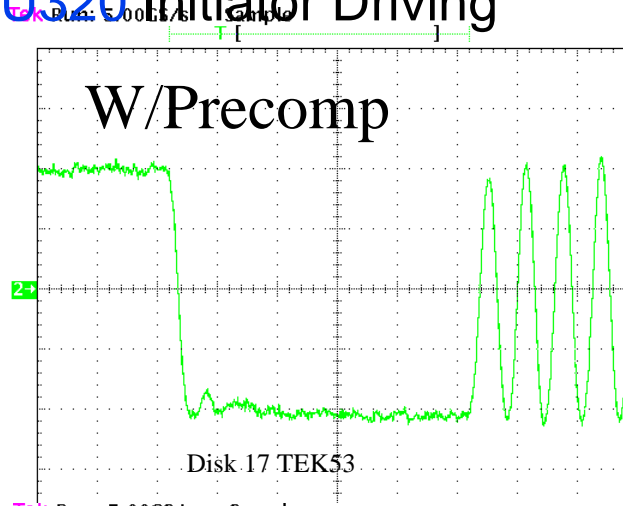
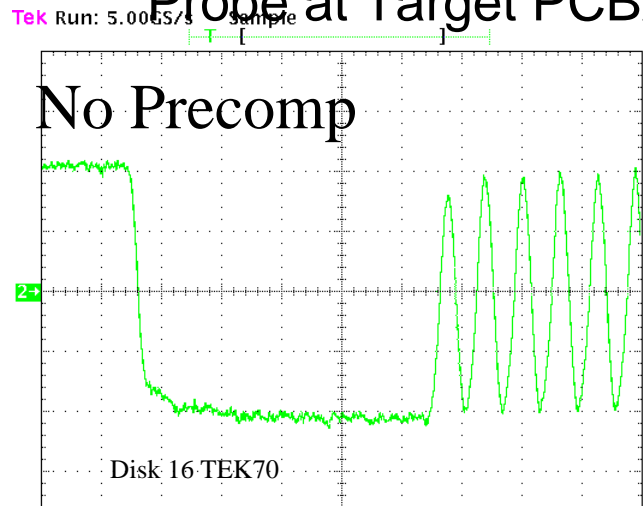


Ch2 100mV Ω M 1.00ns Ch3 \int 0V 27 Oct 2000 14:41:22

Ch2 100mV Ω M 1.00ns Ch3 \int 0V 27 Oct 2000 16:00:25

5-Slot Backplane - SU320 Initiator -12m Rnd , Last Slot

Probe at Target PCBA, Seagate U320 Initiator Driving



27 Oct 2000
14:44:34

27 Oct 2000
15:56:51

SCSI Signal, Backplane & Cable Analysis

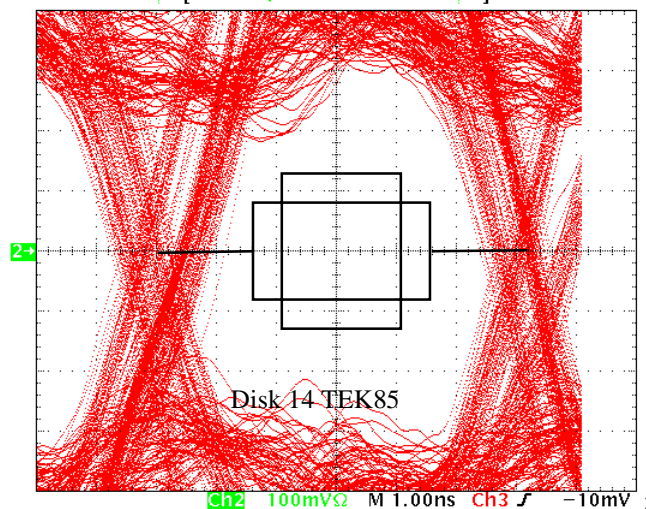
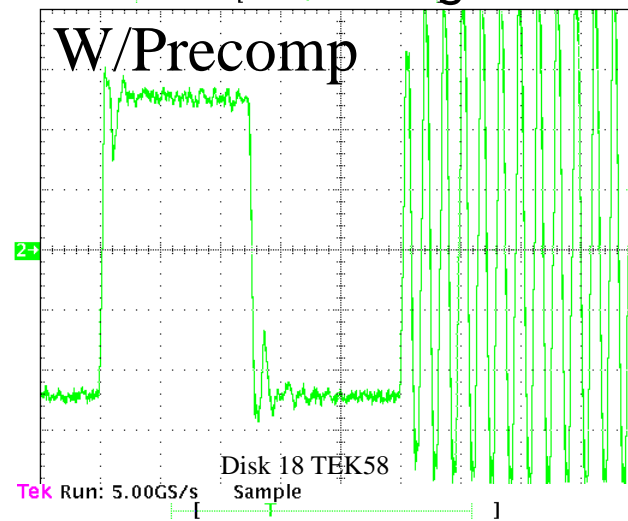
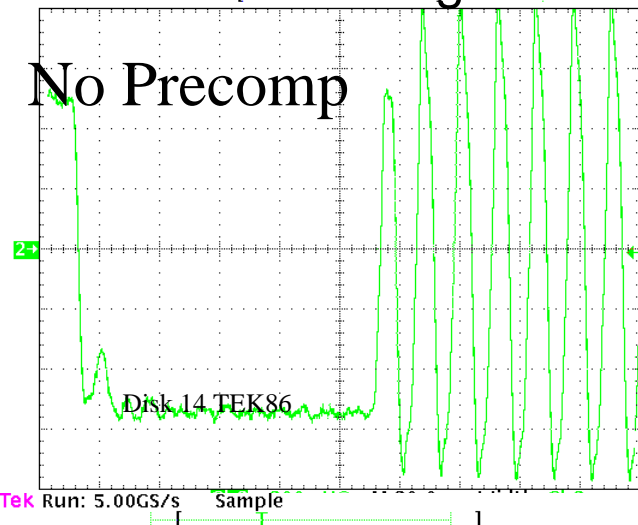
6-Slot Backplane "A"

U320 Target and Initiator Silicon

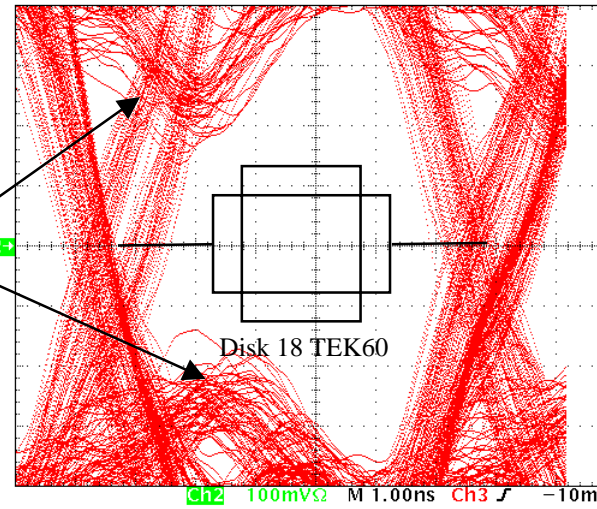
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6-Slot Backplane-A - SU320 Initiator -28" TnF, First Slot

Tek Run: 5.00GS/s Sample
Probe at Target PCBA, Seagate U320 Initiator Driving



Note: these are non-transitioning signals, and also appear in U160 eyes.

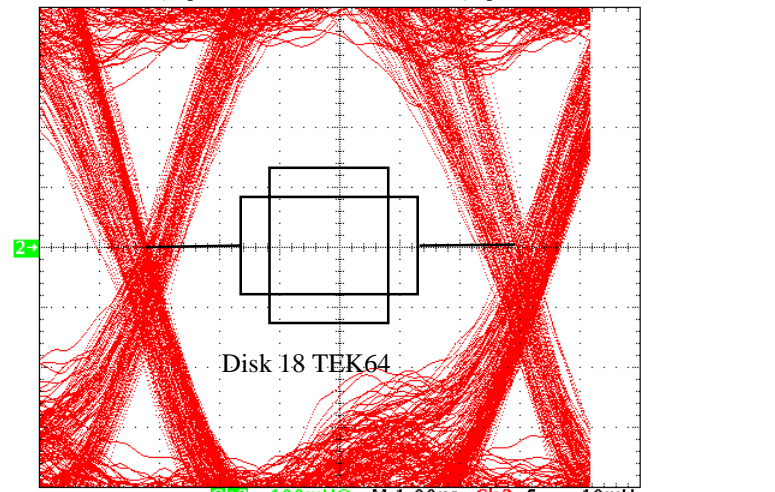
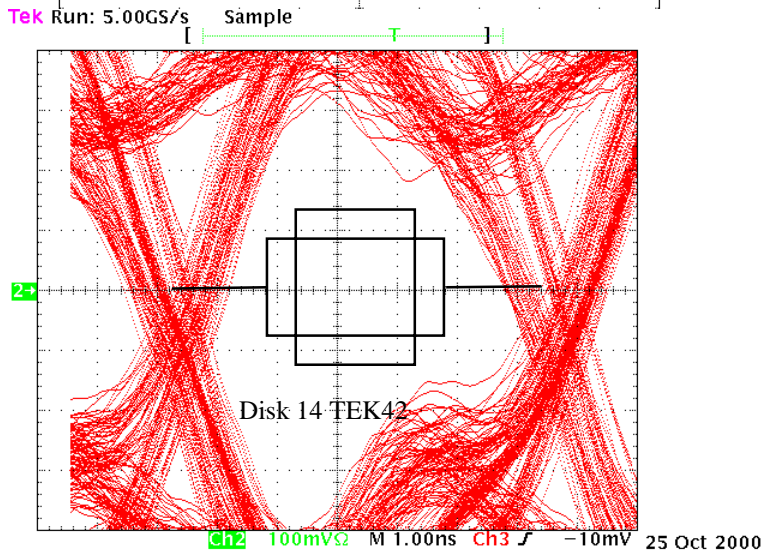
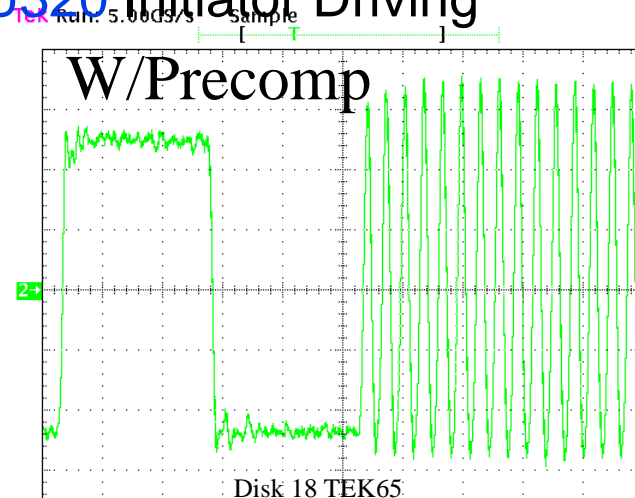
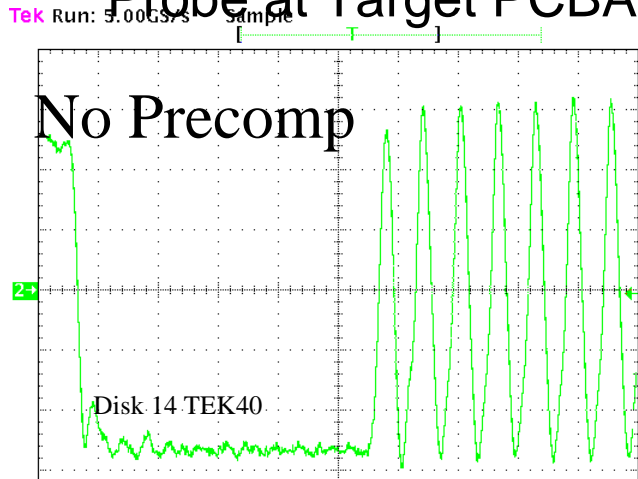


25 Oct 2000
15:47:12

27 Oct 2000
16:32:07

6-Slot Backplane-A - SU320 Initiator -28" TnF, Middle Slot

Probe at Target PCBA, Seagate U320 Initiator Driving

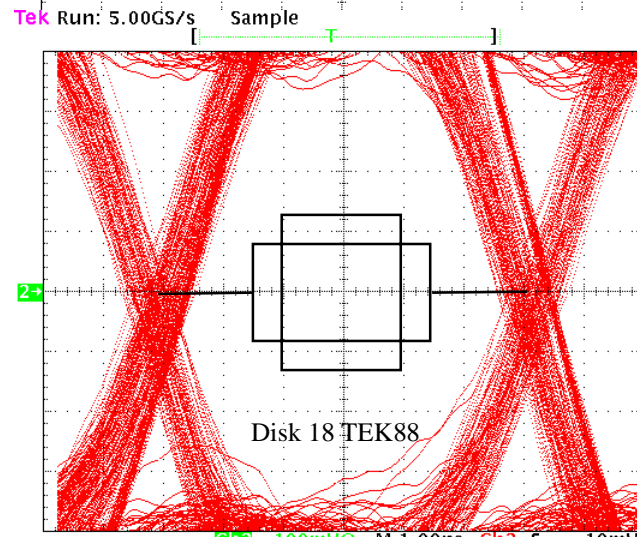
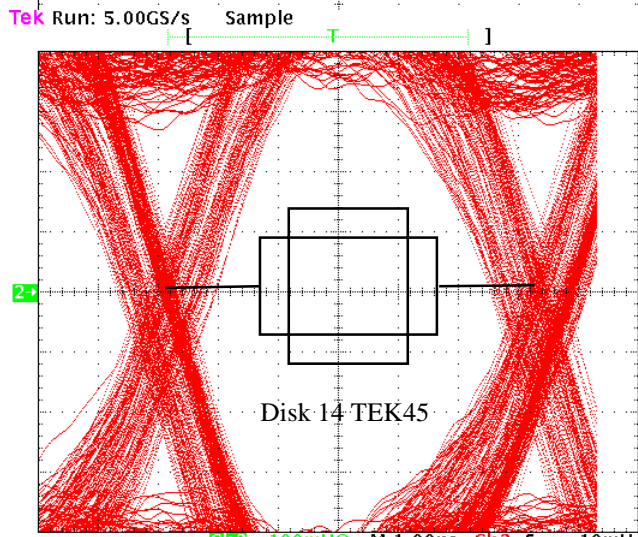
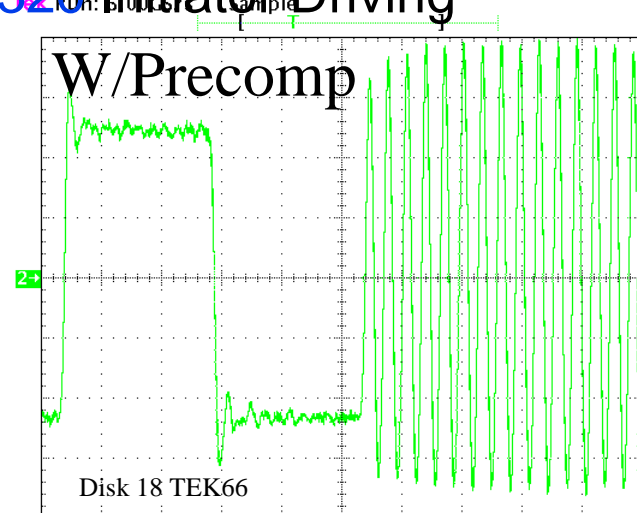
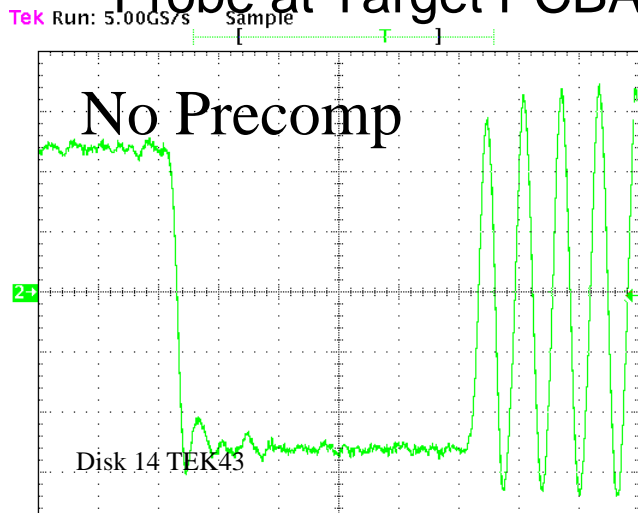


25 Oct 2000
16:00:24

27 Oct 2000
16:39:55

6-Slot Backplane-A - SU320 Initiator -28" TnF, Last Slot

Probe at Target PCBA, Seagate U320 Initiator Driving



Ch2 100mV Ω M 1.00ns Ch3 J -10mV 25 Oct 2000 16:07:56

Ch2 100mV Ω M 1.00ns Ch3 J -10mV 27 Oct 2000 16:46:14

6-Slot Backplane-A - SU320 Initiator -12m Rnd

Probe at Target PCBA, [Seagate U320](#) Initiator Driving

Sorry, could not take these pictures because special connectors used on this backplane and no 12m Cable available.

SCSI Signal, Backplane & Cable Analysis

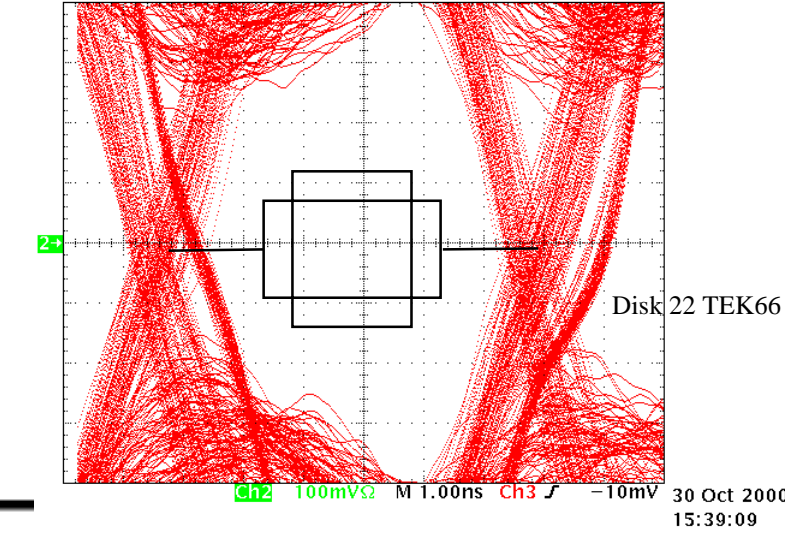
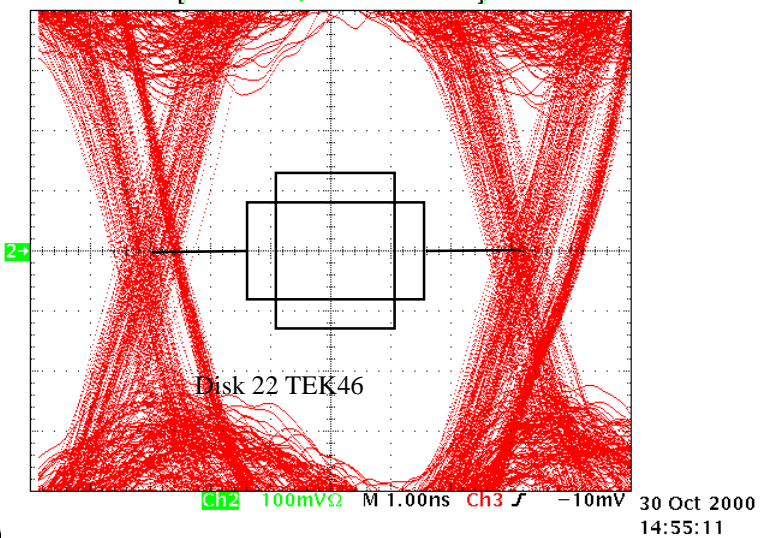
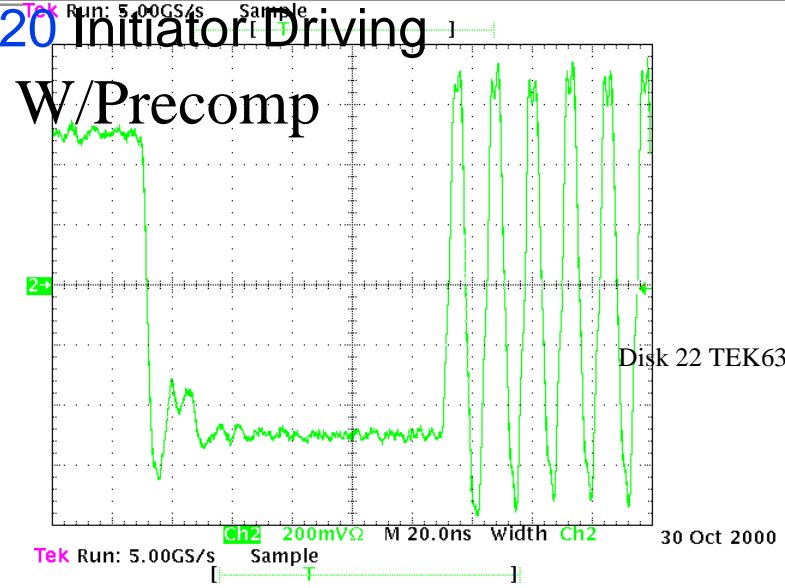
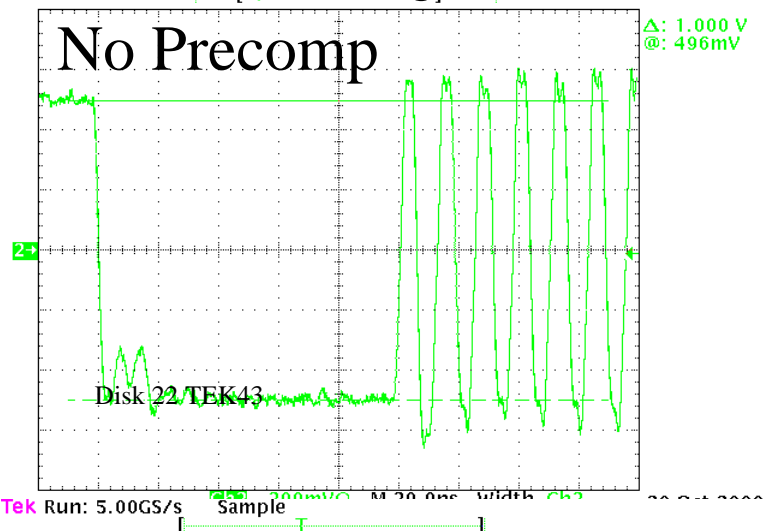
6-Slot Backplane "B"

U320 Target and Initiator Silicon

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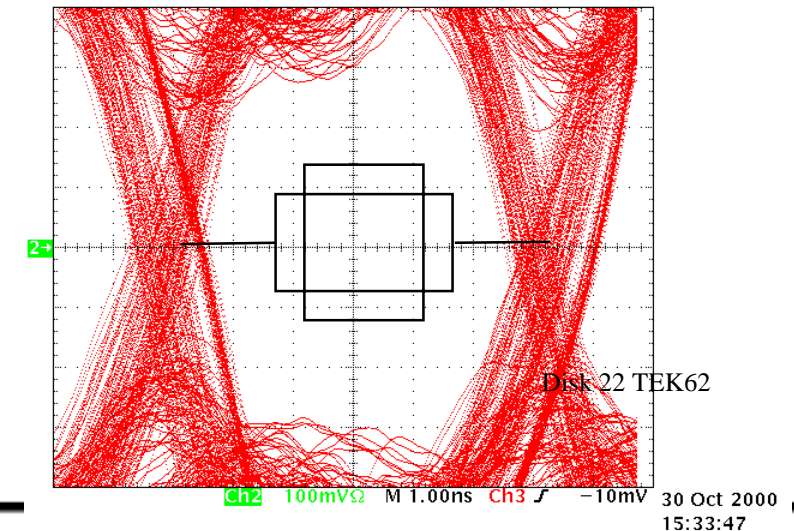
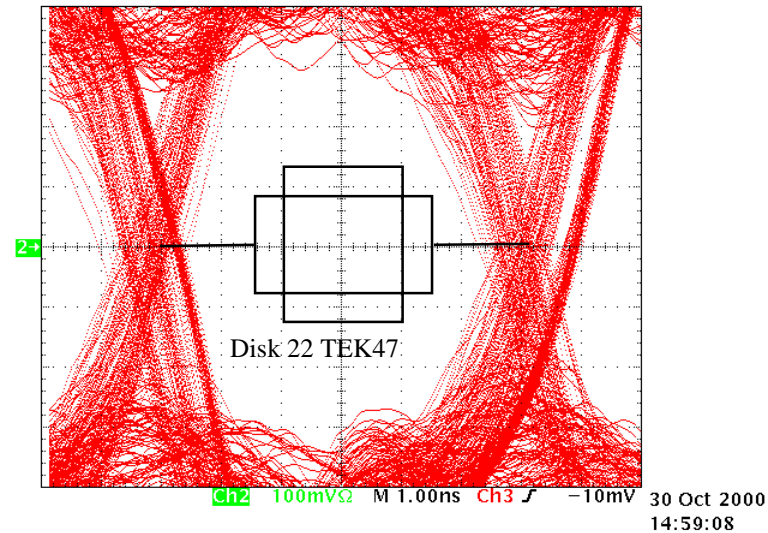
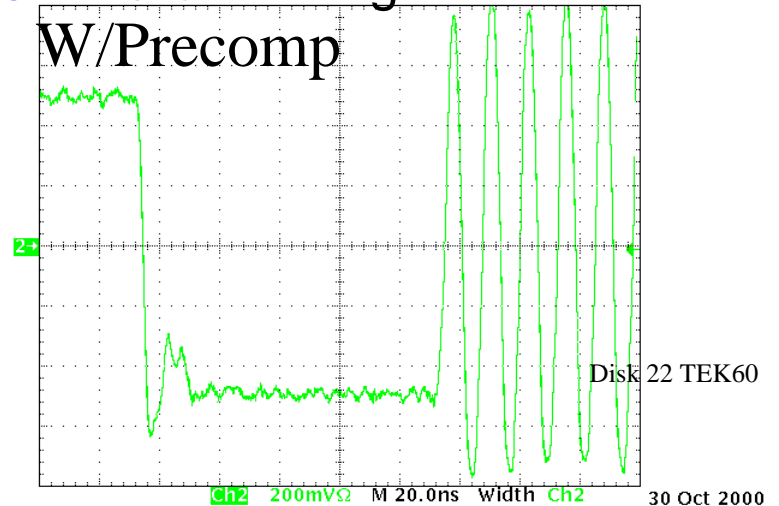
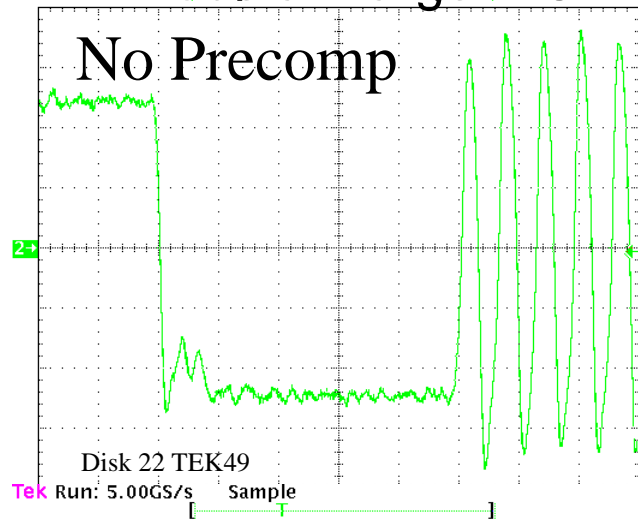
6-Slot Backplane-B - SU320 Initiator -28" TnF, First Slot

Tek Run: 5.00GS/s Sample
Probe at Target PCBA, Seagate U320 Initiator Driving



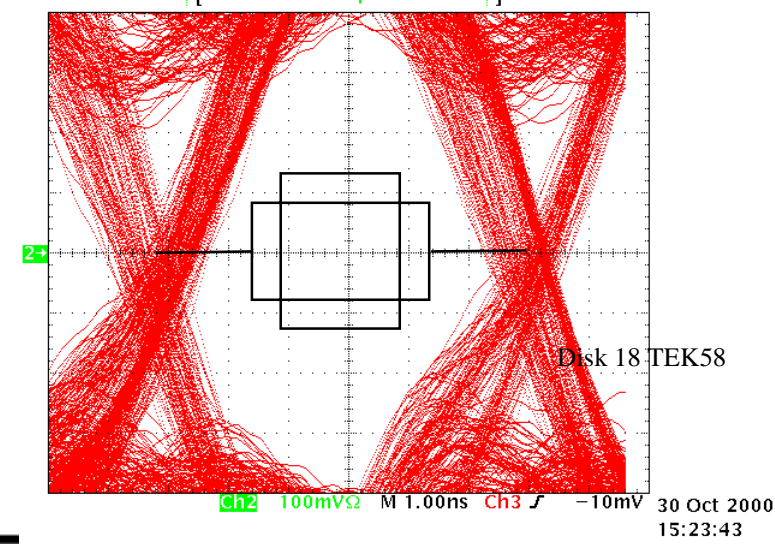
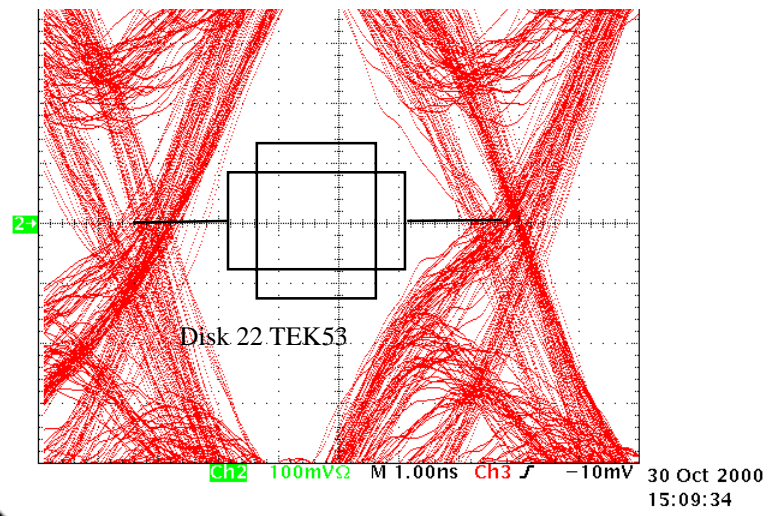
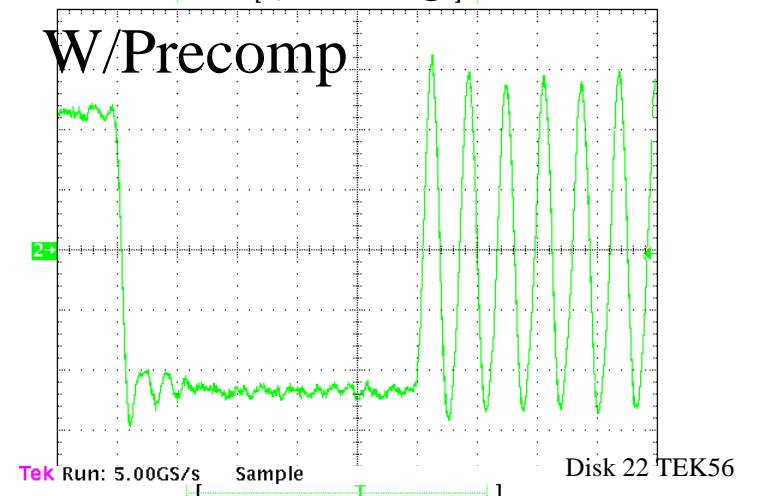
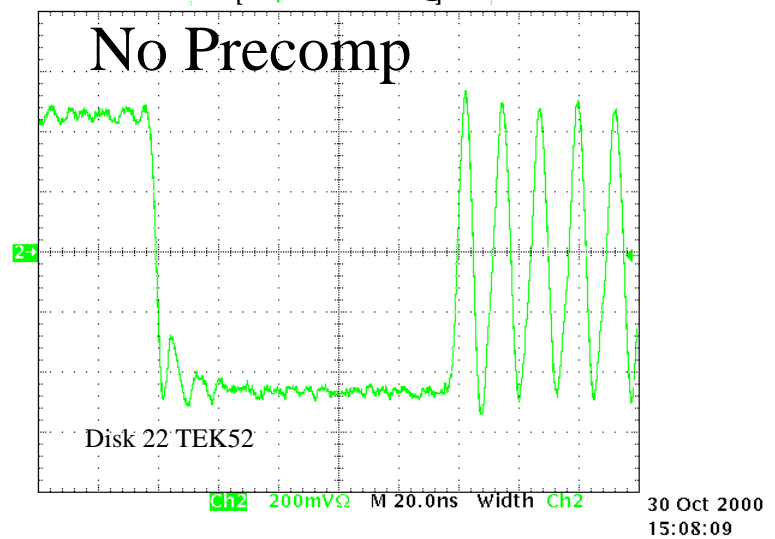
6-Slot Backplane-B - SU320 Initiator -28" TnF, Middle Slot

Tek Run: 5.00GS/s Sample Probe at Target PCBA, Seagate U320 Initiator Driving



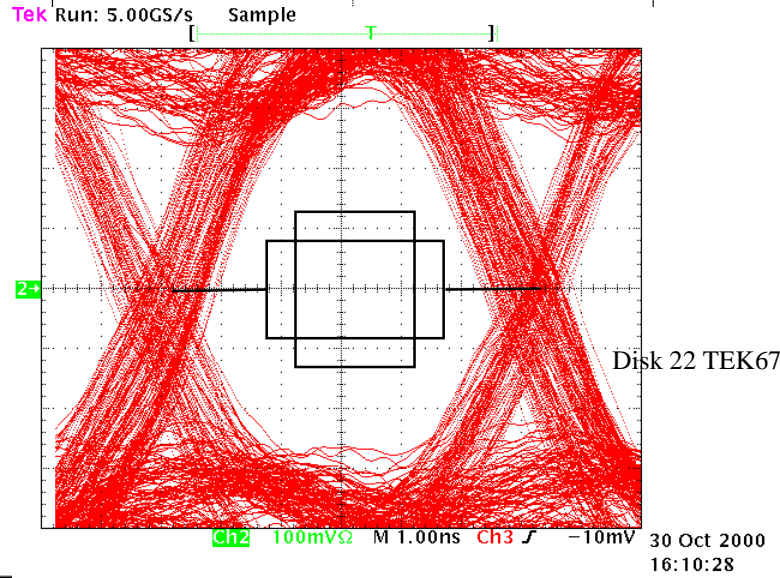
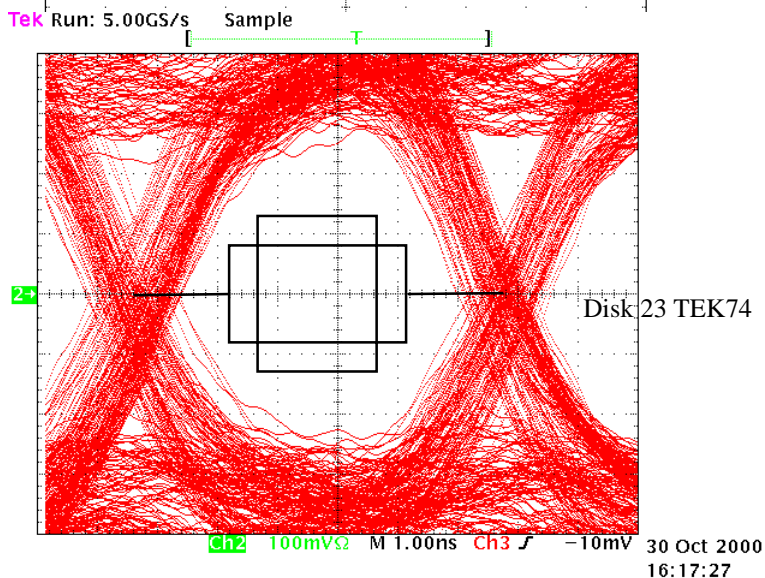
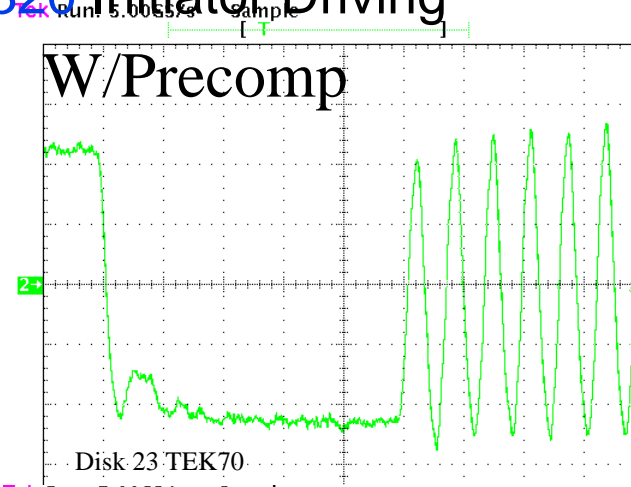
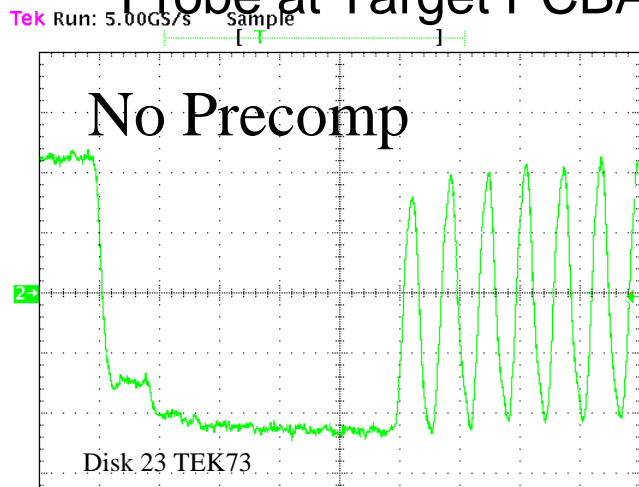
6-Slot Backplane-B - SU320 Initiator -28" TnF, Last Slot

Tek Run: 5.00GS/s Sample Probe at Target PCBA, Seagate U320 Initiator Driving



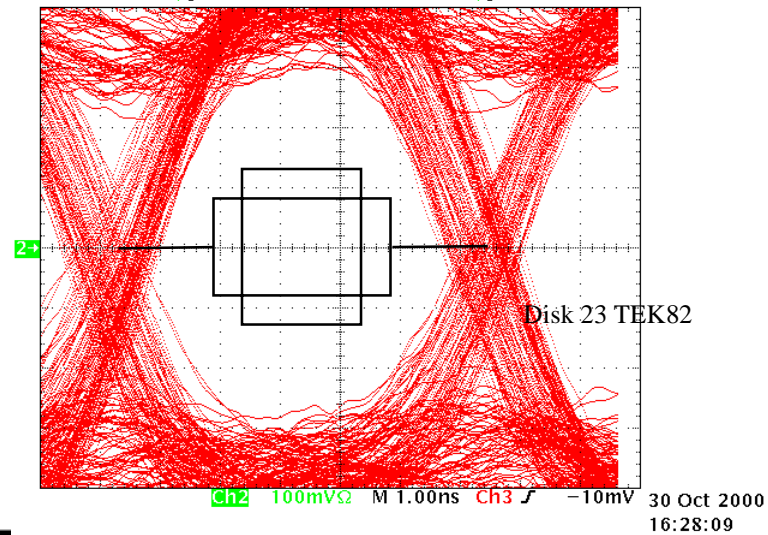
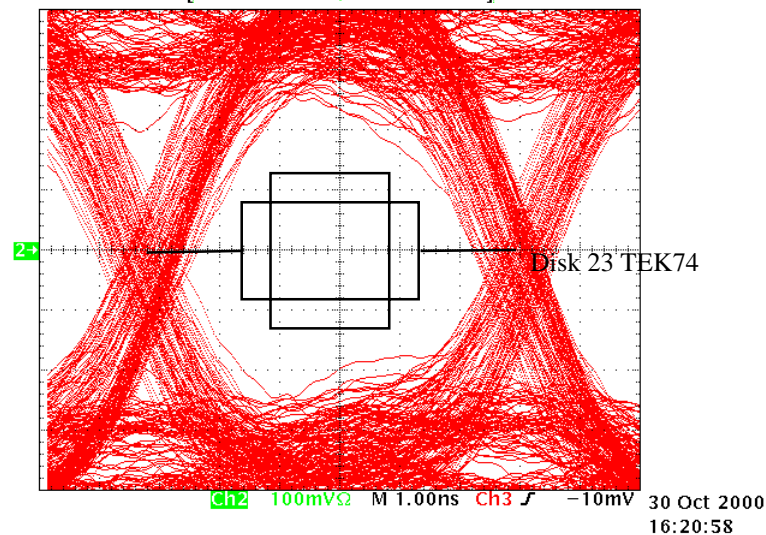
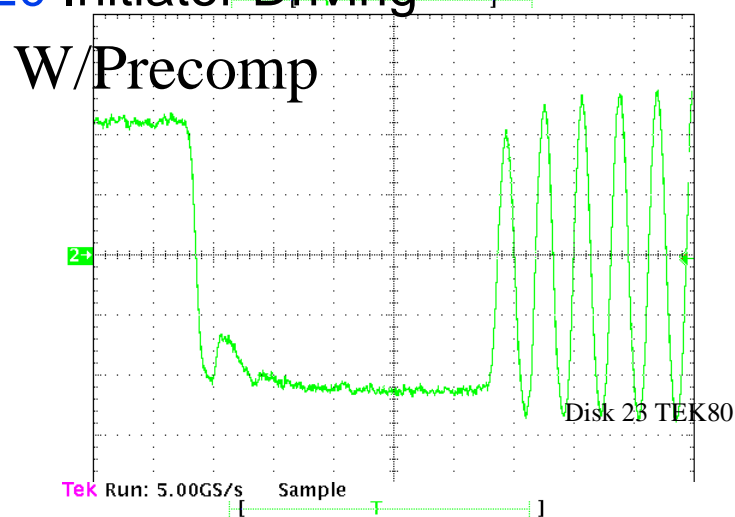
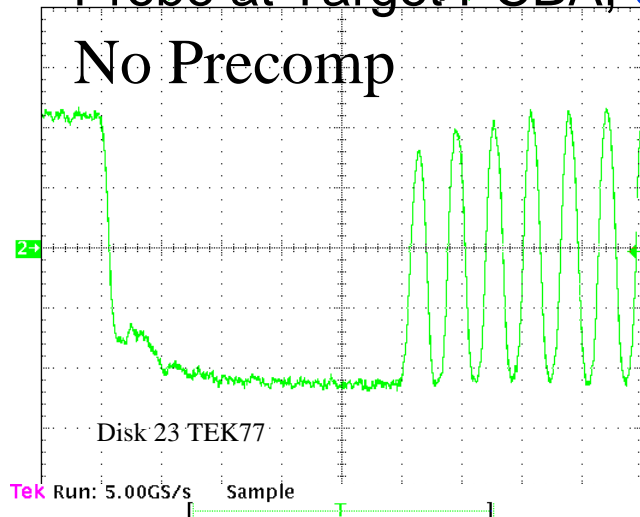
6-Slot Backplane-B - SU320 Initiator - 12m Rnd, First Slot

Probe at Target PCBA, Seagate U320 Initiator Driving



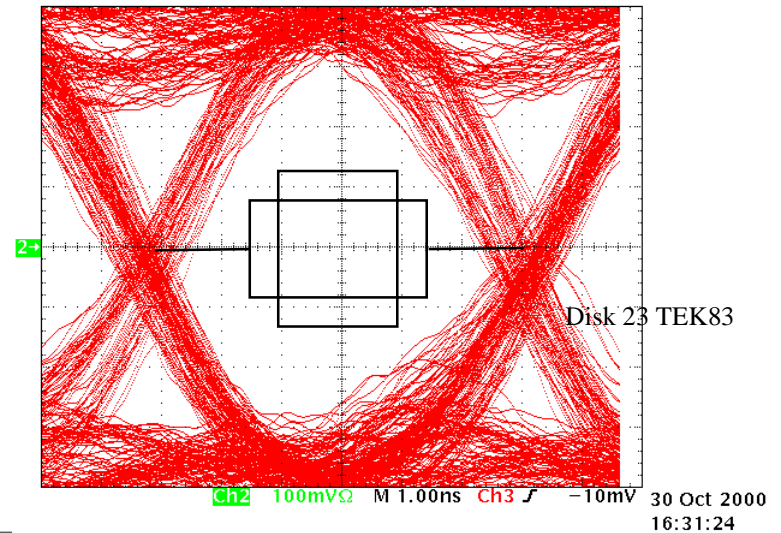
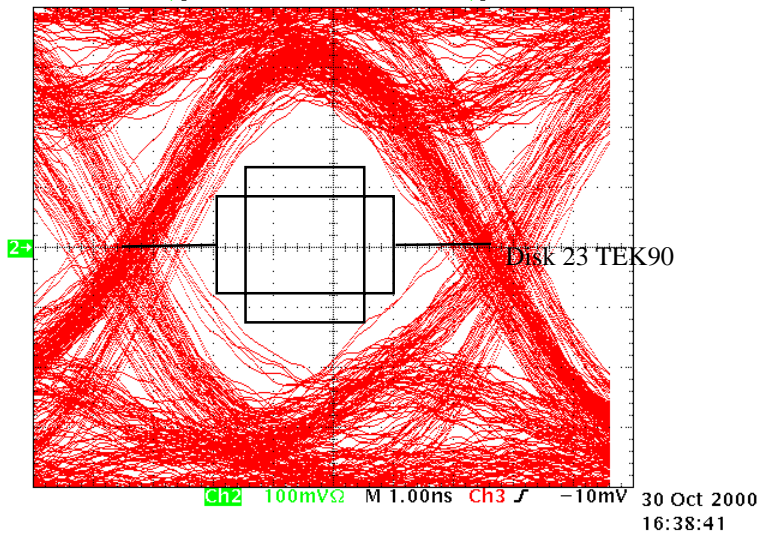
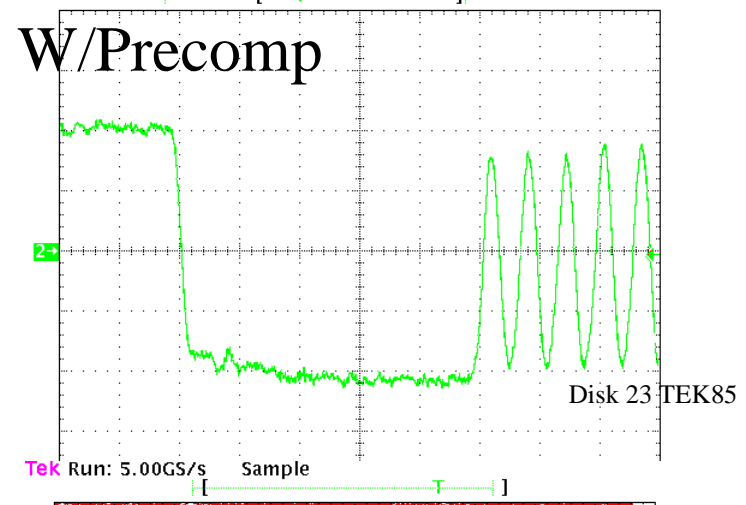
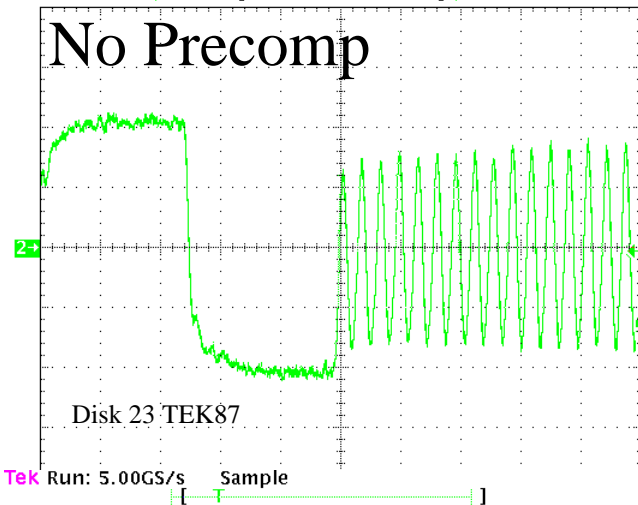
6-Slot Backplane-B - SU320 Initiator - 12m Rnd, Middle Slot

Tek Run: 5.00GS/s Sample Initiator: SU320
Probe at Target PCBA, Seagate U320 Initiator Driving



6-Slot Backplane-B - SU320 Initiator - 12m Rnd, Last Slot

Tek Run: 5.00GS/s Sample Probe at Target PCBA, Seagate U320 Initiator Driving



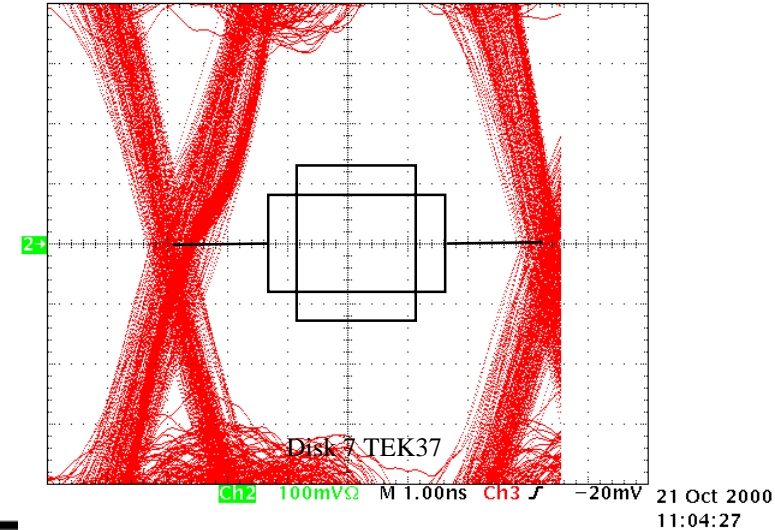
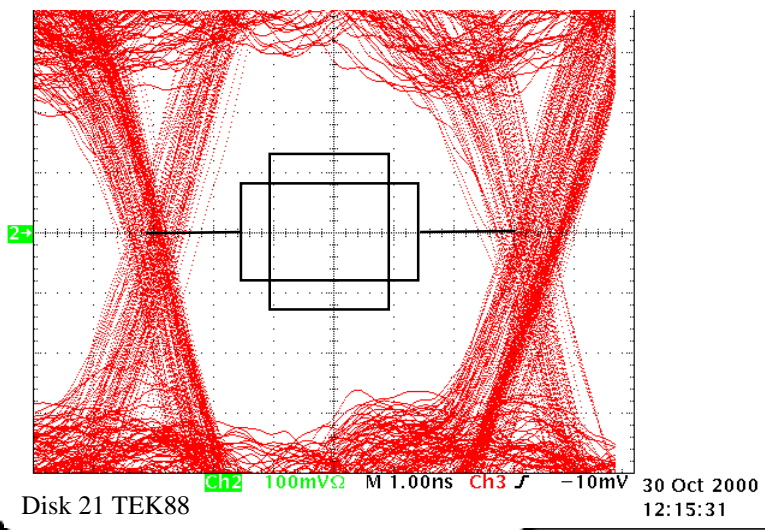
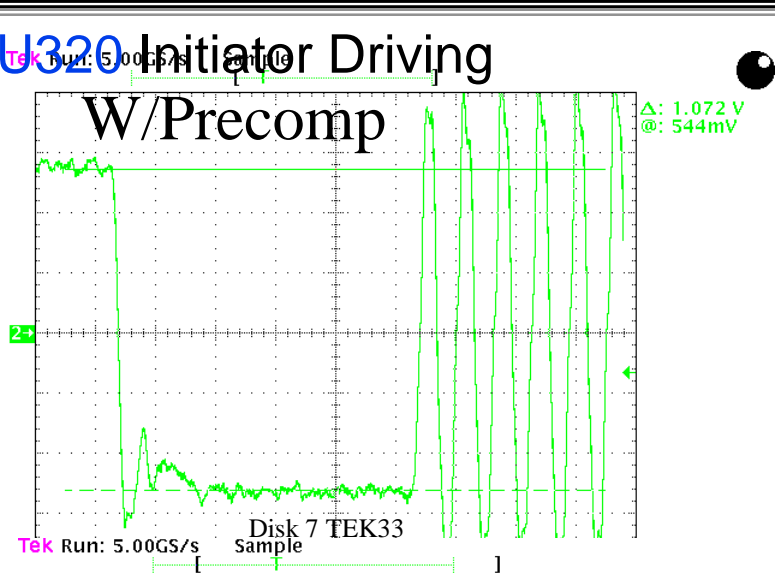
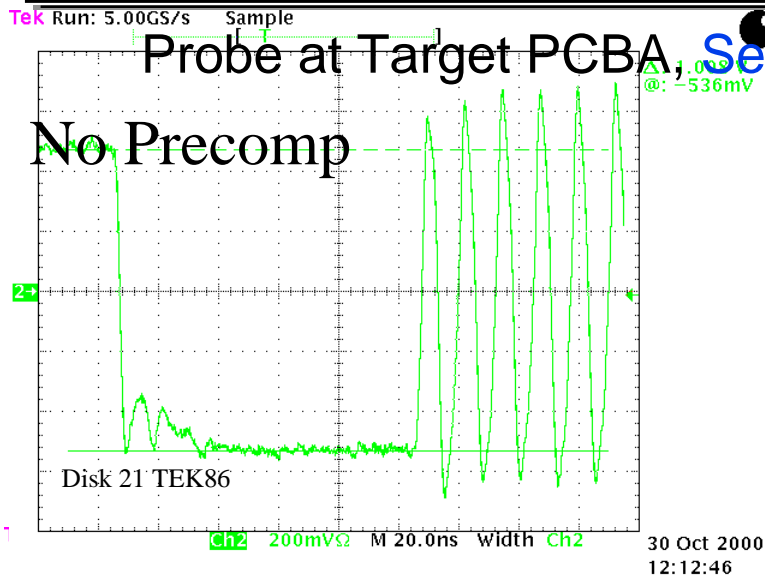
SCSI Signal, Backplane & Cable Analysis

10-Slot Backplane

U320 Target and Initiator Silicon

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VLSI Controller Development
Scotts Valley California

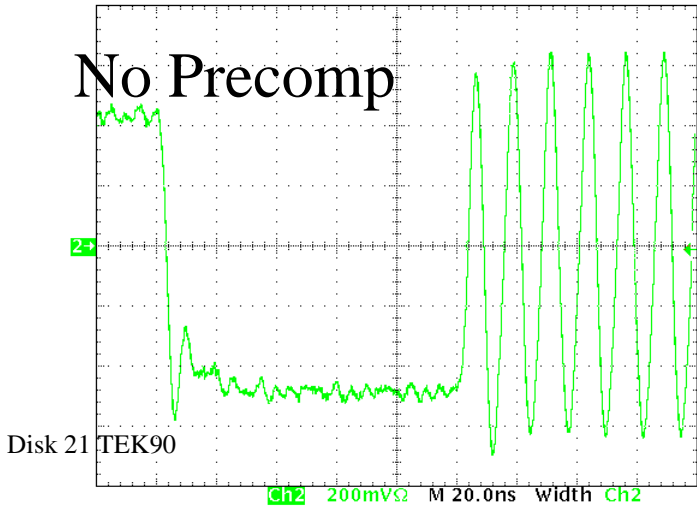
10 - Slot Backplane - SU320 Initiator -28" TnF, First Slot



10 - Slot Backplane - SU320 Initiator -28" TnF, Middle Slot

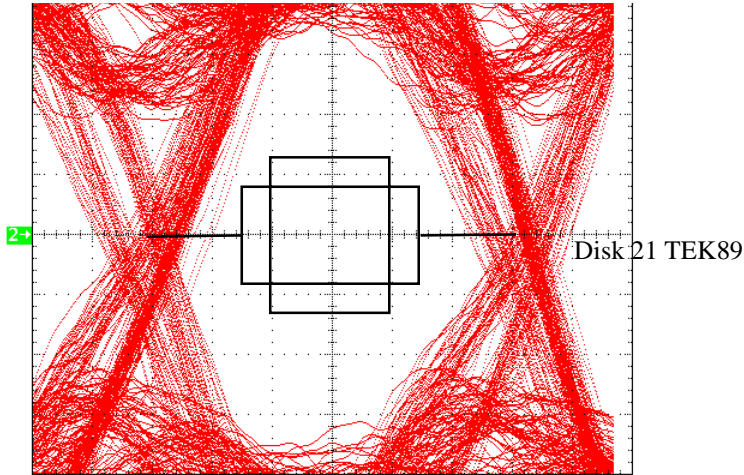
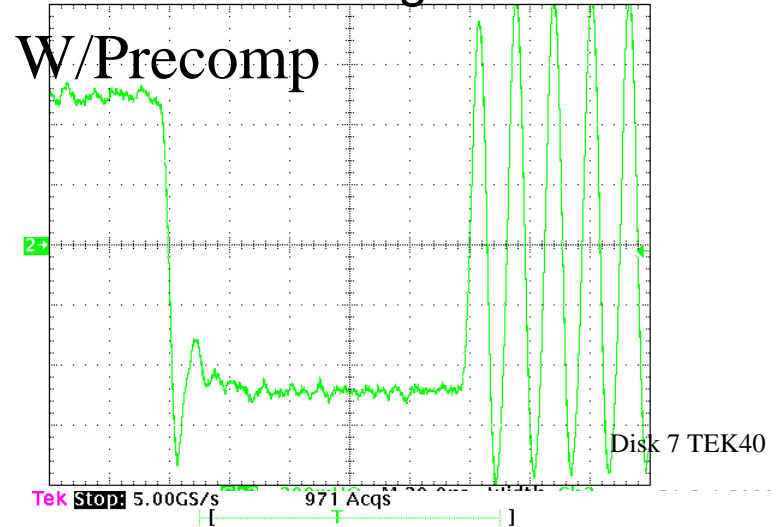
Tek Run 5.00GS/s [Sample] 51 Run 5.00GS/s [Sample]
 Probe at Target PCBA, Seagate U320 Initiator Driving

No Precomp

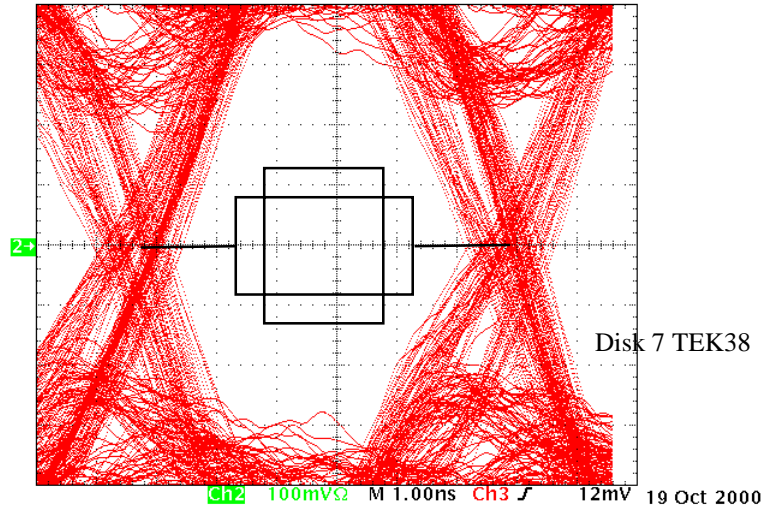


30 Oct 2000
12:21:18

W/Precomp



30 Oct 2000
12:19:29



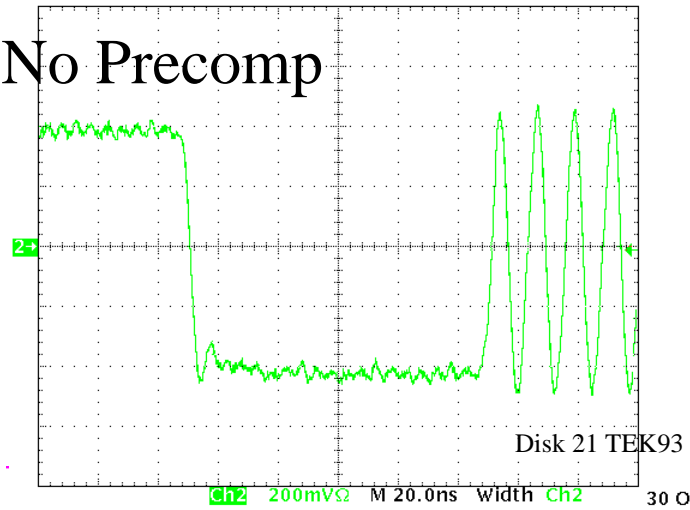
19 Oct 2000
11:02:22

10 - Slot Backplane - SU320 Initiator -28" TnF, Last Slot

Tek Run: 5.00GS/s Sample

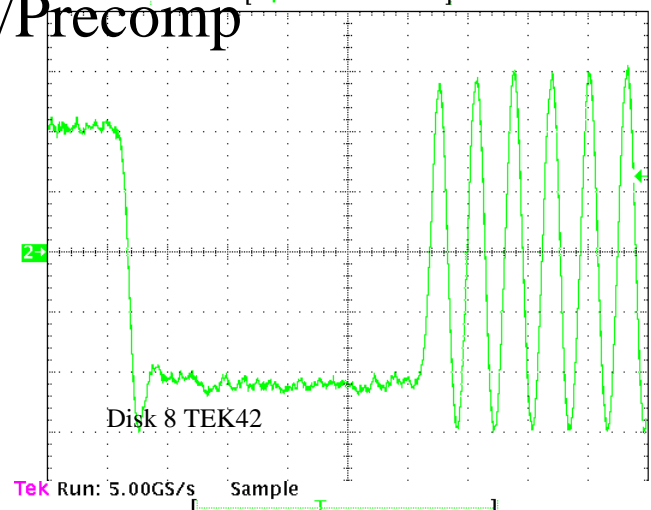
Probe at Target PCBA, Seagate U320 Initiator Driving

No Precomp

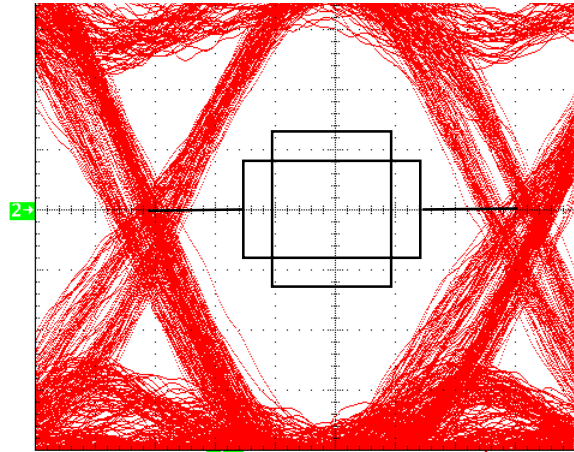


30 Oct 2000
12:27:20

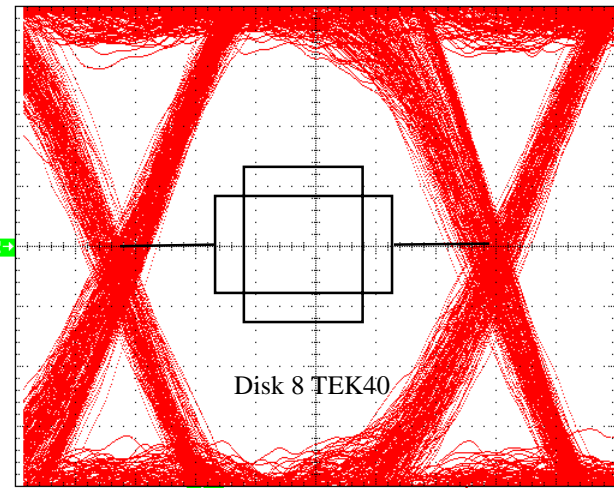
W/Precomp



Tek Run: 5.00GS/s Sample



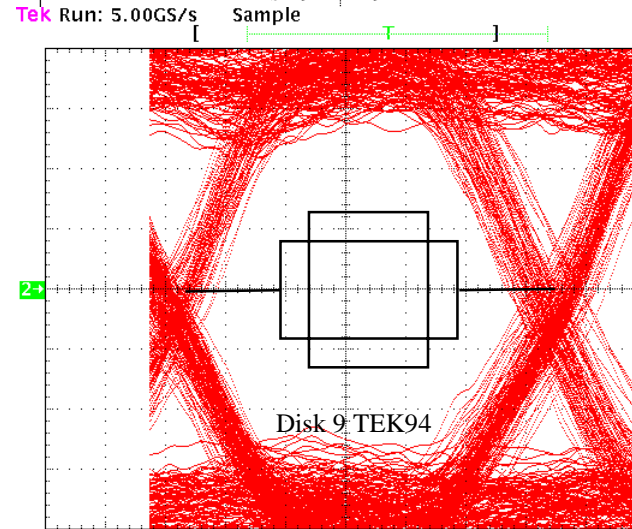
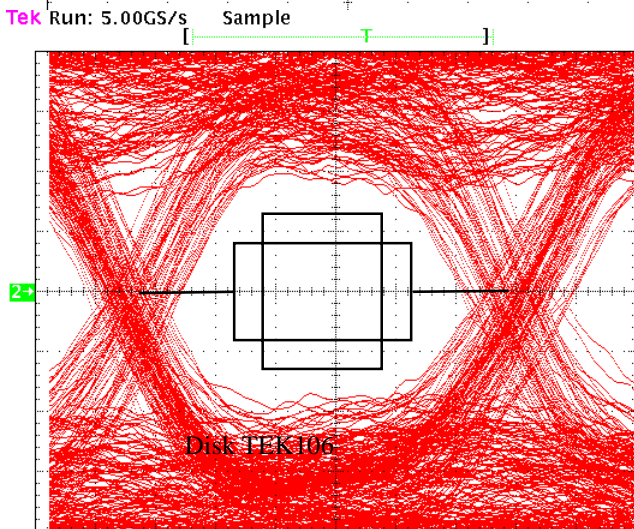
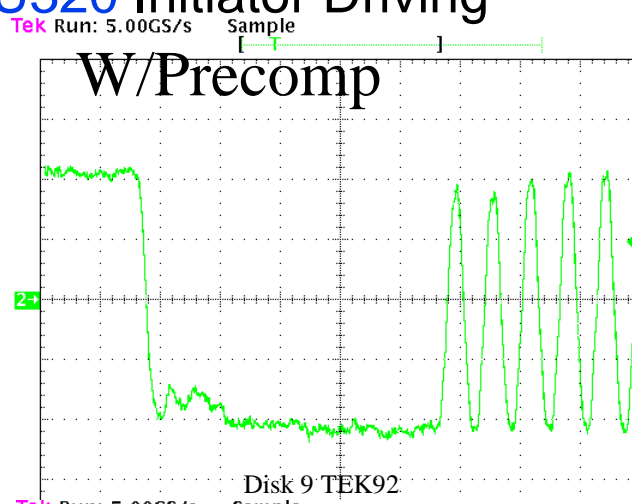
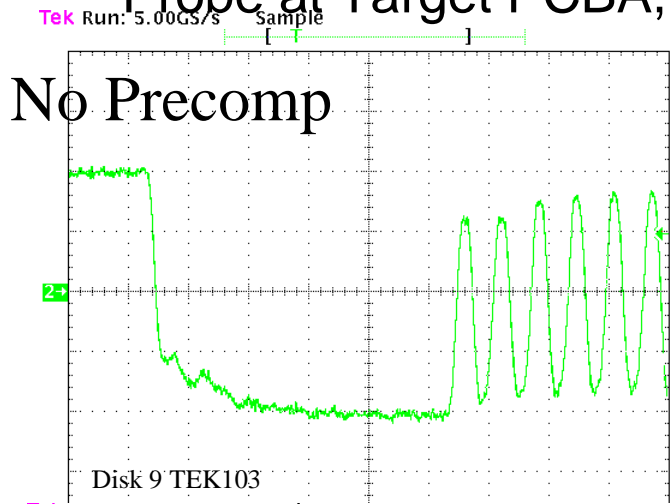
30 Oct 2000
12:28:40



21 Oct 2000
11:41:14

10 - Slot Backplane - SU320 Initiator - 12m Rnd , First Slot

Probe at Target PCBA, Seagate U320 Initiator Driving

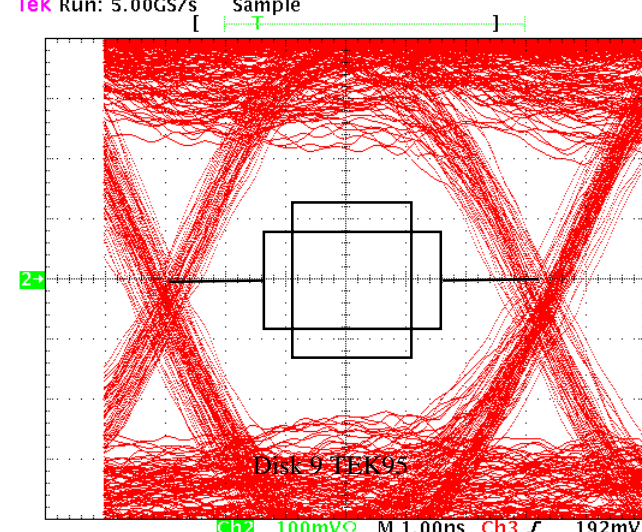
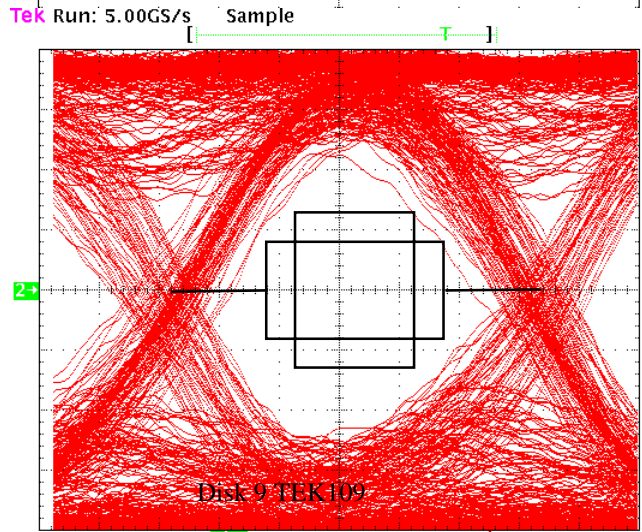
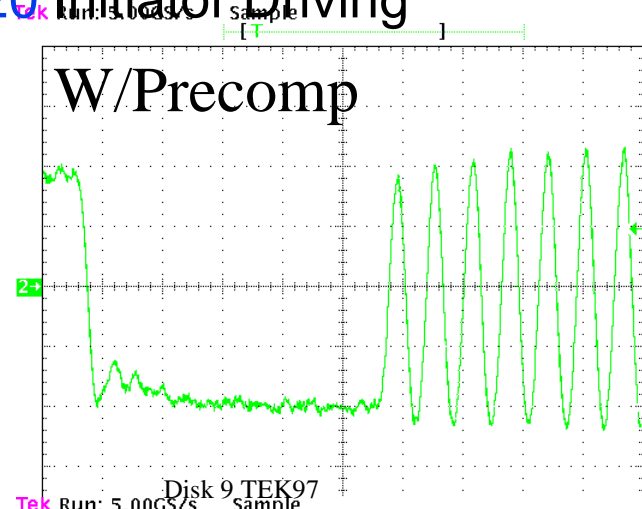
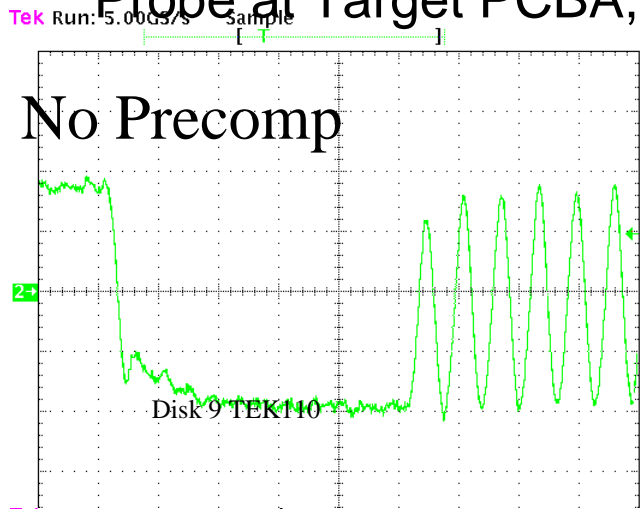


Ch2 100mVΩ M 1.00ns Ch3 192mV 23 Oct 2000 13:43:20

Ch2 100mVΩ M 1.00ns Ch3 192mV 23 Oct 2000 13:11:15

10 - Slot Backplane - SU320 Initiator - 12m Rnd , Middle Slot

Probe at Target PCBA, Seagate U320 Initiator Driving



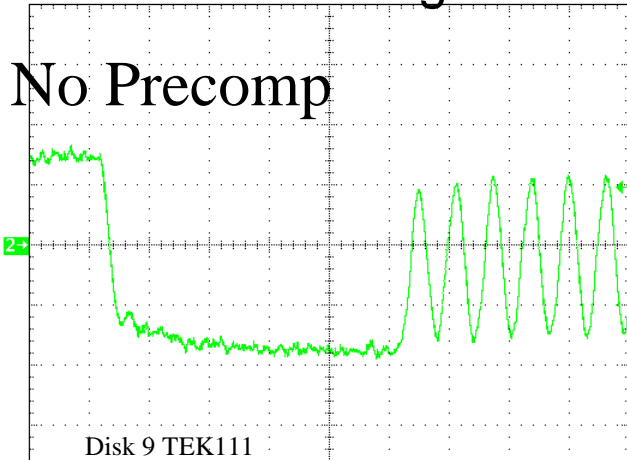
Ch2 100mVΩ M 1.00ns Ch3 192mV 23 Oct 2000 13:48:37

Ch2 100mVΩ M 1.00ns Ch3 192mV 23 Oct 2000 13:17:28

10 - Slot Backplane - SU320 Initiator - 12m Rnd , Last Slot

Tek Run: 5.00GS/s Sample Probe at Target PCBA, Seagate U320 Initiator Driving

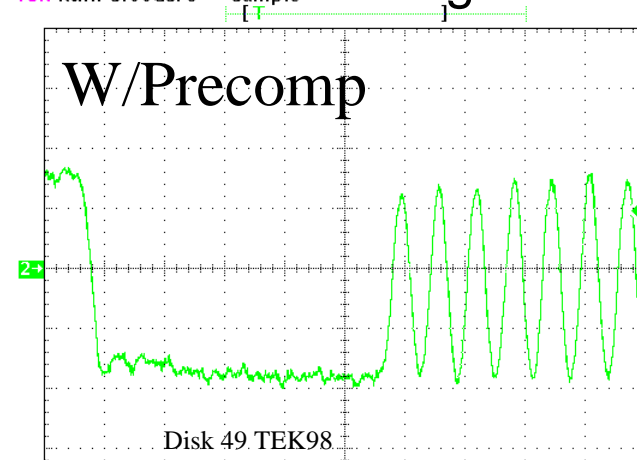
No Precomp



Disk 9 TEK111

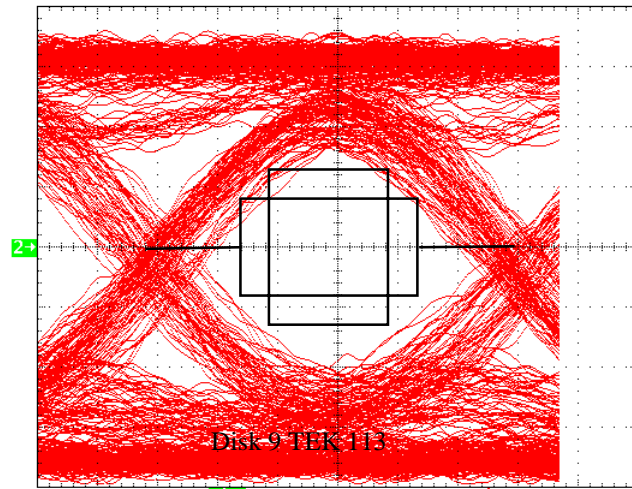
Tek Run: 5.00GS/s Sample

W/Precomp



Disk 49 TEK98

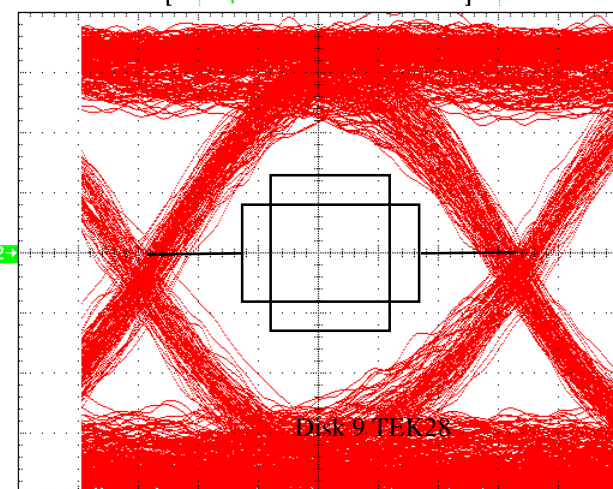
Tek Run: 5.00GS/s Sample



Disk 9 TEK 113

Ch2 100mV Ω M 1.00ns Ch3 192mV

23 Oct 2000
13:54:39



Disk 9 TEK28

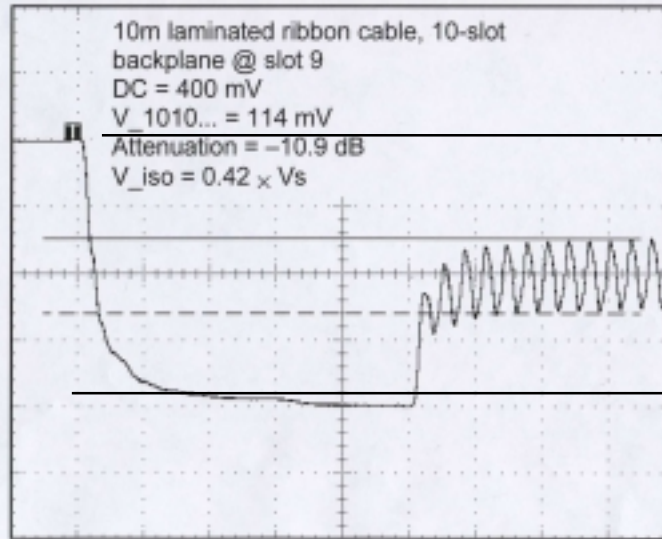
Ch2 100mV Ω M 1.00ns Ch3 192mV

23 Oct 2000
13:23:03

Cable & Backplane Attenuation Compared

Quantum

10m laminated, 10-slot, @ slot 9

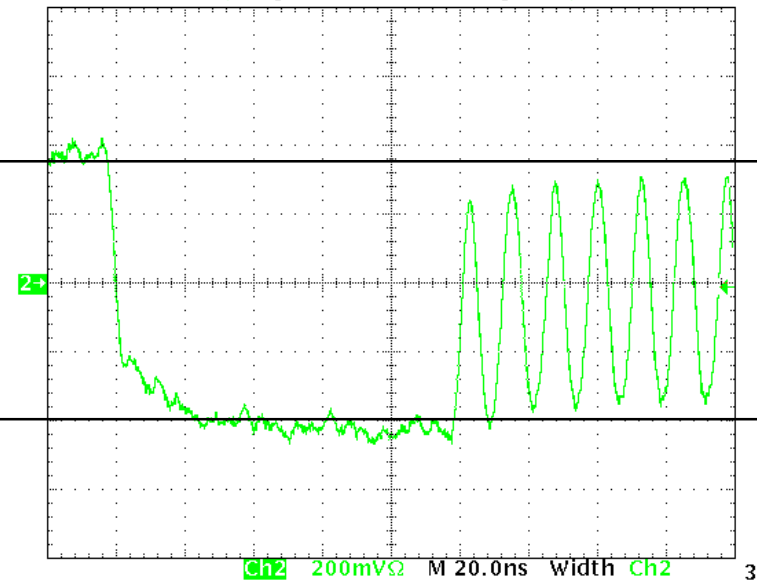


18 Mar 2000

T1808-250-S. Attenuation Data for Ultra320

Richard Liles - Slide 9

Tek Run: 5.00GS/s Sample



30 Oct 2000
12:56:15

Quantum Generator-
Driver Example, 10m

12m Cable, Same Driver
Amplitude, Slot, Backplane,
but Real U320 Silicon

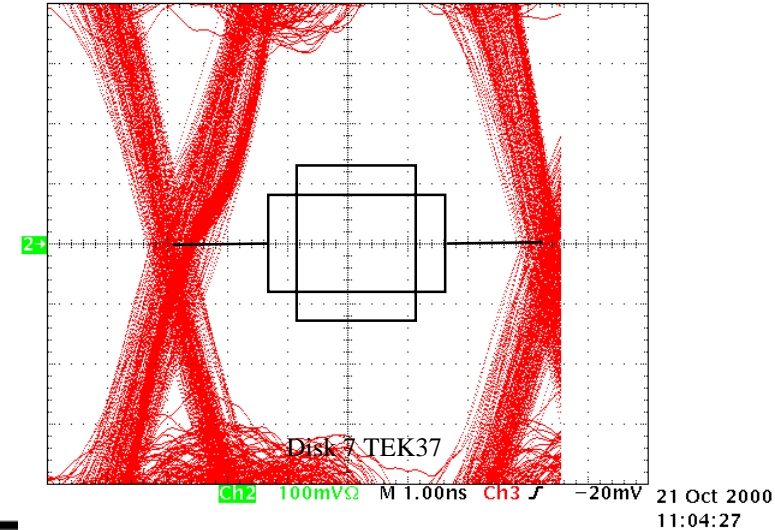
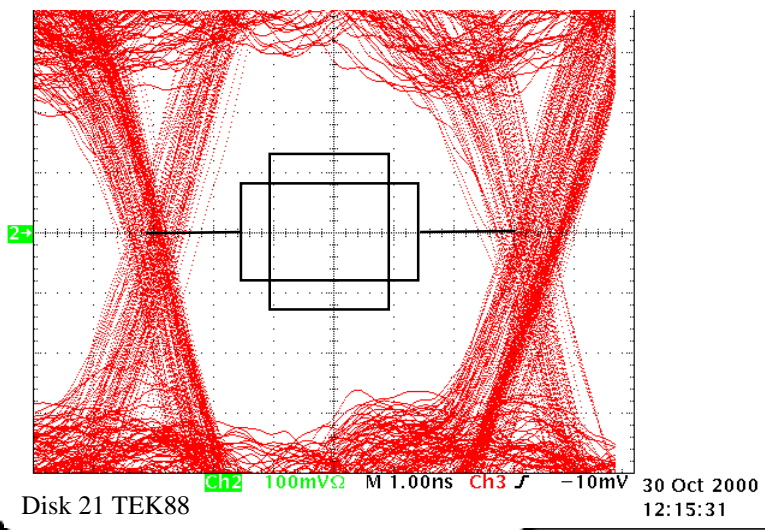
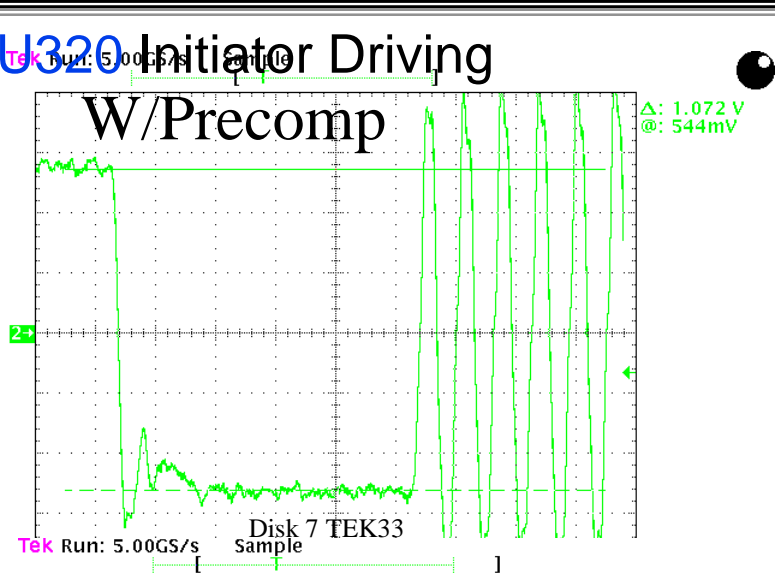
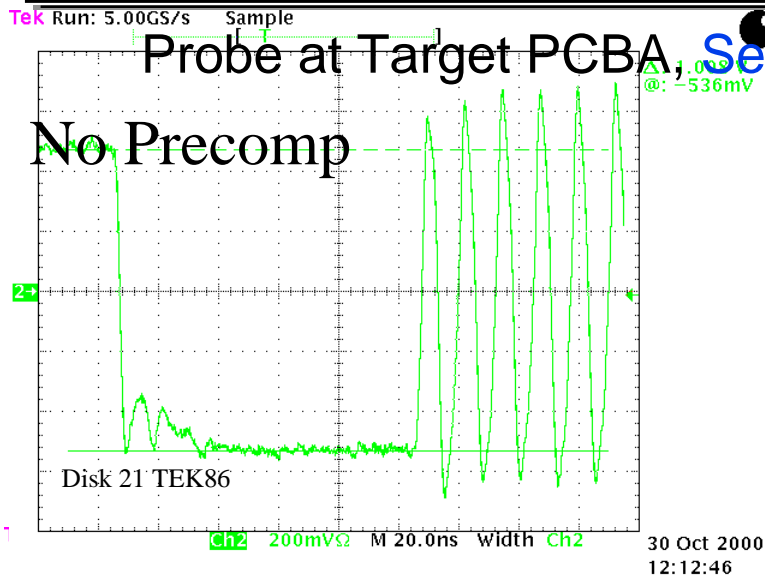
SCSI Signal, Backplane & Cable Analysis

10-Slot Backplane

U320 Target and Initiator Silicon

Seagate Technology
VLSI Controller Development
Scotts Valley California

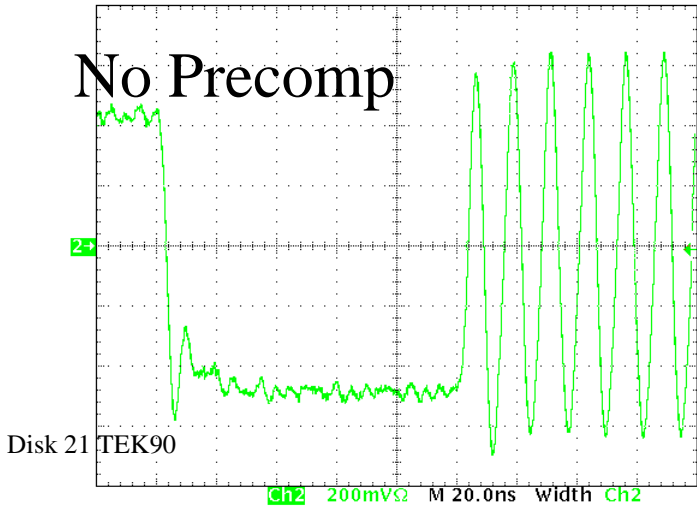
10 - Slot Backplane - SU320 Initiator -28" TnF, First Slot



10 - Slot Backplane - SU320 Initiator -28" TnF, Middle Slot

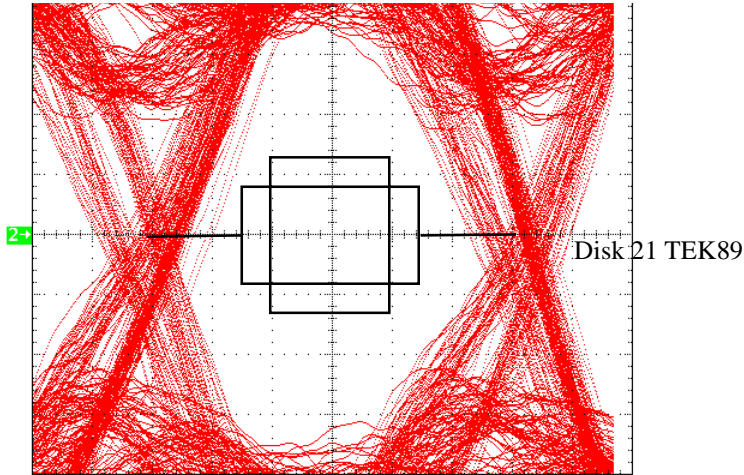
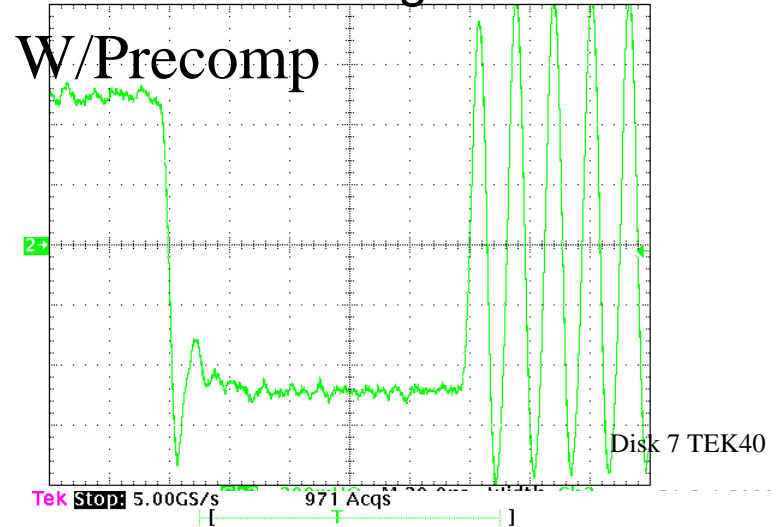
Tek Run 5.00GS/s [Sample] 51 Run 5.00GS/s [Sample]
 Probe at Target PCBA, Seagate U320 Initiator Driving

No Precomp

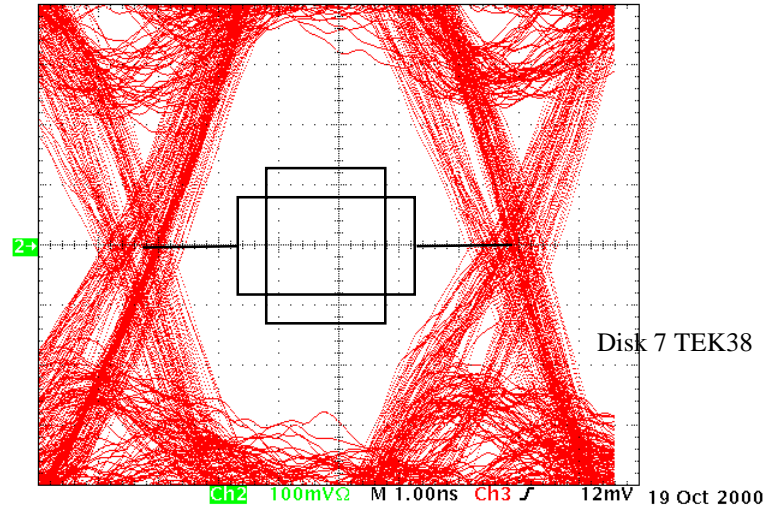


30 Oct 2000
12:21:18

W/Precomp



30 Oct 2000
12:19:29



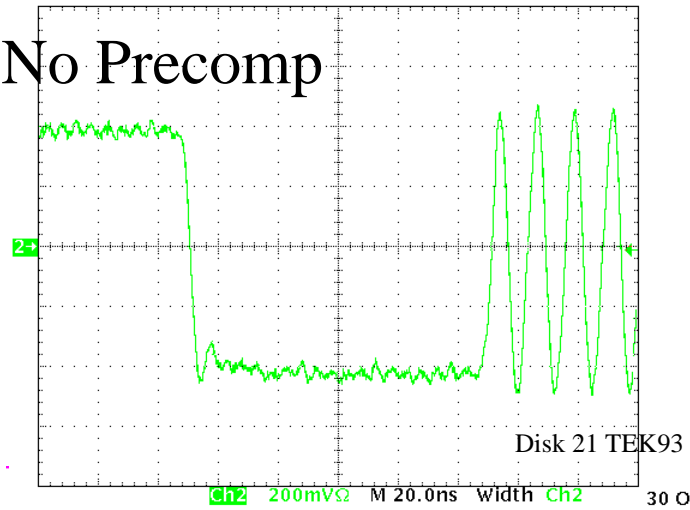
19 Oct 2000
11:02:22

10 - Slot Backplane - SU320 Initiator -28" TnF, Last Slot

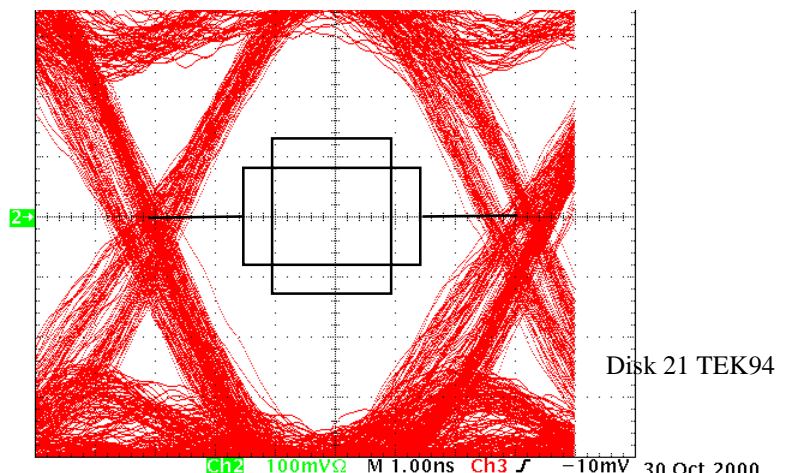
Tek Run: 5.00GS/s Sample

Probe at Target PCBA, Seagate U320 Initiator Driving

No Precomp

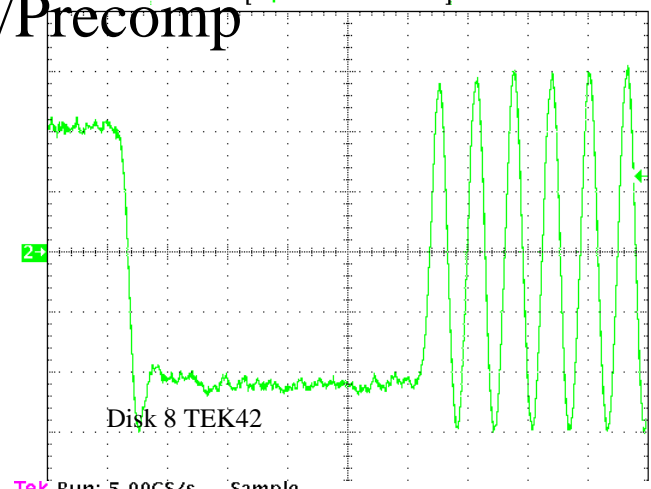


30 Oct 2000
12:27:20

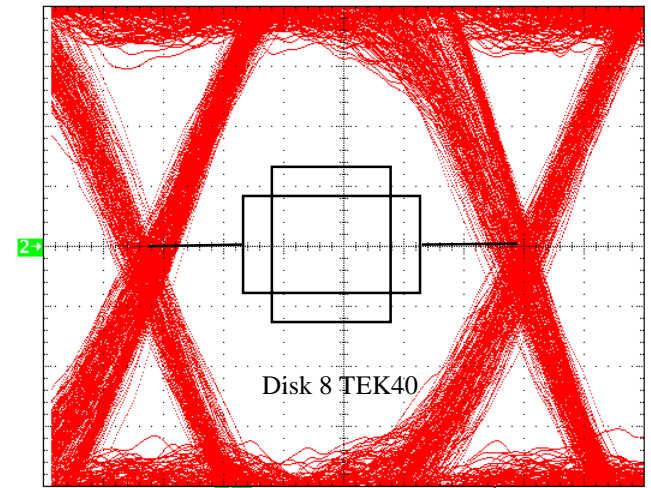


30 Oct 2000
12:28:40

W/Precomp



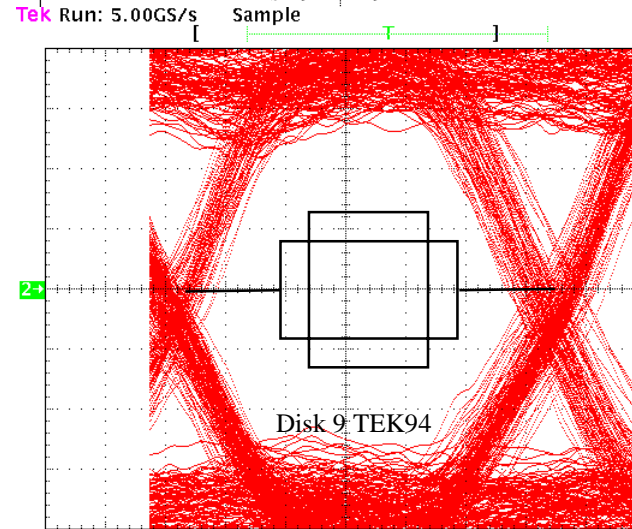
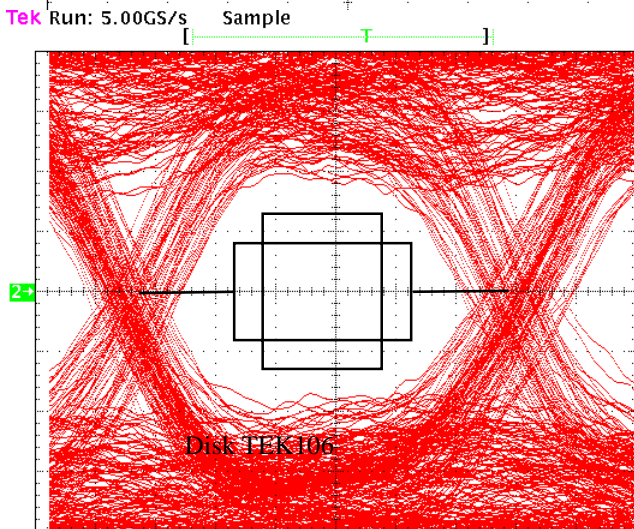
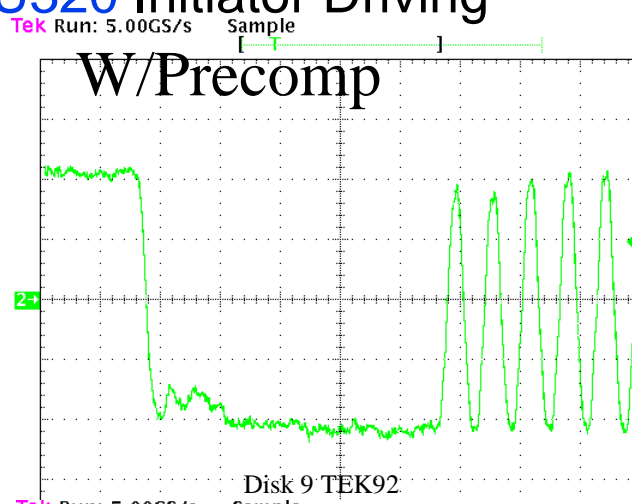
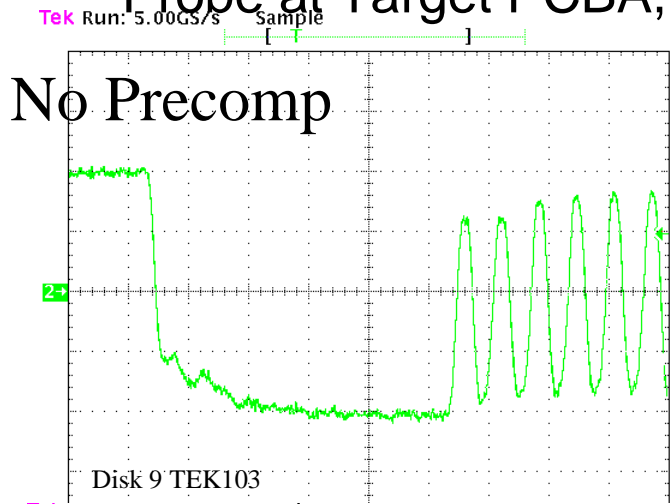
Tek Run: 5.00GS/s Sample



21 Oct 2000
11:41:14

10 - Slot Backplane - SU320 Initiator - 12m Rnd , First Slot

Probe at Target PCBA, Seagate U320 Initiator Driving

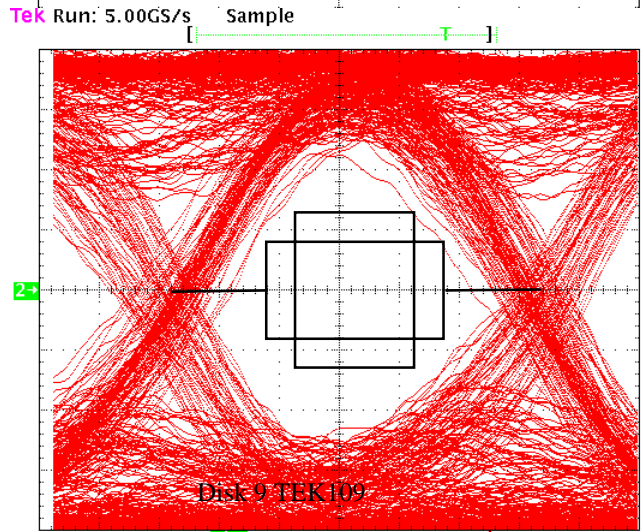
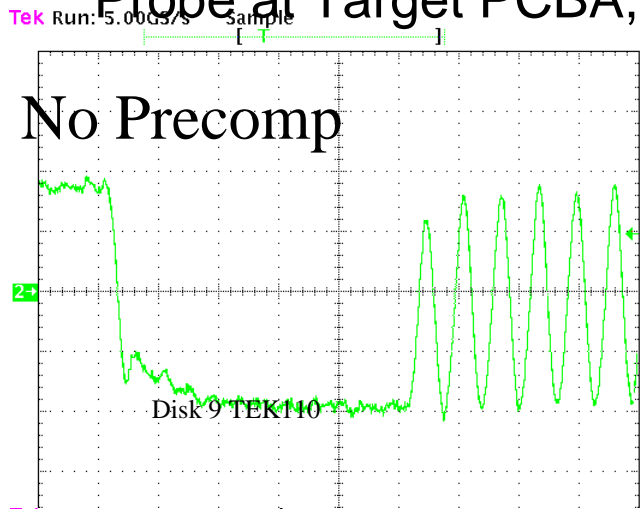


Ch2 100mVΩ M 1.00ns Ch3 192mV 23 Oct 2000 13:43:20

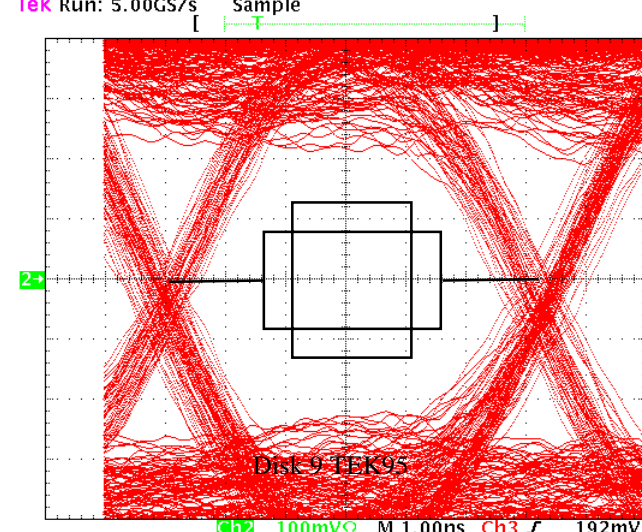
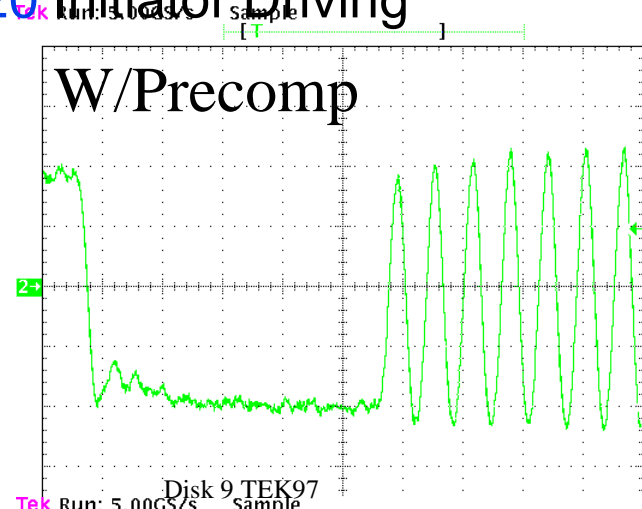
Ch2 100mVΩ M 1.00ns Ch3 192mV 23 Oct 2000 13:11:15

10 - Slot Backplane - SU320 Initiator - 12m Rnd , Middle Slot

Probe at Target PCBA, Seagate U320 Initiator Driving



Ch2 100mVΩ M 1.00ns Ch3 192mV 23 Oct 2000 13:48:37

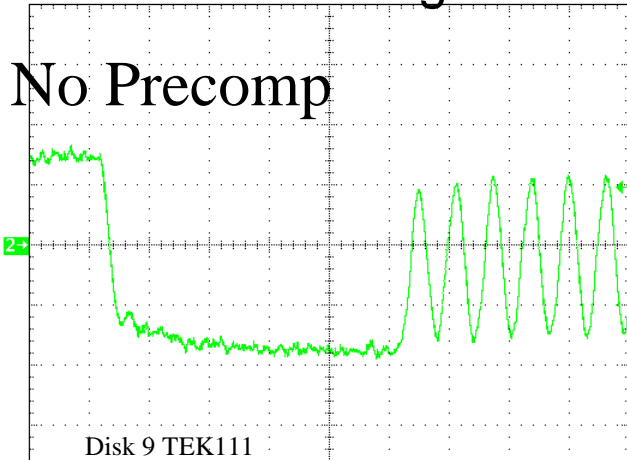


Ch2 100mVΩ M 1.00ns Ch3 192mV 23 Oct 2000 13:17:28

10 - Slot Backplane - SU320 Initiator - 12m Rnd , Last Slot

Tek Run: 5.00GS/s Sample Probe at Target PCBA, Seagate U320 Initiator Driving

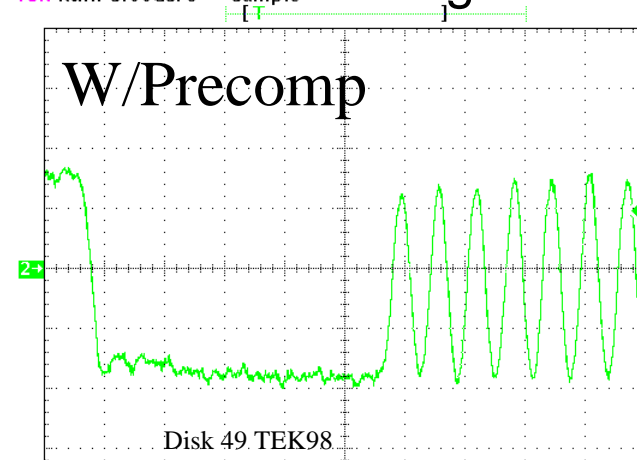
No Precomp



Disk 9 TEK111

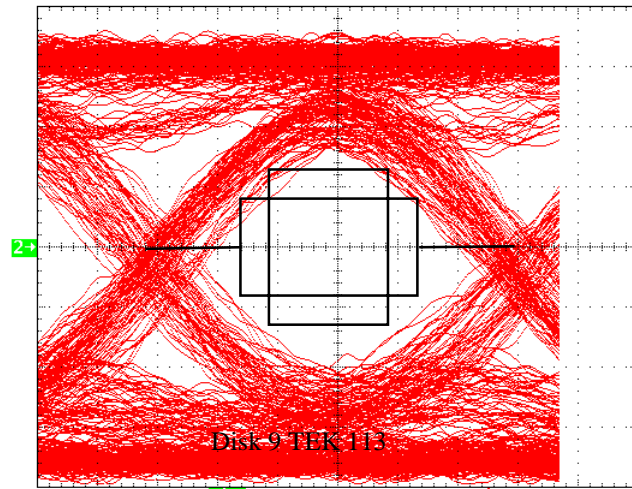
Tek Run: 5.00GS/s Sample

W/Precomp



Disk 49 TEK98

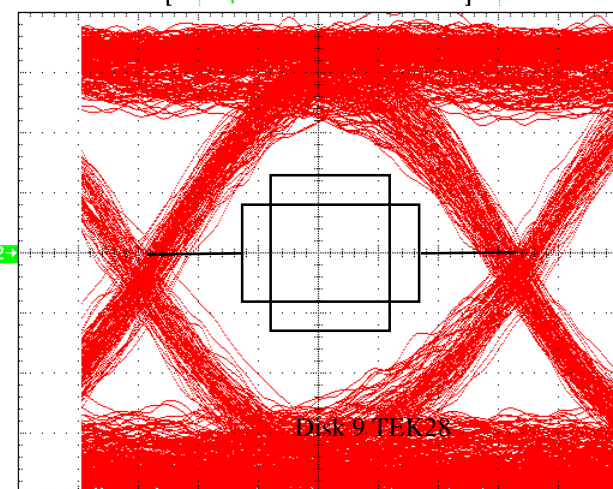
Tek Run: 5.00GS/s Sample



Disk 9 TEK 113

Ch2 100mV Ω M 1.00ns Ch3 192mV

23 Oct 2000
13:54:39



Disk 9 TEK28

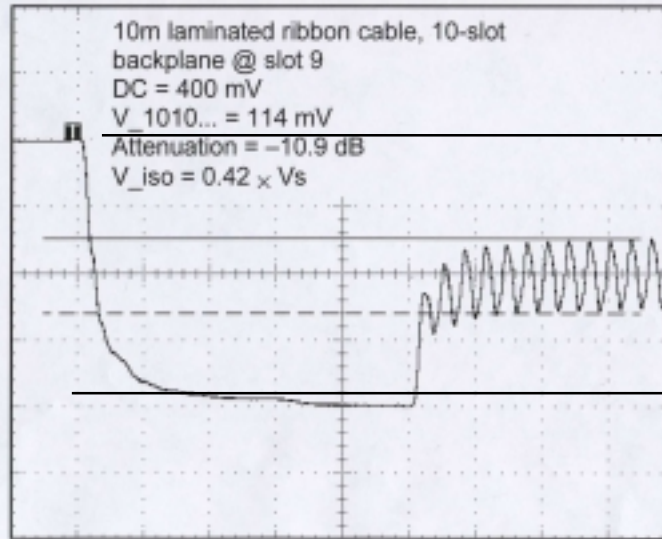
Ch2 100mV Ω M 1.00ns Ch3 192mV

23 Oct 2000
13:23:03

Cable & Backplane Attenuation Compared

Quantum

10m laminated, 10-slot, @ slot 9

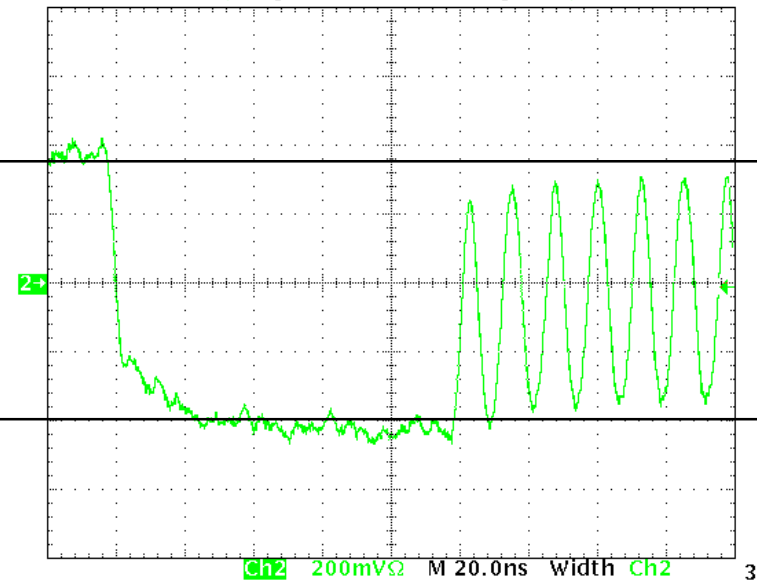


18 Mar 2000

T1808-250-S. Attenuation Data for Ultra320

Richard Liles - Slide 9

Tek Run: 5.00GS/s Sample



30 Oct 2000
12:56:15

Quantum Generator-
Driver Example, 10m

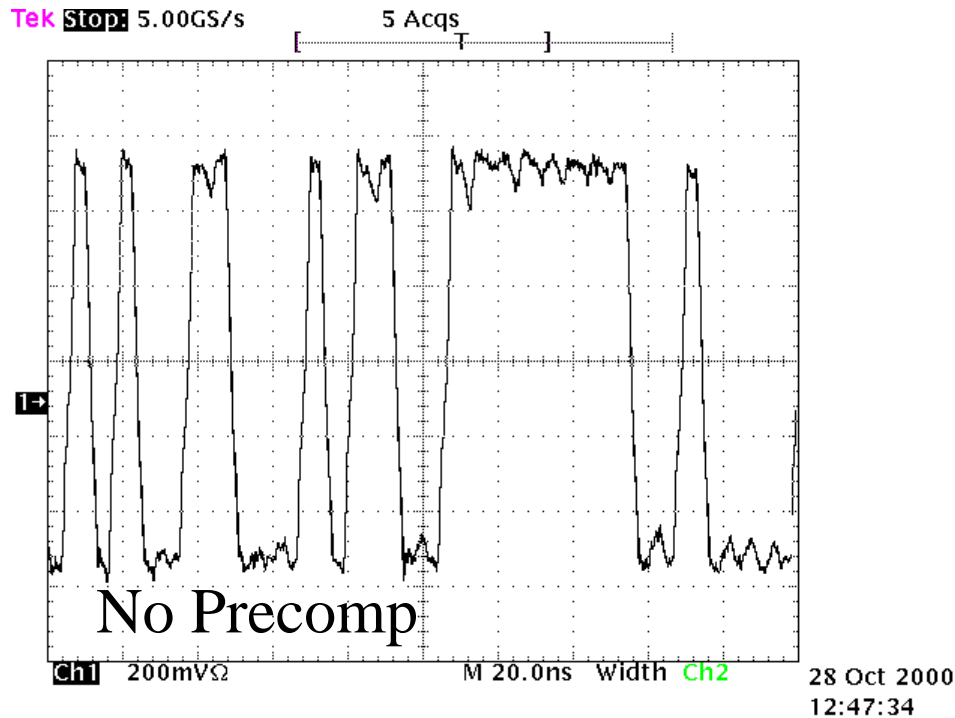
12m Cable, Same Driver
Amplitude, Slot, Backplane,
but Real U320 Silicon

SCSI Signal & Cable Analysis Point-to-Point Testing U320 Target and Initiator Silicon

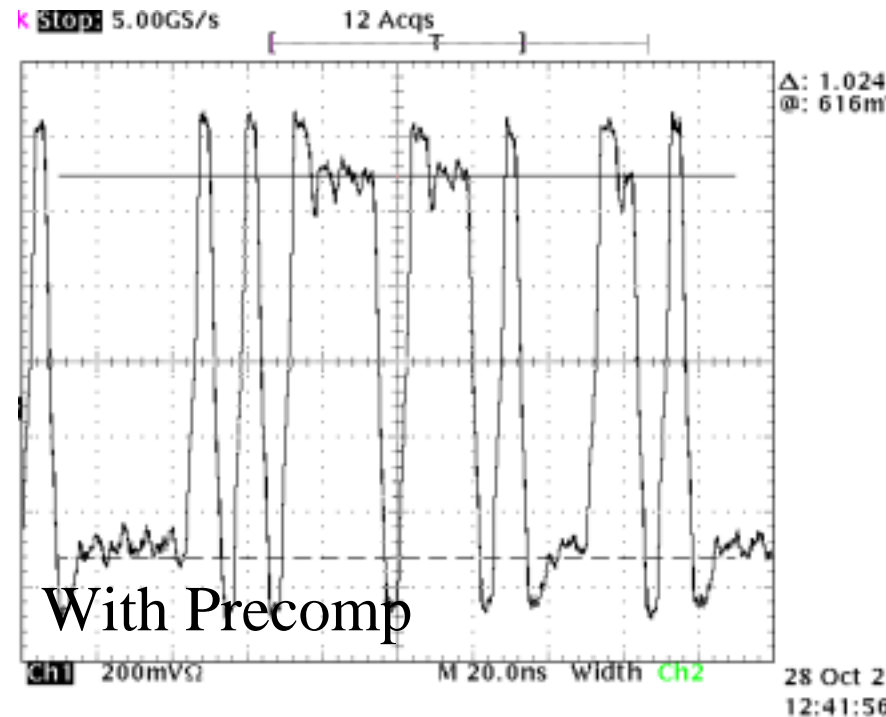
Seagate Technology
VLSI Controller Development
Scotts Valley California

SU320 Initiator - Driver Waveshape Example

Probe at Initiator, Seagate U320 Initiator Driving



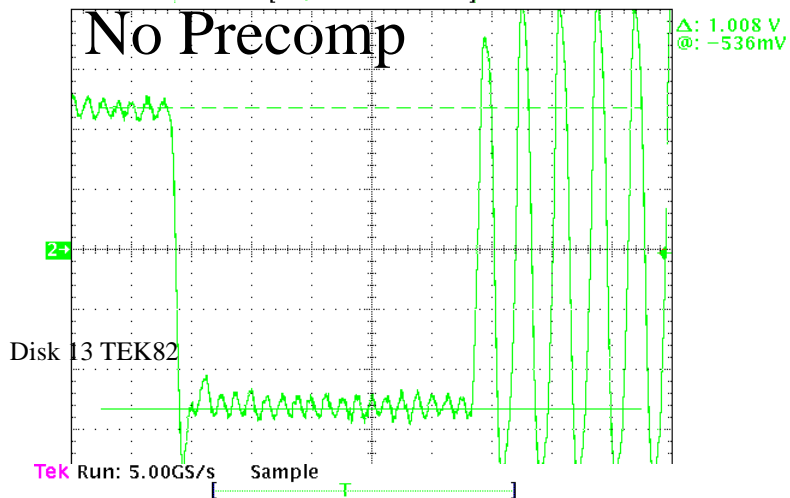
Disk W TEK77



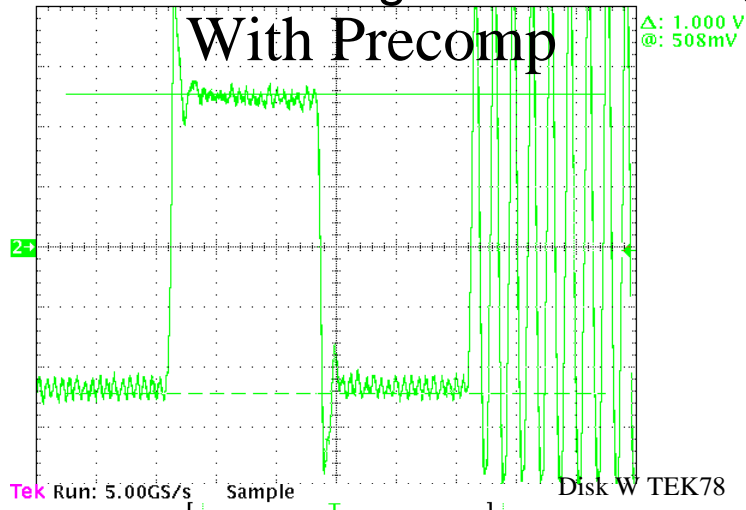
Disk W TEK70

SU320 Initiator - 18" TnF, Point-to-Point

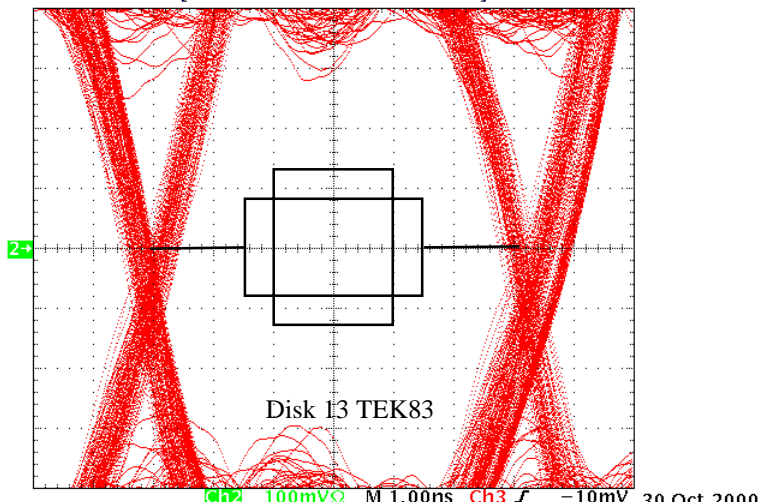
Probe at Target PCBA, Seagate U320 Initiator Driving



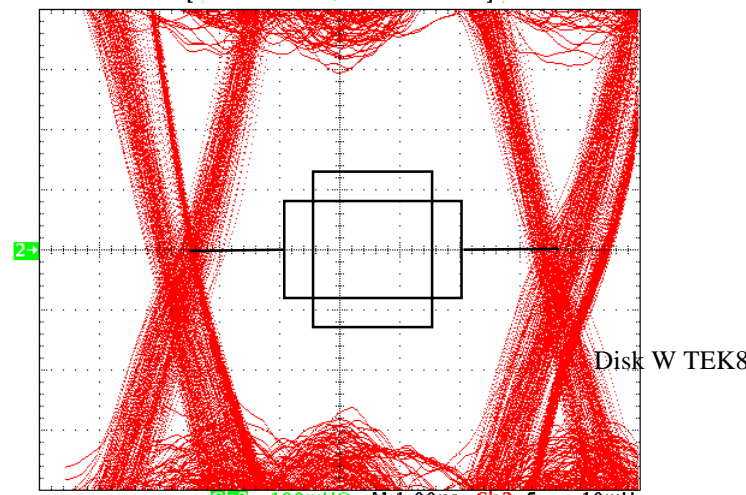
Disk 13 TEK82



Disk W TEK78



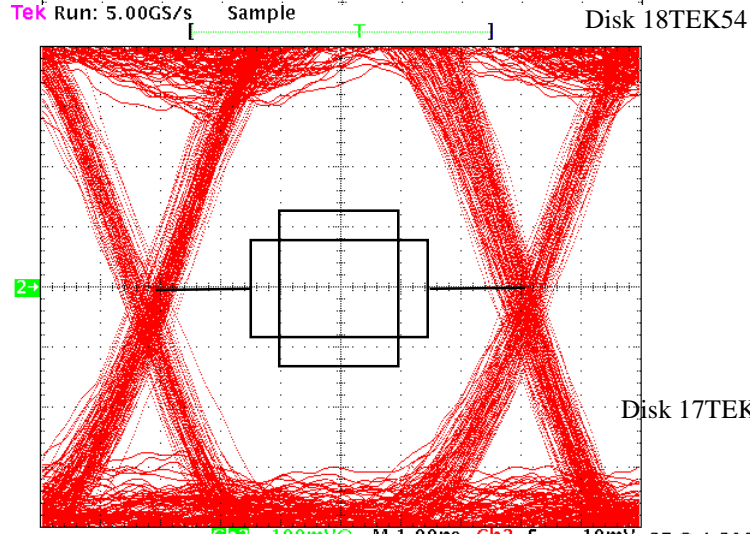
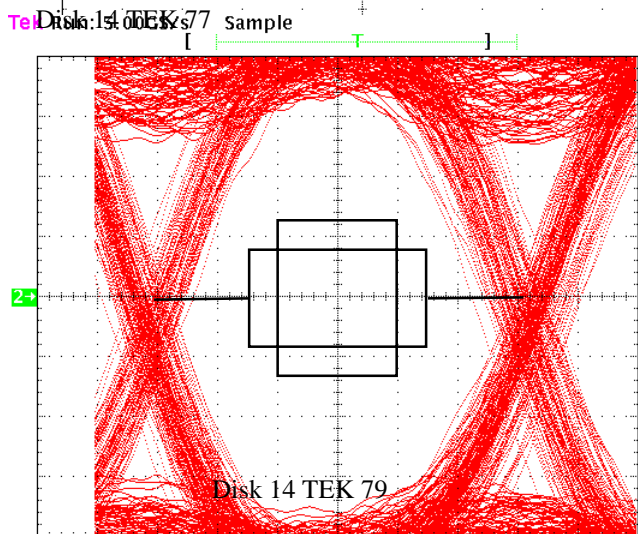
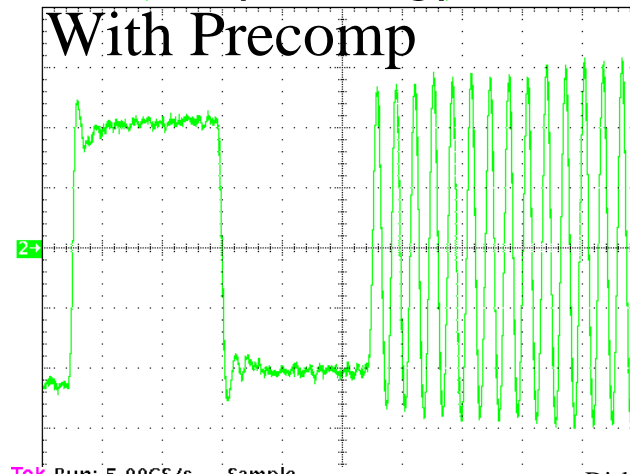
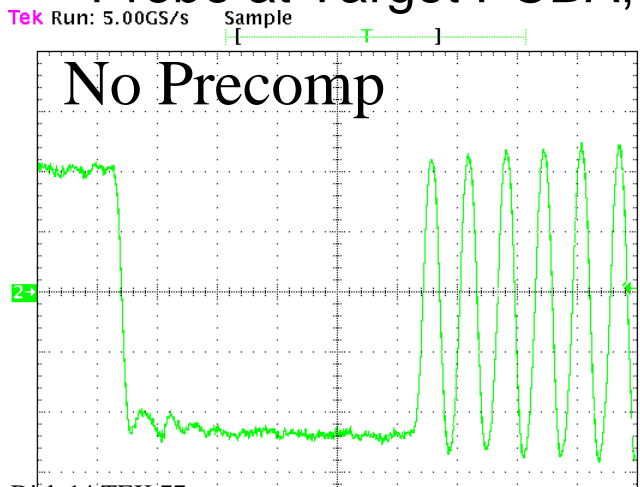
30 Oct 2000
11:56:41



28 Oct 2000
12:59:46

SU320 Initiator -12m Madison Rnd, Point-to-Point

Probe at Target PCBA, Seagate U320 Initiator Driving



Ch2 100mVΩ M 1.00ns Ch3 10mV 25 Oct 2000 15:22:08

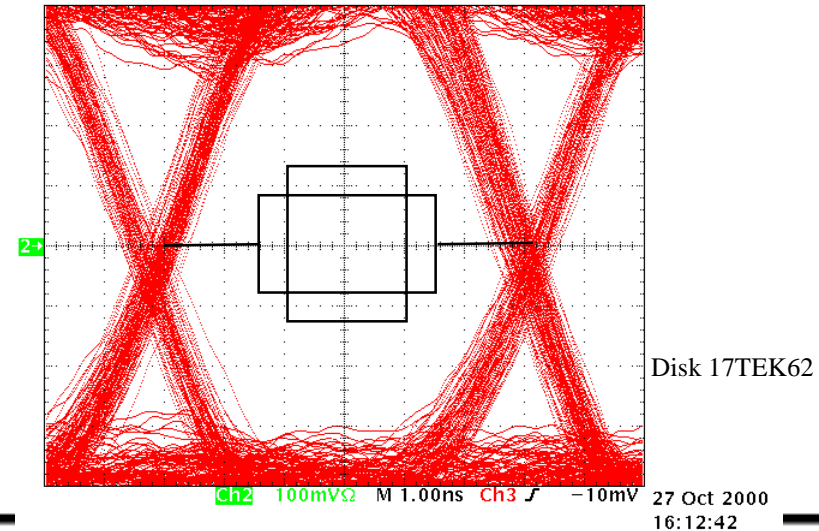
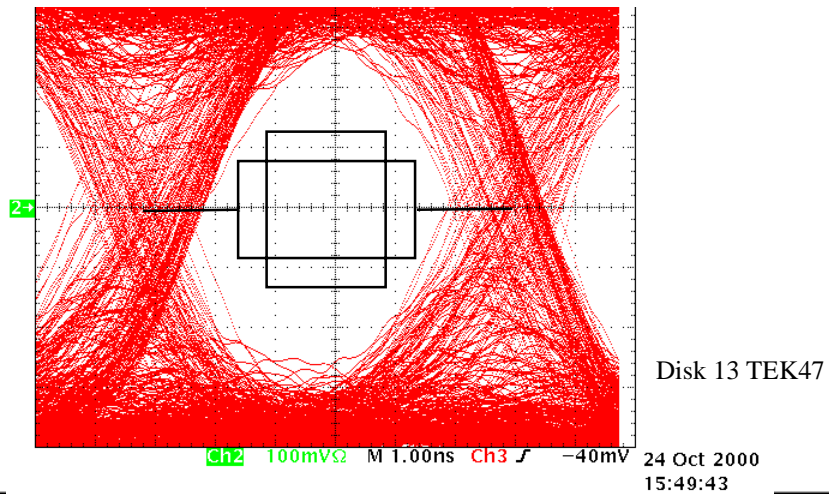
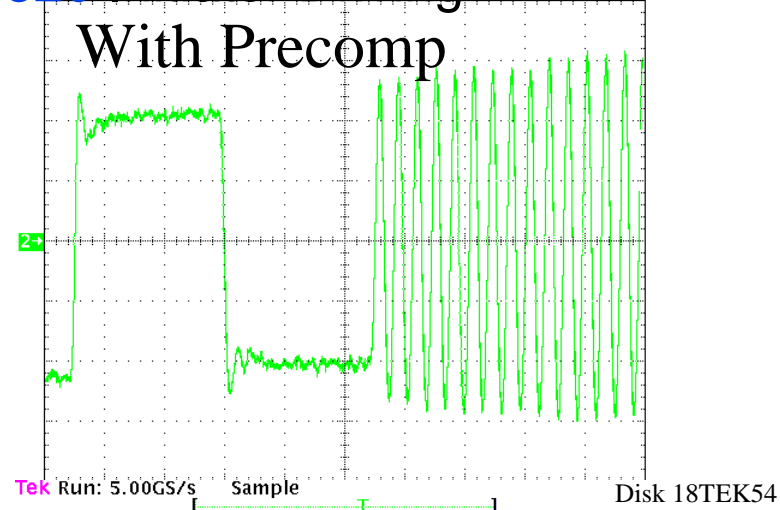
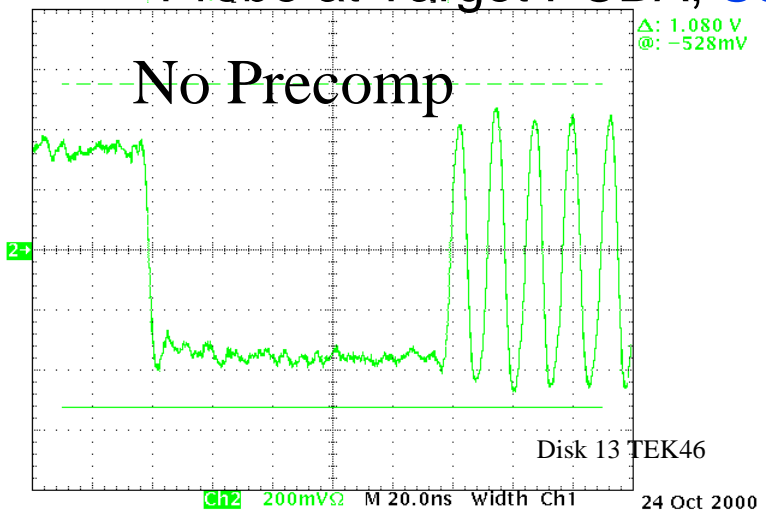
Ch2 100mVΩ M 1.00ns Ch3 -10mV 27 Oct 2000 16:12:42

SU320 Initiator -12m Amphenol TnF, Point-to-Point

Tek Run: 5.00GS/s

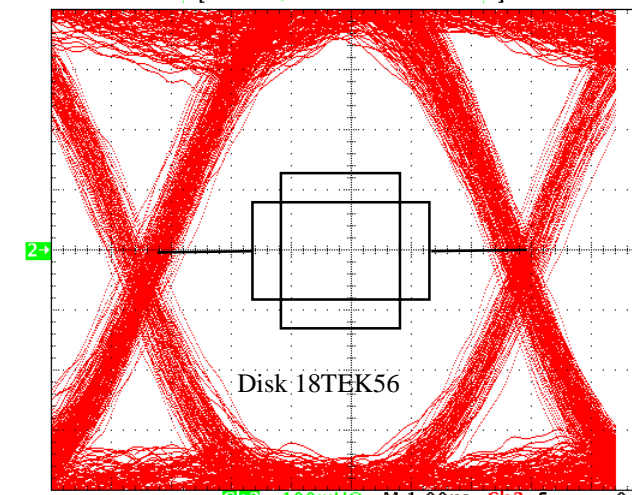
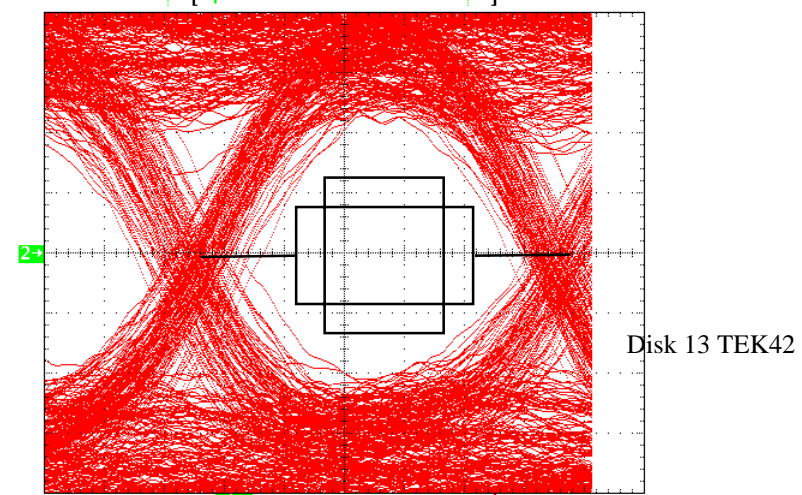
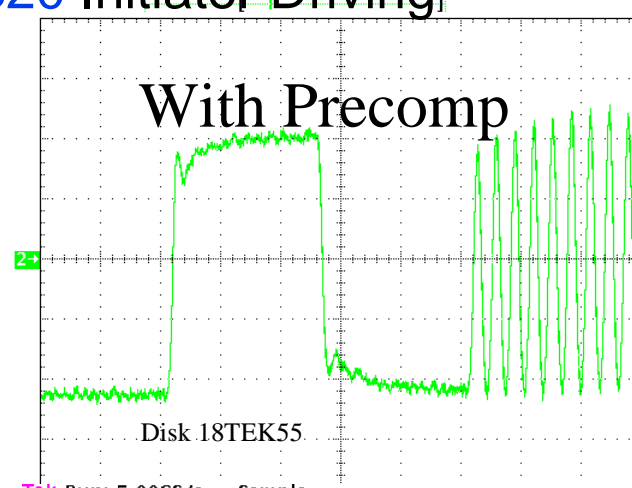
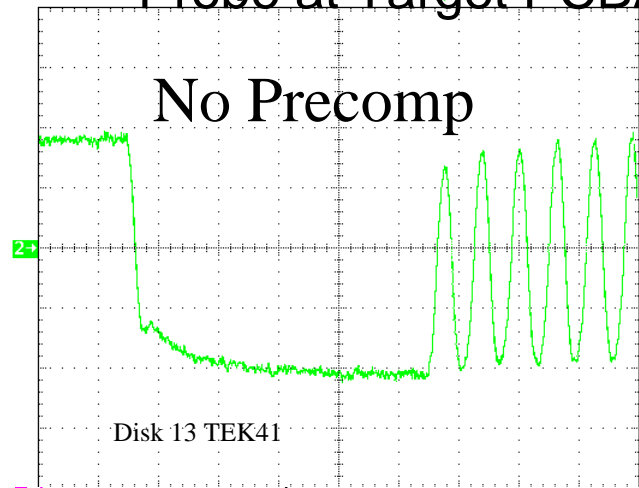
Probe at Target PCBA, Seagate U320 Initiator Driving

Tek Run: 5.00GS/s



SU320 Initiator -25m Hitachi Rnd, Point-to-Point

Tek Run: 5.00GS/s Sample
Probe at Target PCBA, Seagate U320 Initiator Driving



24 Oct 2000
15:26:11

27 Oct 2000
16:00:25