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To: T10 Technical Committee
From: Bill Galloway
Subj: QAS Signal Correction

There are conflicting requirements on initiators for the QAS protocol in sections 7.3.4.1 and 10.5.4.

7.3.4.1 Management of LVD release glitches

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Table 24 - Glitch management requirements for SCSI devices using LVD drivers

The initiator shall wait *until* two system deskew delays after it detects C/D, I/O, and MSG false before releasing the ACK and ATN signals from the actively negated state.

10.5.4 QAS phase

6) If the initiator did not create an attention condition then the initiator shall release all SCSI signals *within* two system deskew delays after detecting MSG, C/D, and I/O signals false.

I think clause 6 under 10.5.4 should be replaced with the following:

6) If the initiator did not create an attention condition then the initiator shall release all SCSI signals except ACK and ATN within two system deskew delays after detecting MSG, C/D, and I/O signals false. The ACK and ATN signals shall follow the timing specified in Table 24.