## T10/00-347r0

# SPI-4 Spec Proposal for Ultra320 

Richard Uber
Quantum Corporation

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## SPI-4 Proposals 1

- Table A.1:
- Change $\mathrm{V}_{\mathrm{A}}$ (non OR-tied signals) maximum from -TBD to -370 mV ,
- Change $\mathrm{V}_{\mathrm{N}}$ (non OR-tied signals) minimum from TBD to 370 mV ,
- Change $\mathrm{V}_{\mathrm{A}}$ (OR-tied signals) maximum from -TBD to -370 mV .
- Section A.2.2 paragraph 4: in 2 places change 320 mV to 370 mV .
- Section A.2.1, weak driver amplitude:
- Change min from $60 \%$ to $50 \%$,
- Change max from $78 \%$ to $66 \%$.
- Table A. 2
- Change first 4 TBDs to 370 mV ,
- Change max equation " $\mathrm{V}_{\mathrm{A}}=1.15$ * $\left|\mathrm{V}_{\mathrm{N}}\right|-57$ " to

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" \mathrm{~V}_{\mathrm{A}}=1.11 *\left|\mathrm{~V}_{\mathrm{N}}\right|+26 ",
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- Change min equation " $\mathrm{V}_{\mathrm{A}}=0.85{ }^{*}\left|\mathrm{~V}_{\mathrm{N}}\right|+50$ " to $" V_{A}=0.90 *\left|V_{N}\right|-23 "$.
- Add receiver mask for isolated edges with AAF circuitry enabled, per diagram. "isolated pulse".
- Eye diagram voltage must specified at the center of the pulse, not at the trailing edge.
- Specification should be based on bus characteristics, and not written around the Quantum AAF implementation.
- Goal is to enable U320 operation on all working U160 buses.
- Review of backplane data and finalizing the spec TBDs will take until the November meeting.


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## SPI-4 Proposal (for AAF) 3

- Add AAF receiver mask for isolated assertion edge.
- Add mirror image mask for isolated negation edges.


