To: T10 Technical Committee

From: Rob Elliott, Compaq Computer Corporation (Robert.Elliott@compaq.com)

Date: 1 September 2000

Subject: PPR/MESSAGE REJECT implied agreement

There is a problem in the PPR description in SPI-3 and SPI-4.

SPI-3 revision 14, Table 56, page 157 (PDF page 179).

• SPI-4 revision 0, Table 58, page 198.

Table xx: PPR messages implied agreements

Target's PPR response	Implied agreement
MESSAGE REJECT message	The initiator shall set eight-bit/asynchronous transfer with
	protocol options field set to 0h

If an Ultra 3 initiator has an WDTR/SDTR negotiation in effect with an Ultra 2 target, the target will issue a MESSAGE REJECT to an incoming PPR OUT because it doesn't understand that message. The target will maintain its current negotiated settings. The initiator should do the same, rather than drop down to eight-bit/async mode. Otherwise, the data phase won't work; the target may start streaming REQs while the initiator expects only one at a time.

We believe that most SPI-3 devices remember the previous agreement anyway.

Suggested correction:

Table xx: PPR messages implied agreements

Target's PPR response	Implied agreement
MESSAGE REJECT message	The initiator shall retain its previous data transfer mode and
	protocol options