

Date: July 13, 2000

To: T10 Committee (SCSI)

From: George Penokie (IBM)

Subject: Make PPR Pace\_On bit reserved

**Overview**

The PPR bit labeled pace\_on is redundant with the negotiated rate of f-160 and above. Therefore it should be made reserved.

The changes to SPI-4 follow:

**Table 1 - PARALLEL PROTOCOL message format**

Bit Byte	7	6	5	4	3	2	1	0
0	EXTENDED MESSAGE (01h)							
1	EXTENDED MESSAGE LENGTH (06h)							
2	PARALLEL PROTOCOL REQUEST (04h)							
3	TRANSFER PERIOD FACTOR							
4	RESERVED							
5	REQ/ACK OFFSET							
6	TRANSFER WIDTH EXPONENT (m)							
7	P_EN	RESERVED			<del>PACE_ON</del> RESERVED	QAS_REQ	DT_REQ	IU_REQ

~~A pacing transfers enabled bit (PACE\_ON) of zero indicates that paced transfers shall be disabled. A PACE\_ON bit of one indicates that paced transfers shall be used on all DT DATA phases. For negotiated transfer periods greater than 6.25 ns the PACE\_ON bit shall be set to zero. For a negotiated transfer period of 6.25 ns the PACE\_ON bit shall be set to one.~~