- ## SPI4r0, DataOut with Calibration:

  - Target asserts SEL a minimum of 2 deskew delays (90ns) after changing phase. No maximum delay is specified.

  - Initiator responds with training pattern after seeing SEL asserted

- ## SPI4r0, DataOut without Calibration:

  - Target changes phase

  - Initiator responds with free-running clock on ACK

- ## These two specifications are in conflict

- ## Proposed modification:

  - Target shall assert SEL a minimum of 2 deskew delays and a maximum of 4 deskew delays (180ns) after changing phase to DTDATAOUT, if doing calibration.

  - After detecting a phase change to DTDATAOUT, initiator shall wait a minimum of 200ns, then sample the SEL line. If SEL is asserted, initiator shall begin transmission of the training pattern. If SEL is not asserted, initiator shall begin transmitting the free-running clock on ACK.