IBIS Connector Modeling Specification

What is IBIS?

IBIS = Input / Output Buffer Information Specification

What is this IBIS stuff anyhow?

IBIS is a method of providing the Input/Output device characteristics through V/I data without disclosing any circuit/process information. It can be thought of as a behavioral modeling specification suitable for transmission line simulation of Digital Systems and applicable to most digital components.

Further Information:

IBIS Website: http://www.eia.org/eig/ibis/ibis.htm
IBIS Connector Website: http://www.eda.org/pub/ibis/connector/

Compare IBIS Connector Models to SPICE

SPICE: Parametric Representations
IBISnn: Matrix Representation: Parametrics defined by tool

What does IBIS Have to do with Connectors???
What has the Committee been up too?

**Goals:**
- Make Sure that Syntax will work
- Ensure syntax is not ambiguous
- Release the specification for vote near DAC 2000 time frame.

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IBIS: An opportunity to Optimize Connector Models

Present methods do not address problems encountered in automatically attaching connector models to SI and Timing simulators.

Model Size / Complexity / Ease of Use
Present methods do not allow "on the fly" model size scaling (SWATH mechanism)

Problem Construction and Reconstruction Time
Addressed by incorporating SLM, MLM, and CMLM in a single file (with a single pin mapping). As a side benefit, this should allow simulators to select the most appropriate model based on "level of accuracy" required. (Solution time addressed by hardware and model selection tradeoffs.)

Number of Models required.
Many different models can be required if a connector is to be used with different PCB mounting configurations (i.e. edgecard stack height).

Common Definition of Transmission Line Parameters

Further, consistency is NEEDED...
We have focused on Standard Measurements (EIA); Empirically Analytical and Empirical Measurement and Methods.... Etc.

Should we also have a similar model format / definition between connector vendors??

Compete on Designs, NOT on ability to manipulate a model

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As an Example..

Types of Models

SLM: Single Line Model
MLM: Multiline Model
CMLM: Cascaded Multiline Model

Which one is correct???

Be VERY careful with SLMs
Which one is correct???

Model extraction tools generate different models.

Some simulations would show similar results others would show significant differences!!

There is efficiency in allowing the end user to decide (or change their mind).

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IBISCnn supports many different types of connector modeling methods and all types of connectors.

For Example
- Differential and unbalanced signaling
- and/or to use both signaling methods at the same time
- and / or with the same model.
- Single Line Models (uncoupled)
- Multiline Models (coupled)
- Cascaded models (Coupled or Uncoupled)
- "Angled" connectors (Right Angle, 30 degree, 45 degree, other)
- "Cross Connected" pins (i.e. modjacks, other)
- Board to board (i.e. Pin and Socket / Backplane, Edgecard)
- Board to Cable
- Other??

There isn't a model that I have made that can't be done with the IBISCnn Specification!

A matrix section is a set of tables of numerical values that represent the electrical relationships between all conductors of a given geometry. Further, the matrices used in this specification closely follow the concepts used in the IBIS IC specification.

For example:
[Begin_Section] NameA
[Derivation Method] Lumped
[Inductance Matrix] Full_matrix
[Capacitance Matrix] Full_matrix
[Resistance Matrix] Full_matrix
[End_Section] NameA
Cascaded Matrices

Q: What are cascaded matrices?
A: Cascaded matrices are a series of matrix slices that walk down the connector from one side to the other. The matrices are defined the same as in the IBIS .pkg specification and may be full, spares, banded, or diagonal.

For example:

```
[Begin_Cn_Model] Hdi20 Hdi20Pin Hdi20Phy MLM
Cn_Section NameA Mult=1.0
Cn_Section NameB Mult=1.0
[End_Cn_Model] Hdi20
```

Stub Matrices

Q: What is a stub matrix?
A: A matrix that is connected to the cascaded matrix sections by a tee.

For example:

```
[Begin_Cn_Model] Hdi100 Hdi100Pin Hdi100Phy MLM
Cn_Section NameA Mult=1.0
Cn_Section NameB Mult=1.0
Cn_Stub     StubNameA Mult=1.0 StubNameB Mult=1.0
Cn_Section NameC Mult=1.0
[End_Cn_Model] Hdi100
```
What do we mean when we talk about a Section?

A: A section is made up of a set of matrices. Each matrix is a table of numerical values that defines one of the electrical parameters of a connector or transmission line. Specifically, the matrix represents the electrical relationships between all conductors of a given geometry.

As represented in the Connector specification:

```
[Begin_Section] NameA
  [Inductance Matrix] Full_matrix
  
  [Capacitance Matrix] Full_matrix
  
  [Resistance Matrix] Full_matrix
  
[End_Section] NameA
```

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What is a cascaded model?

Each section is made up of a SLM or MLM

Could also include a "stub" matrices.

A "swath" Operator is also an option…

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What Is A “Swath”?  

A: It is a method of using a small matrix section to define a much larger and variable size section of a connector. This facilitates faster simulations, smaller file sizes, and makes the creation of a family of connectors much easier.

For example:

<table>
<thead>
<tr>
<th>SwathRows SwathCols RightEdge LeftEdge TopEdge BottomEdge</th>
</tr>
</thead>
<tbody>
<tr>
<td>[Begin_Cn_Swath] 4 6 1 1 0 0</td>
</tr>
<tr>
<td>[Cn_Z] 50</td>
</tr>
<tr>
<td>[End_Cn_Swath]</td>
</tr>
</tbody>
</table>

This example defines a small matrix section of 4 rows and 6 columns. The right- and left-most columns are used only for the right and left edges of the connector. This might be used to define a connector with 4 rows and 6 to 1000 columns. The edges of the small swath matrix are to be connected to 50 ohms to ground when not used at the edge of the connector.

Addresses Model pin count

Anatomy of a Swath

50 Columns

6 Rows

Really High Pin Count Connector

6 x 50 = 300 pins!! = 300 x 300 matrix = 90,000 points

Assume symmetrical matrix: 90,000 / 2 = 45,000 matrix values = too many
45,000 matrix values x 3 matrices (R,L,C) = 135,000... WAY TOO MANY
135,000 x multiple cascaded sections = .......

Also, difficult / impossible for field solvers in a single simulation
Anatomy of a Swath (cont.)

Really High Pin Count Connector

Left Center Right

6 Rows 50 Columns 6 Rows

7 Columns 6 Rows 7 Columns

6 Rows 7 Columns 6 Rows

Automatic creation of banded matrices!!

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Anatomy of a Swath (cont.)

Automatic creation of banded matrices!!

1 1000

1000

Off Diagonal

Diagonal

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Anatomy of an ICM

Model 1
Model 2
Model 3

"Header Info"

"Components"

With a few lines of descriptors text, more than one model can be described

Sections
Solder Tail
Press Fit SMT
Body 1
Body 2
Body 3
PCB4mil
PCB5mil
PCB6mil

Each Section consists of up to 3 fully coupled matrices!!!

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Putting the Pieces Together

[Cn_Number_of_Conductors] 40
[Cn_Columns_of_Pins] 20
[Cn_Rows_of_Pins] 2
[SwathRows SwathCols RightEdge LeftEdge TopEdge BottomEdge]
[Begin_Cn_Swath] 2 4 1 1 0 0
[Cn_Z] 50
[Cn_Horizontal_Swath]
[End_Cn_Swath]

Cn_Section NameA Mult=1.0
Cn_Section NameB Mult=1.0
Cn_Section NameA Mult=1.5
[End_Cn_Model] Hdi200

[Begin_Section] NameA
[Derivation Method] Distributed
[Inductance Matrix] Full_matrix
[Capacitance Matrix] Full_matrix
[Resistance Matrix] Full_matrix
[End_Section] NameA

What is the Main Disadvantages to a Matrix Approach?

Results from using .ICM models could be Simulation Tool Dependent!

- Before, we had Berkeley SPICE to relate model performance results to empirical data.

Potential Solutions:

"Golden Models": SPICE and IBISCnn models created from the same matrices

User Caution and Understanding

Simulator Vendor Understanding
What is needed for Industry Acceptance of IBISCnn...

Golden Parser

Golden Models

Accuracy methodology documentation for Models
  Levels of comparison (i.e. information only or other?)
  Differential / Unbalanced simulations

Accuracy validation requirements:
  Define Model Types (Ground, no ground)
  Define which tests

Define tests that use different signaling Schemes (Differential / Unbalanced)
  Run golden models in SPICE Analysis (LC, TDR, Crosstalk, TDR, other)
  Run golden models with example non-SPICE simulator (LC, TDR, Crosstalk, TDR, etc.)
  Compare results
  Define test setup (source, sink, connections)
  Which domains? (Time, Frequency, both?)

Further publicly announced support from connector companies.

NOTE: Purposely NOT confirming to empirical measurements!

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Bottom Line...

In order to promote consistency, model format standardization is required.

The model format needs to be implemented correctly by the simulator!!!

Simulator operation will need to be verified.

Hopefully, IBISCnn will increase our level of customer support while reducing our overall workload.

Questions ???
What is Next?

• Short Term Goals:
  • Finalize the sub-committee changes
  • Vote to adopt the version 1.0 “ICM” specification.
  • Ask for funds to create the golden parser to be used for syntax checking connector models.
  • Update the examples to support the final syntax and keywords
  • Additional accuracy confirmation of .icm model
    • compare to SPICE model
    • compare to IBIS Package model

• Version 2.x Goals:
  • Add TRUE lossy modeling